

V3T5000-WRC

Preliminary version

Features

- Rugged 3U VPX with NVIDIA Quadro® RTX 5000
- 3072 CUDA cores, 48 RT cores and 384 Tensor cores, 16GB GDDR6 memory
- 9.4 TFLOPS peak FP32 performance
- Support up to 4 DisplayPort 1.4b displays
- Support CUDA Compute version 7.5, OpenCL 1.2, OpenGL 4.6, DirectX 12 and Vulkan 1.1 API
- 5-year life cycle availability

Specifications



GPU Engine Specs

GPU	NVIDIA Quadro RTX 5000
GPU Architecture	NVIDIA Turing TU104
NVIDIA CUDA Cores	3072
Floating Point Performance	9.4 TFLOPS SP Peak

Memory Specs

Memory Size	16GB GDDR6
Memory Clock	14 Gbps
Memory Interface Width	256-bit
Memory Bandwidth (GB/sec)	448

Feature Support

Bus Support	PCI Express 3.0
Open GL	4.6
DirectX	12
Open CL	1.2
Operation System	Windows 10 64-bit Linux 64-bit

VITA Standards

VITA 46.0 VPX Base Standard
VITA 46.4 PCI Express on VPX Fabric Connector
VITA 65 OpenVPX Architecture Framework for VPX

Display Support

Max. Digital Display Support	7680x4320
Max. Displays per Board	4
Display Interface	Four DisplayPort 1.4 outputs to VPX P2

Power Specs

Max. Board Power Consumption (W)	110 W
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Dimensions

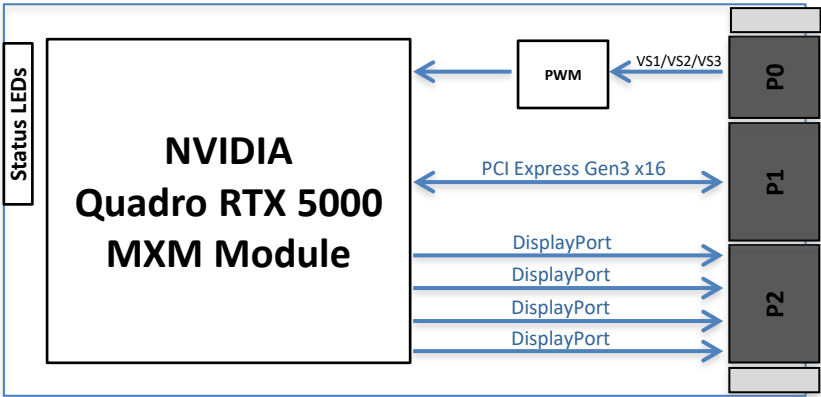
Form Factor	Conduction cooled 3U VPX
Dimensions	100 x 160 mm

Environmental

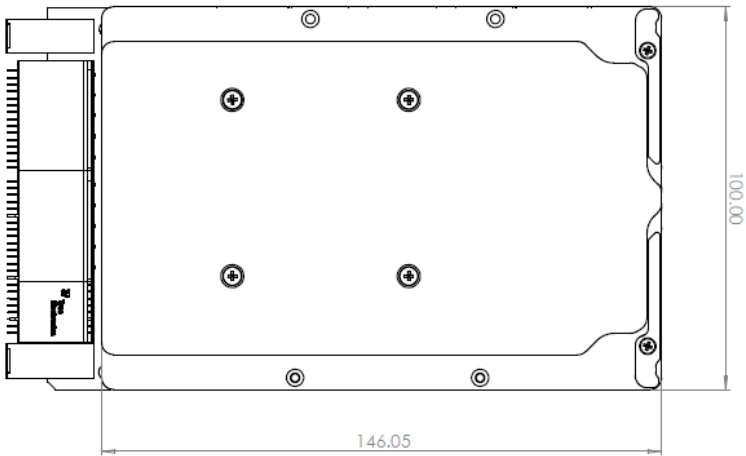
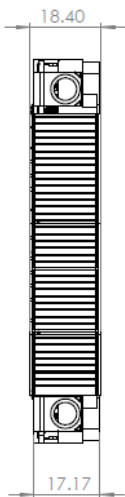
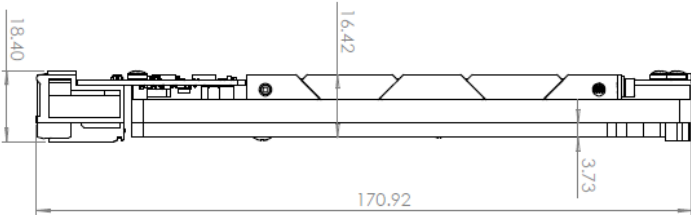
Operating Temp. (at wedge lock)	0 to +55°C / WT: tbd.
Storage Temp.	Relative Humidity 5 to 90% -40 to +125°C, Relative Humidity 5 to 95%



Block Diagram



Mechanical



Ordering Information

Module Number	Description
V3T5000-WRC	VPX3U, NVIDIA Quadro RTX 5000, 16GB GDDR6, Conduction-cooled, 0°C to +55°C (at Wedge-lock)