

規格書

SPECIFICATION

品名 SWITCHING POWER SUPPLY

STYLE NAME :

型號 MPSM-5600V


MODEL NO. :

料號

PART NO. :

版次 A1

REVISION :

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MODEL NO. MPSM-5600V

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1.0 Scope

This specification defines the performance characteristics of a grounded, Ac input, 600 watts, 5 output level power supply. This specification also defines world wide safety requirements and manufactures process test requirements.

2.0 Input requirements

2.1 Voltage (sinusoidal) : 100~240 VAC full range(With $\pm 10\%$ tolerance).

2.2 Frequency

The input frequency range will be 47hz~63hz.

2.3 Steady-state current

10A/5A at any low/high range input voltage.

2.4 Inrush current

20/40 Amps @ 115/230 VAC

2.5 Power factor correction

The power supply shall incorporate universal power input with active power factor correction, which shall reduce line harmonics in accordance with the EN61000-3-2 standards.

PFC can reach the target of 95% @230VAC, Full load.

3.0 Output requirements

3.1 DC load requirements

Normal Output voltage	Load current(A)		Regulation tolerance	
	Min.	Max.	Max.	Min.
+5V	0.5	25	+5%	-5%
+12V	2	45	+5%	-5%
-12V	0	0.8	+5%	-5%
+3.3V	0.5	25	+5%	-5%
+5Vsb	0.1	3.5	+5%	-5%

Total power :600W(MAX)

+5V AND +3.3V Total Max.:40A

When doing the cross regulation test (one output channel at high load and the other output channels at low load), it is requested to set the higher output channel at 80% max. of its spec., and the lower output channels at 20% max. of theirs.

3.2 Regulation

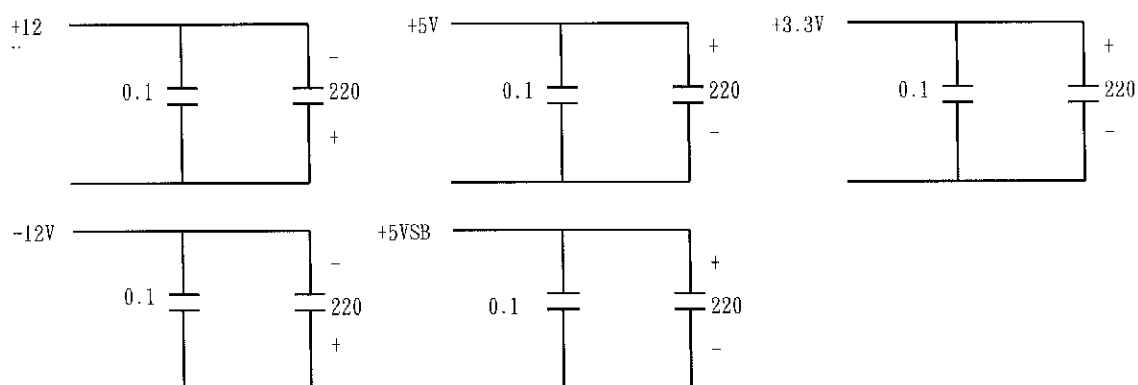
Output DC voltage	Line regulation
+5V	$\pm 50\text{mV}$
+12V	$\pm 50\text{mV}$
-12V	$\pm 50\text{mV}$
+3.3V	$\pm 50\text{mV}$
+5Vsb	$\pm 50\text{mV}$

3.3 Ripple and noise

3.3.1 Specification

+5V	50mV (P-P)
+12V	120mV (P-P)
-12V	120mV (P-P)
+3.3V	50mV (P-P)
+5Vsb	50mV (P-P)

3.3.2 Ripple voltage test circuit



0.1uf is ceramic the other is tantalum.
Noise bandwidth is from DC to 20MHz

3.4 Overshoot

Any overshoot at turn on or turn off shall be less 10% of the nominal voltage value , all output shall be within the regulation limit of section 3.2 before issuing the power good signal of section 6.0.

3.5 Efficiency

Power supply efficiency typical 80-85% at 25 °C 115V FULL LOAD .

3.6 Typical Distribution of Efficiency

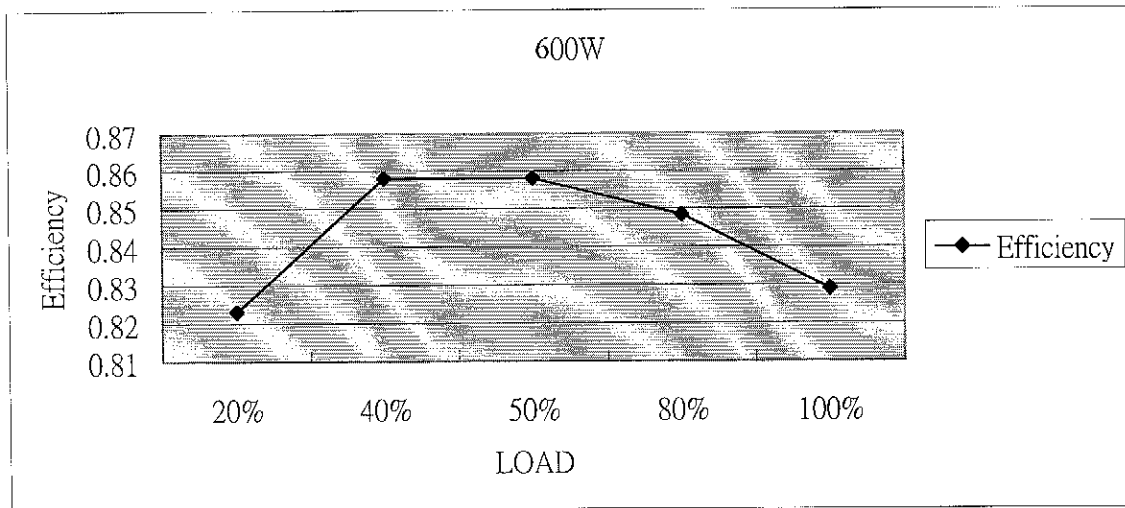
LOAD(20%)					
Output	+5V	+3.3V	+12V	-12V	5VSB
Load Current	3.87A	3.87A	6.97A	0.12A	0.54A
Voltage(Rms)	5.07V	3.32V	12.19V	11.91V	5.09V
P in(AC in 115V)	148W				
EFF.	82.3%				

LOAD(40%)					
Output	+5V	+3.3V	+12V	-12V	5VSB
Load Current	7.75A	7.75A	13.94A	0.25A	1.08A
Voltage(Rms)	5.06V	3.3V	12.17V	11.86V	5.06V
P in(AC in 115V)	283W				
EFF.	85.8%				

LOAD(60%)					
Output	+5V	+3.3V	+12V	-12V	5VSB
Load Current	11.62A	11.62A	20.91A	0.37A	1.63A
Voltage(Rms)	5.04V	3.28V	12.14V	11.95V	5.02V
P in(AC in 115V)	423W				
EFF.	85.8%				

LOAD(80%)					
Output	+5V	+3.3V	+12V	-12V	5VSB
Load Current	15.49A	15.49A	27.89A	0.5A	2.17A
Voltage(Rms)	5.02V	3.26V	12.15V	11.93V	4.99V
P in(AC in 115V)	570W				
EFF.	84.9%				

LOAD(100%)					
Output	+5V	+3.3V	+12V	-12V	5VSB
Load Current	20A	20A	34A	0.8A	3.5A
Voltage(Rms)	5.01V	3.24V	12.12V	12.04V	4.93V
P in(AC in 115V)	728W				
EFF.	82.9%				



P.S Any difference either on the DC output cable (i.e., length, wire gauge) or on the accurate of instruments will conclude different test result.

4.0 Protection

4.1 Input (primary)

The input power line must have an over power protection device in accordance with safety requirement of section 8.0

4.2 Output (secondary)

4.2.1 Over power protection

The power supply shall provide over power protection on the power supply latches all DC output into a shutdown state. Over power of this type shall cause no damage to power supply, after over load is removed and a power on/off cycle is initiated, the power supply will restart. Trip point total power min. 120%, max. 160%.

4.2.2 Over voltage protection

If an over voltage fault occurs, the power supply will latch all DC output into a shutdown state.

	Min	Typical	Max
+3.3V	3.6V	4.1V	4.3V
+5V	5.6V	6.1V	6.5V
+12V	13.2V	14.3V	15.0V

4.2.3 Short circuit

A short circuit placed on +5V, +3.3V, +12V, -12V output to DC return shall cause no damage and power supply latch.

4.2.4 Restart after protection

After the protection mode (OPP/OVP) or short circuit is removed and a power on/off cycle is initiated, the power supply will restart. If the PSU is using AC power switch to control on/off, it has to be switched off at least for 5 seconds to restart the unit.

5.0 Power supply sequencing

5.1 Power on (see fig.1)

5.2 Hold up time

When power shutdown DC output 5V must be maintain 16msec in regulation limit at. normal input voltage

5.3 Power off sequence (see fig. 1)

6.0 Signal requirements

6.1 Power good signal (see fig. 1)

The power supply shall provide a "power good" signal to reset system logic, indicate proper operation of the power supply.

At power on, the power good signal shall have a turn on delay of at least 100ms but not greater than 500ms after the output voltages have reached their respective minimum sense levels.

7.0 Environment

7.1 Operation

Temperature	0 to 40 degrees centigrade
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7.2 Insulation resistance

Primary to secondary	: 30 meg. Ohm min. 500 VDC
Primary to FG	: 30 meg. Ohm min. 500VDC

7.3 Dielectric withstanding voltage

For approval purpose :

Primary to secondary	: 3000 VAC for 1min.
Primary to Frame Gnd	: 2200 VAC for 1 min.

For production purpose: 100% test

Primary to Frame Gnd	: 2200VAC for 1 sec
Cut off current	: 15mA

7.4 Leakage current

0.3 mA max. at nominal voltage VAC.

8.0 Safety

8.1 Underwriters laboratory (UL).

The power supply designed to meet UL 60601.

8.2 TUV

The power supply shall be designed to meet TUV EN-60601.

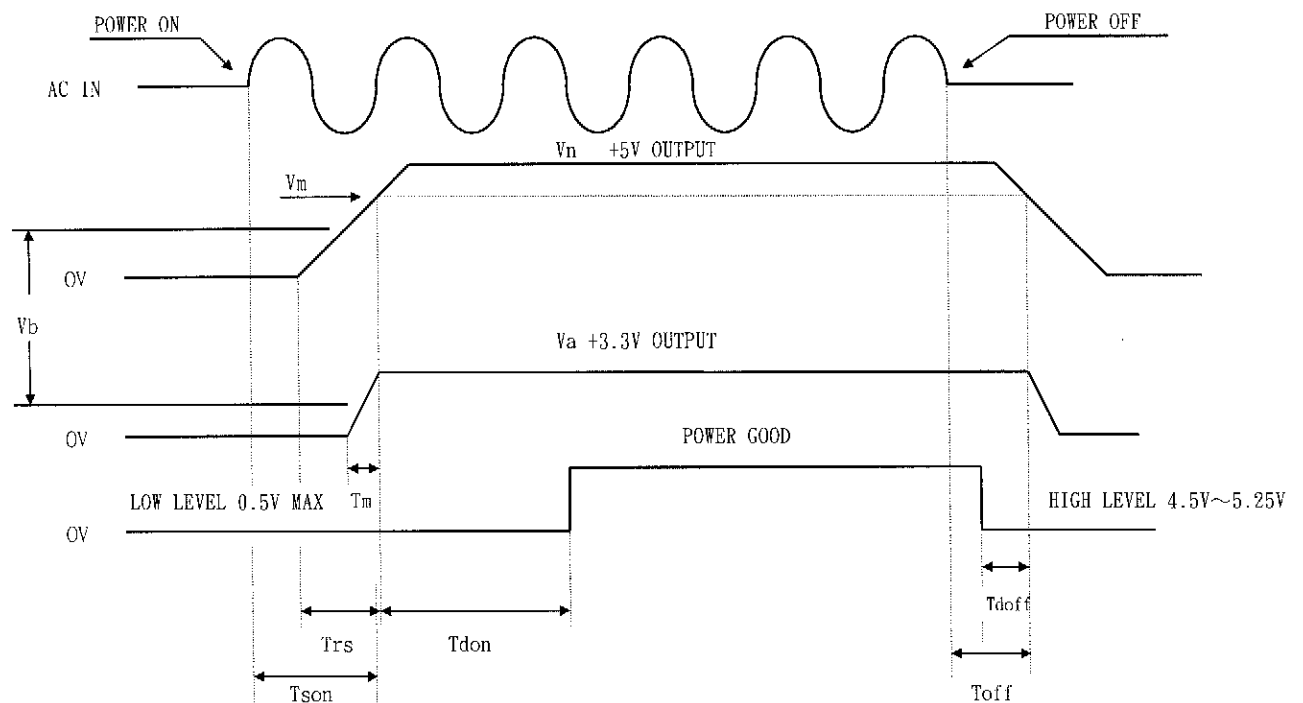
9.0 Reliability

9.1 Burn in

All products shipped to customer must be burn in. The burn in shall be performed at high line voltage.

10.0 Mechanical requirements

10.1 Physical dimension : 160 mm(D) * 150 mm(W) * 86 mm(H)



Vn	Nominal voltages +5V
Vm	Minimum voltages +4.5V
Va	Nominal voltages +3.3V
Vb	+2.0V max
Tson	Switch on time (2000 ms. max.)
Trs	+5V rise time (50ms. max.)
Tdon	Delay turn-on (100ms. < Tdon < 500ms.)
Tdoff	Delay turn-off (1 ms. min.)
Toff	Hold up time (16ms Min)

《Figure 1》