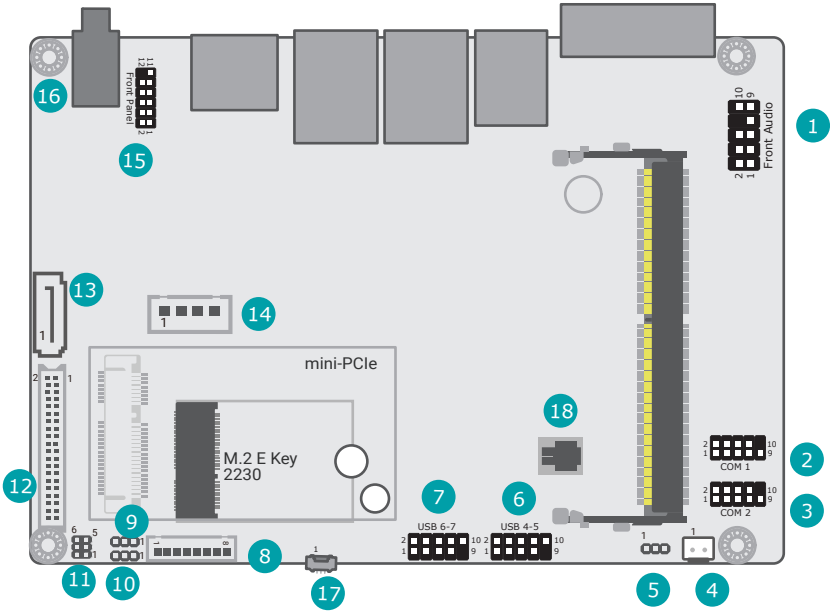


Board Layout



- 1 Front Audio
- 2 COM 1
- 3 COM 2 or 8bit GPIO
- 4 Battery
- 5 UART
- 6 USB2_4/5
- 7 USB2_6/7
- 8 Backlight Control
- 9 Panel Backlight
- 10 Panel Inverter Power
- 11 Panel Power
- 12 LVDS
- 13 SATA1
- 14 SATA Power
- 15 Front Panel
- 16 9~36VDC-In 2x2 Pin DC Input (Opt.)
- 17 System Fan
- 18 eSPI
- 19 eDP



Jumper Settings

| | |
|----------------------------|--------------|
| 9 Backlight Voltage | DPJP1 |
| 3V3 (default) | 1-2 On |
| 5V | 2-3 On |

| | |
|--------------------------------|--------------|
| 10 Panel Inverter Power | DPJP2 |
| 12V (default) | 1-2 On |
| 5V | 2-3 On |

| | |
|-----------------------|--------------|
| 11 Panel Power | DPJP3 |
| 12V | 1-2 On |
| 5V | 3-4 On |
| 3.3V (default) | 5-6 On |

Pin Assignments

| 1 Front Audio | | | |
|----------------------|------------|-----|------------|
| Pin | Function | Pin | Function |
| 1 | MIC2-L | 2 | AGND_AUDIO |
| 3 | MIC2-R | 4 | N.C. |
| 5 | LINE2-R | 6 | MIC2-JD |
| 7 | AGND_AUDIO | 8 | -- |
| 9 | LINE2-L | 10 | LINE2-JD |

| 2 COM 1 (RS232/RS422/RS485) | | | |
|------------------------------------|----------|-----|----------|
| Pin | Function | Pin | Function |
| 1 | MDCD1# | 2 | MSIN1 |
| 3 | MSOUT1 | 4 | MDTR1# |
| 5 | GND | 6 | MDSR1# |
| 7 | MRTS1# | 8 | MCTS1# |
| 9 | MRI1# | 10 | N.C. |

| 3 COM 2 (RS232) | | | |
|------------------------|----------|-----|----------|
| Pin | Function | Pin | Function |
| 1 | MDCD2- | 2 | MSIN2- |
| 3 | MSO2- | 4 | MDTR2- |
| 5 | GND | 6 | MDSR2- |
| 7 | MRTS2- | 8 | MCTS2- |
| 9 | MRI2- | 10 | N.C. |

| 3 COM 2 (8bit GPIO Pin Out) | | | |
|------------------------------------|----------|-----|----------|
| Pin | Function | Pin | Function |
| 1 | DIO6 | 2 | DIO4 |
| 3 | DIO5 | 4 | DIO3 |
| 5 | GND | 6 | DIO1 |
| 7 | DIO2 | 8 | DIO0 |
| 9 | DIO7 | 10 | ---- |

*Change COM2 to 8bit GPIO is available in different part number.

| 4 Battery | | | |
|------------------|----------|-----|----------|
| Pin | Function | Pin | Function |
| 1 | VCC | 2 | VCC |
| 3 | -Data | 4 | -Data |
| 5 | +Data | 6 | +Data |
| 7 | GND | 8 | GND |
| 9 | --- | 10 | N.C. |

| 5 UART | |
|---------------|----------|
| Pin | Function |
| 1 | UART_TX |
| 2 | GND |
| 3 | UART_RX |

6 USB2_4/5

| Pin | Function | Pin | Function |
|-----|------------|-----|------------|
| 1 | 5V_USB45 | 2 | 5V_USB45 |
| 3 | USB2_4_C_N | 4 | USB2_5_C_N |
| 5 | USB2_4_C_P | 6 | USB2_5_C_P |
| 7 | GND | 8 | GND |
| 9 | N.C | 10 | N.C |

7 USB2_6/7

| Pin | Function | Pin | Function |
|-----|------------|-----|------------|
| 1 | 5V_USB67 | 2 | 5V_USB45 |
| 3 | USB2_6_C_N | 4 | USB2_7_C_N |
| 5 | USB2_6_C_P | 6 | USB2_7_C_P |
| 7 | GND | 8 | GND |
| 9 | N.C | 10 | N.C |

8 Backlight Control

| Pin | Function |
|-----|-----------|
| 1 | GND |
| 2 | GND |
| 3 | DIMMING |
| 4 | VCC_PANEL |
| 5 | LVDS_3V3 |
| 6 | BLONOFF |
| 7 | INV_PWR |
| 8 | INV_PWR |

12 DPCN4

| Pin | Function | Pin | Function |
|-----|----------------|-----|----------------|
| 1 | GND | 2 | GND |
| 3 | LVDSA_LANE3_P | 4 | LVDSB_LANE3_P |
| 5 | LVDSA_LANE3_N | 6 | LVDSB_LANE3_N |
| 7 | GND | 8 | GND |
| 9 | LVDSA_LANE2_P | 10 | LVDSB_LANE2_P |
| 11 | LVDSA_LANE2_N | 12 | LVDSB_LANE2_N |
| 13 | GND | 14 | GND |
| 15 | LVDSA_LANE1_P | 16 | LVDSB_LANE1_P |
| 17 | LVDSA_LANE1_N | 18 | LVDSB_LANE1_N |
| 19 | GND | 20 | GND |
| 21 | LVDSA_LANE0_P | 22 | LVDSB_LANE0_P |
| 23 | LVDSA_LANE0_N | 24 | LVDSB_LANE0_N |
| 25 | GND | 26 | GND |
| 27 | LVDSA_CLK_P | 27 | LVDSB_CLK_P |
| 29 | LVDSA_CLK_N | 30 | LVDSB_CLK_N |
| 31 | GND | 32 | GND |
| 33 | LVDS_DDC_CLK | 34 | N.C |
| 35 | LVDS_DDC_DATA | 36 | LVDS_3V3 |
| 37 | GND | 38 | GND |
| 39 | LVDS_PANEL_PWR | 40 | LVDS_PANEL_PWR |



DFI reserves the right to change the specification at anytime prior to the product's release. For more information please contact your DFI sales representatives.

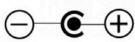
15 Front Panel

| Pin | Function | Pin | Function |
|-----|--------------|--------|--------------|
| 1 | N.C | 2 | SUSLED_PWR |
| 3 | SATA LED | 4 | POWER LED |
| 5 | | HD_PWR | |
| | HD_LED# | 6 | SUS_LED# |
| 7 | RESET SWITCH | 8 | POWER BUTTON |
| 9 | | GND | |
| | SYSRST# | | PWSIN# |
| 11 | N.C | 12 | N.C |

16 9~36VDC-In or 2x2 pin DC input (Opt.)

| Pin | Function |
|-----|----------|
| 1 | GND-1 |
| 2 | GND-2 |
| 3 | 12V-1 |
| 4 | 12V-2 |

9~36VDC-In :



2x2 Pin DC Input (Opt.) :



17 System FAN

| Pin | Function |
|-----|-----------|
| 1 | SYSFANIN |
| 2 | SYSFANOUT |
| 3 | GND |

19 eDP

| Pin | Function |
|-----|----------------|
| 1 | -- |
| 2 | INV_P WR |
| 3 | INV_P WR |
| 4 | INV_P WR |
| 5 | INV_P WR |
| 6 | -- |
| 7 | -- |
| 8 | DIMMING_1 |
| 9 | BLONOFF_1 |
| 10 | GND |
| 11 | GND |
| 12 | GND |
| 13 | GND |
| 14 | eDP_HPD_CN |
| 15 | GND |
| 16 | GND |
| 17 | LCD_BIST |
| 18 | LVDS_PANEL_PWR |
| 19 | LVDS_PANEL_PWR |
| 20 | GND |
| 21 | eDPB_AUXN |
| 22 | eDPB_AUXP |
| 23 | GND |
| 24 | eDPB_LANE0_C_P |
| 25 | eDPB_LANE0_C_N |
| 26 | GND |
| 27 | eDPB_LANE1_C_P |
| 28 | eDPB_LANE1_C_N |
| 29 | GND |
| 30 | LCD_CABC |