SI-111-N

Intel® Atom® x6000
Fanless Signage Player

User's Manual

Version 1.0a
(July 2025)

iBASE

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Compliance

CE

This product has passed CE tests for environmental specifications and limits. This product is in accordance with the directives of the European Union (EU). If users modify and/or install other devices in this equipment, the CE conformity declaration may no longer apply.

FC

This product has been tested and found to comply with the limits for a Class B device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with manufacturer's instructions, may cause harmful interference to radio communications.

WEEE

This product must not be disposed of as normal household waste, in accordance with the EU directive for waste electrical and electronic equipment (WEEE - 2012/19/EU). Instead, it should be disposed of by returning it to a municipal recycling collection point. Check local regulations for disposal of electronic products.

Green IBASE

This product is compliant with the current RoHS 2 restrictions and prohibits use of the following substances in concentrations exceeding 0.1% by weight (1000 ppm) except for cadmium, limited to 0.01% by weight (100 ppm).

- Hexavalent chromium: 1,000 ppm
- Poly-brominated biphenyls (PBBs): 1,000 ppm
- Poly-brominated diphenyl ethers (PBDEs): 1,000 ppm
- Cadmium: 100 ppm
- Mercury: 1,000 ppm
- Lead: 1,000 ppm
- Bis(2-ethylhexyl) phthalate (DEHP): 1,000 ppm
- Butyl benzyl phthalate (BBP): 1,000 ppm
- Dibutyl phthalate (DBP): 1,000 ppm
- Diisobutyl phthalate (DIBP): 1,000 ppm

Important Safety Information

Carefully read the precautions before using the device.

Environmental conditions:

- Lay the device horizontally on a stable and solid surface in case the device may fall, causing serious damage.
- Leave plenty of space around the device and do not block the openings for ventilation. Never drop or insert any objects into the ventilation openings.
- Slots and openings on the chassis are for ventilation. Do not block or cover these openings. Make sure you leave plenty of space around the device for ventilation. Never insert objects of any kind into the ventilation openings.

Care for your IBASE products:

- Before cleaning the device, turn it off and unplug all cables such as power as residual current may still be present.
- Use neutral cleaning agents or diluted alcohol to clean the device chassis with a cloth. Then wipe the chassis with a dry cloth.
- Vacuum the dust with a computer vacuum cleaner to prevent the air vent or slots from being clogged.

WARNING

Attention during use:

- Do not use this product near water.
- Do not spill water or any other liquids on your device.
- Do not place heavy objects on the top of the device.
- Operate this device from the type of power indicated on the marking label. If you are not sure of the type of power available, consult your distributor or local power company.
- Do not walk on the power cord or allow anything to rest on it.
- If you use an extension cord, make sure that the total ampere rating of the product plugged into the extension cord does not exceed its limits.

Avoid Disassembly

You are not suggested to disassemble, repair or make any modification to the device. Disassembly, modification, or attempted repairs may pose safety hazards and cause damage to the device, or even result in injury or property damage, and will void any warranty.

CAUTION

Danger of explosion if internal lithium-ion battery is replaced by an incorrect type. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Warranty Policy

- **IBASE standard products:**

  24-month (2-year) warranty from the date of shipment. If the date of shipment cannot be ascertained, the product serial numbers can be used to determine the approximate shipping date.

- **3rd-party parts:**

  12-month (1-year) warranty from delivery for the 3rd-party parts that are not manufactured by IBASE, such as CPU, memory, SSD/HDD, power adapter, panel and touchscreen.

✳ *Products that fail due to misuse, accident, improper installation or unauthorized repair shall be treated as out of warranty and customers shall be billed for repair and shipping charges.*

Technical Support & Services

1. Visit the IBASE website at www.ibase.com.tw to find the latest information about the product.
2. If you need any further assistance from your distributor or sales representative, prepare the following information of your product and elaborate upon the problem.
   - Product model name
   - Product serial number
   - Detailed description of the problem
   - The error messages in text or in screenshots if there is any
   - The arrangement of the peripherals
   - Software in use (such as OS and application software, including the version numbers)
3. If repair service is required, you can download the RMA form at the website of IBASE. Fill out the form and contact your distributor or sales representative.

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Table of Contents

Chapter 1
General Information

The information provided in this chapter includes:

- Features
- Packing List
- Accessories
- Specifications
- Product View
- Dimensions

1.1 Introduction

The SI-111-N is a palm-sized fanless 4K digital signage player system based on the Intel® Atom® x6211E and Celeron® N6210 processors. Equipped with an HDMI 2.0b port that supports 3840 x 2160 @60Hz resolution, the industrial-grade system enables businesses to easily connect to a 4K display and create immersive visual experiences that attract attention and effectively promote their products or services.

IBASE values sustainable development and ESG practices. The SI-111-N incorporates various energy-saving features, including IBASE's proprietary iSMART green technology that enables power on/off scheduling with power resume capabilities, and the Observer utility that remotely monitors system voltages and temperature to ensure the system is operating efficiently while minimizing energy consumption. The SI-111-N's extensive I/O connectivity offers a rich array of expansion options, reliable data handling, and wireless operation, which includes 1x 2.5GbE LAN, 1x COM (RS-232) port, 1x M.2 M-Key (2280) and 1x M.2 E-Key (2230) sockets.

1.2 Features

- Intel® Atom® X6000/ Celeron® Processors
- iSMART intelligent energy-saving & Observer remote monitoring technologies
- 2x DDR4-3200 SO-DIMM, Dual channel
- 1x HDMI 2.0b
- 3x USB 3.1
- 1x 2.5GbE LAN port
- 1x COM (RS-232)
- 1x M.2 M-Key (2280) for storage
- 1x M.2 E-Key (2230) for Wi-Fi, Bluetooth or capture card options
- TPM 2.0 and watchdog timer
- Industrial-grade robust, fanless and compact design

1.3 Packing List

The product package should include the items listed below. If any of the items below is missing, contact the distributor or the dealer from whom you purchased the product.

- SI-111-N Digital Signage Player
- Power Adaptor
- Power Cord

1.4 Specifications

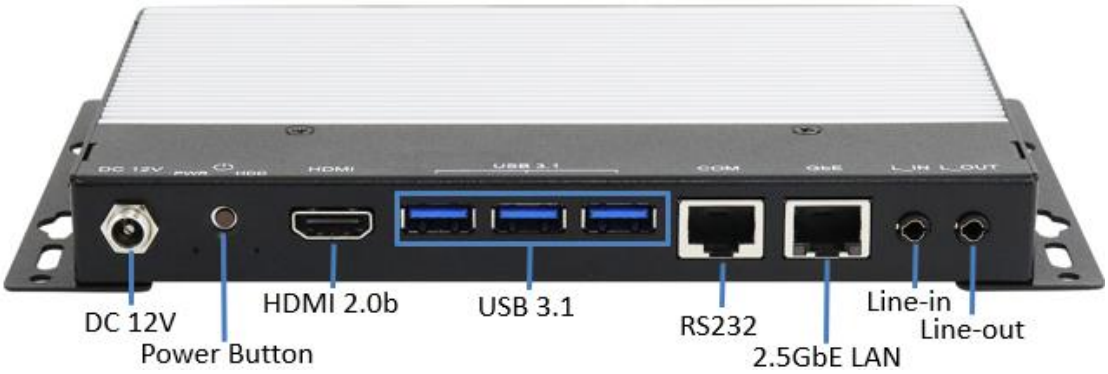
Product	SI-111-N
Motherboard	MBD111
CPU	Intel® Atom® x6211E (1.3~3.0GHz) Intel® Celeron® N6210 (1.2~2.6GHz)
CPU Socket	FCBGA1493
Chipset	SoC Integrated
Memory	2x DDR4-3200 SO-DIMM, Max. 32GB (support IBECC)
Graphics	Intel® UHD Graphics for 10th Gen Intel® Processors
LAN	1x Intel® I226IT 2.5GbE LAN for Atom® x6000 series 1x Intel® I226V 2.5GbE LAN for Celeron®
Expansion Slots	1x M.2 M-Key (2280) for storage 1x M.2 E-Key (2230) for Wi-Fi, Bluetooth or capture card option
I/O Interface	1x HDMI 2.0b, 3x USB 3.1, 1x RJ45 for 2.5GbE LAN 1x RJ45 for RS232 serial port 2x Audio connectors for Line-in / Line-out 1x Power button, 1x Lockable power connector 1x Power / HDD LED
Auto Control and Monitoring	Watchdog Timer: 256 segments, 0, 1, 2…255 (sec/min)
Power Requirement	+12V DC

Construction	1x M.2 M-Key (2280)
Weight	Aluminum + SGCC
Chassis Color	Black & White
Storage	1x M.2 M-Key (2280)
Power	50W power adaptor
Mounting	Standard system bracket
Dimensions	181mm(W) x 123mm(D) x 21.5mm(H)
Operating Temperature	0°C ~ 45°C (32°F~113°F) for SI-111-N -20°C ~ 70°C (-4°F~158°F) for SI-111-N11E
Storage Temperature	-40°C~80°C (-40°F~176°F)
Relative Humidity	10%~90% (non-condensing)
Vibration	SSD: 5 grms / 5~500Hz / random operation
Certification	CE, FCC Class-B, UKCA, cULus, LVD
Operating System	Win10 IoT Enterprise (64-bit) Ubuntu Linux (64-bit)

All specifications are subject to change without prior notice.

1.5 Product View

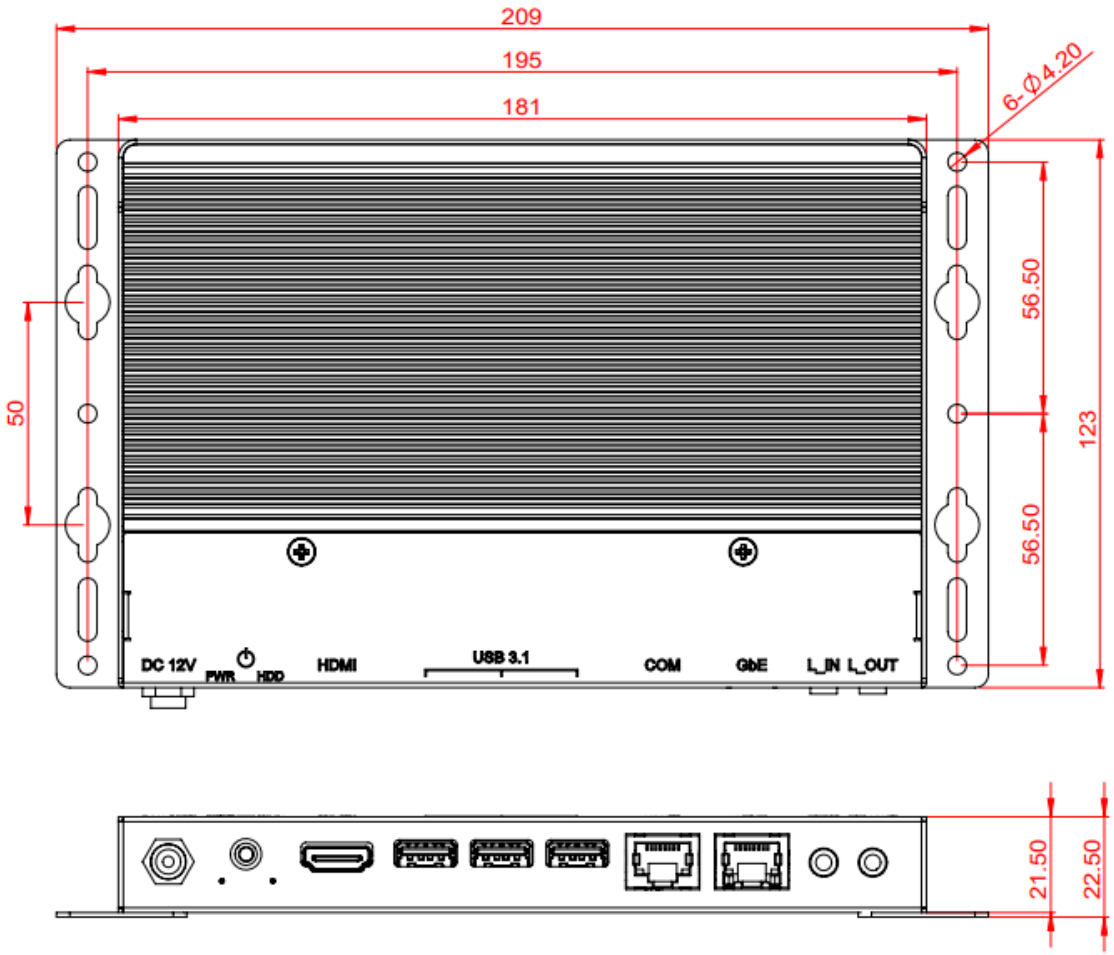
Front View



Oblique View

1.6 Dimensions

Unit: mm



Chapter 2
Hardware Installation & Motherboard Information

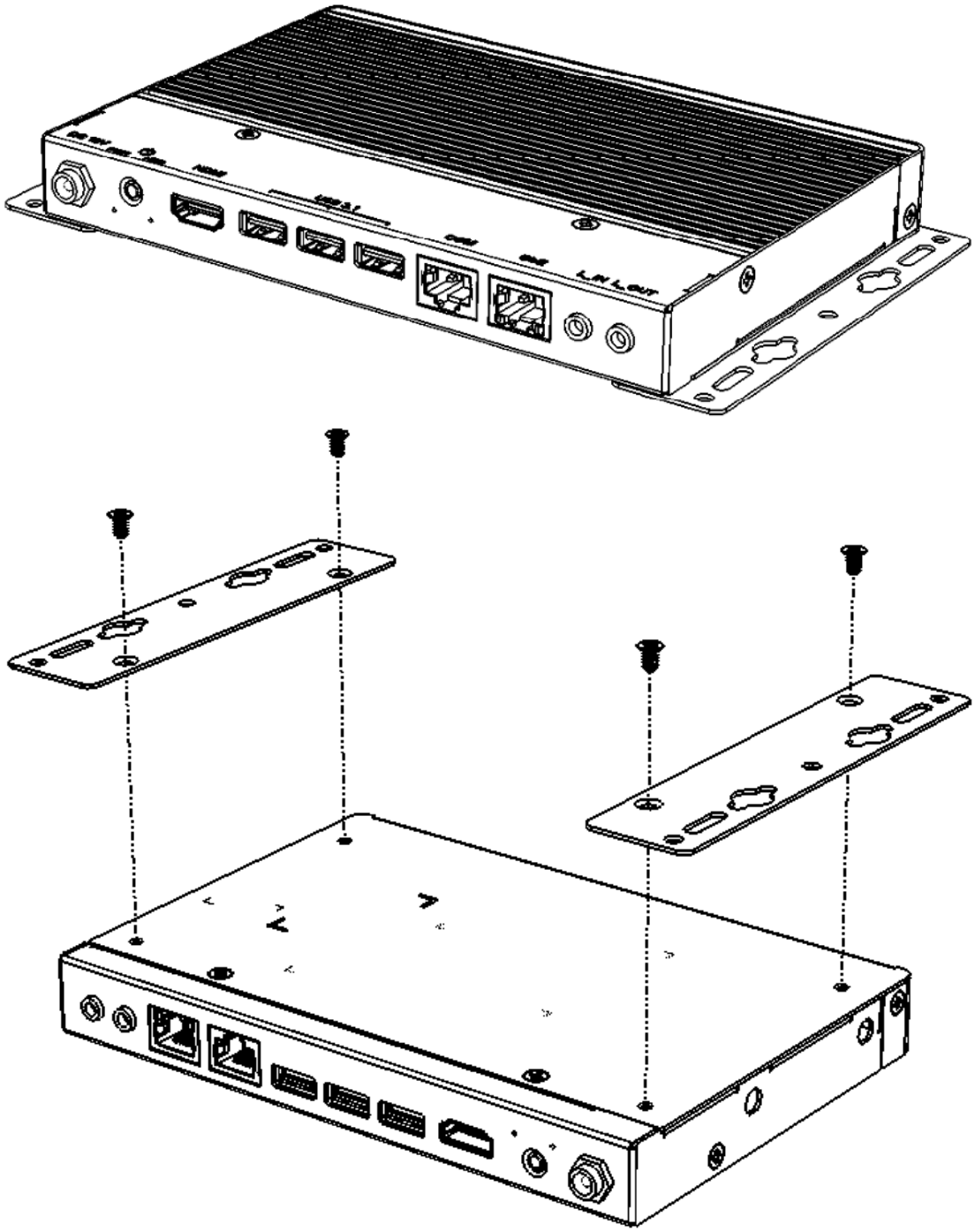
The information provided in this chapter includes:

- Installation of memory, M.2 cards and antennas
- Information and locations of connectors

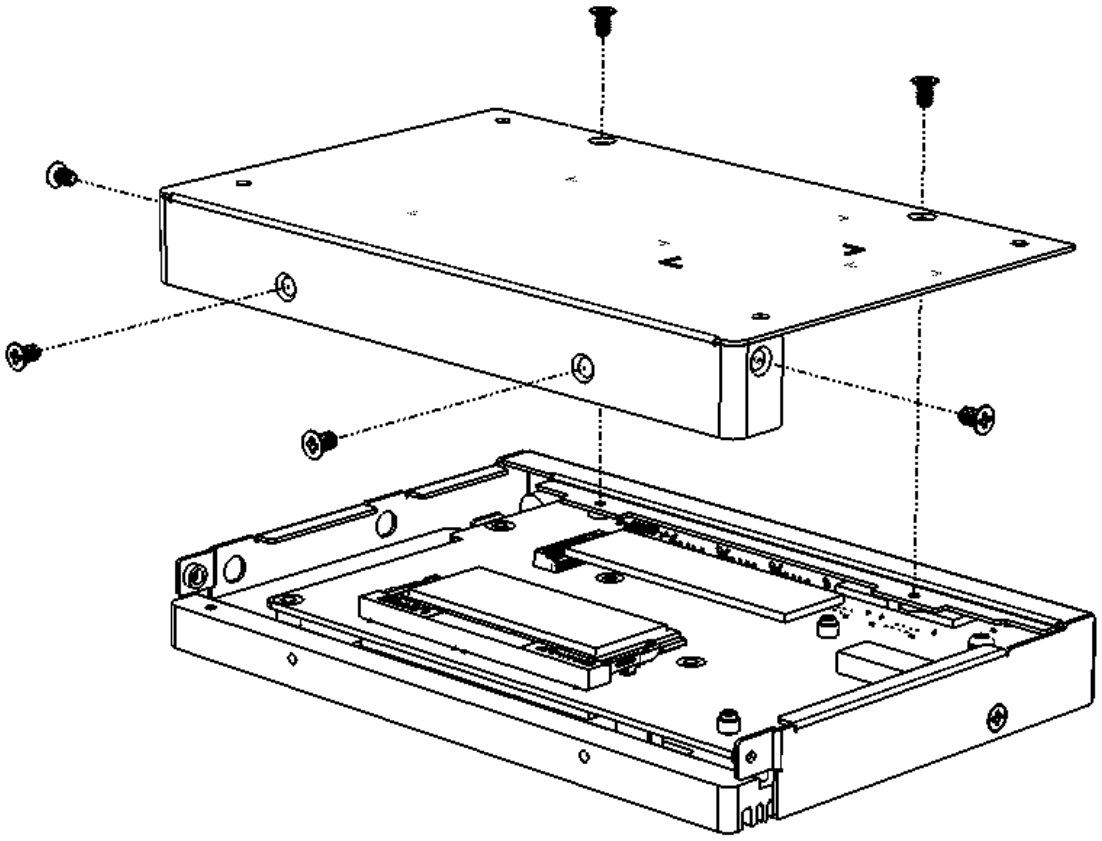
2.1 Installation / Replacement

The following pictures show how to disassemble the SI-111-N.

1. Turn the unit upside down and remove the mounting kit, as shown below.



2. Remove the screws as indicated in the image below to release the cover. Proceed to the next section to install or replace memory or other modules.



2.1.1 Memory

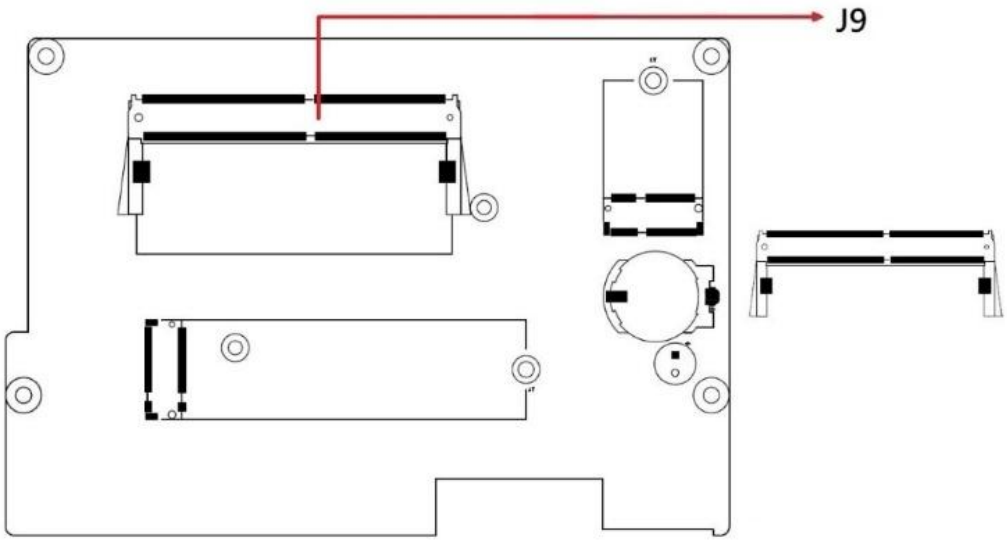
To install memory modules, locate the memory slot on the motherboard and follow these steps:

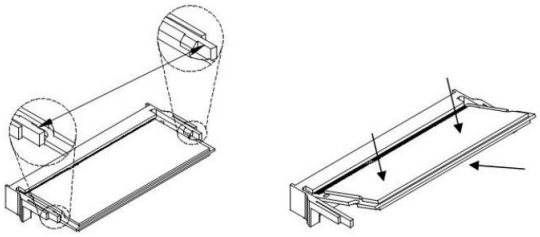
1. Align the notch (key) on the memory module with the ridge in the memory slot.
2. Insert the module at a slight angle, then gently push it upright until it clicks into place. The locking clips on both sides of the slot will automatically close to secure the module.

Note: Ensure the module is fully seated and both clips are locked.

To remove a memory module:

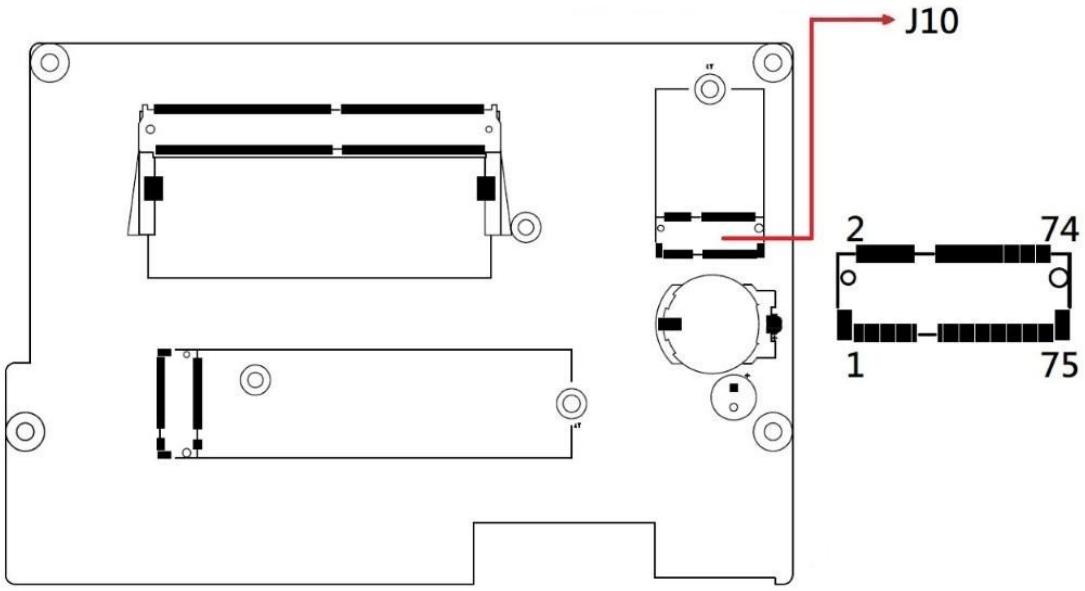
- Press both ejector tabs outward using your fingertips. The module will pop up and can then be lifted out.

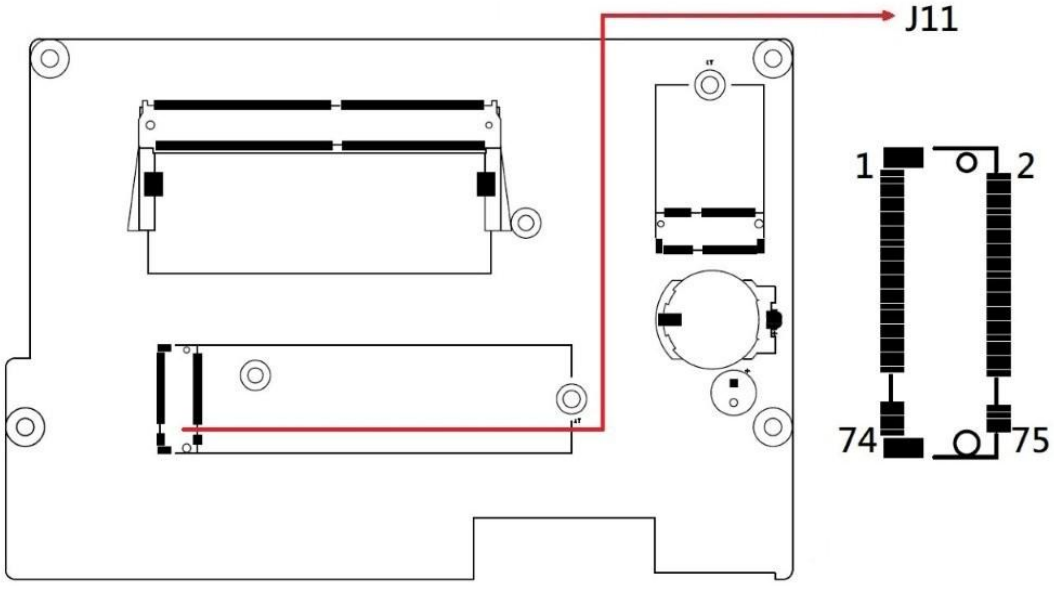




M.2 Cards Installation

1. Locate the M.2 slot on the motherboard.
2. Align the key notch on the M.2 card with the slot.
3. Insert the card at a slight angle.
4. Secure the card by fastening it with the screw provided.



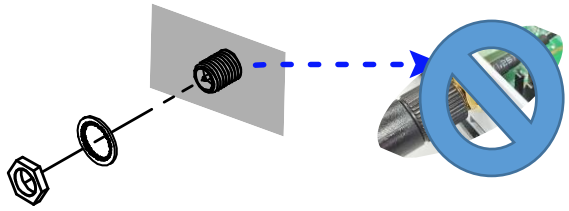


2.1.2 WiFi / 5G Antenna Installation

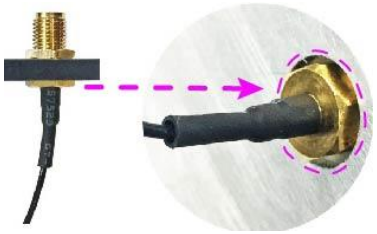
Thread the Wi-Fi/5G antenna extension cable through an antenna hole in the front I/O cover, then secure the antenna as shown below. Apply adhesive around the edge of the hex nut on the inner side of the front I/O cover to prevent the extension cable from loosening or falling out.

ⓘ **Info:** The diameter of the nut is approximately 6.35 mm (0.25”-36UNC).

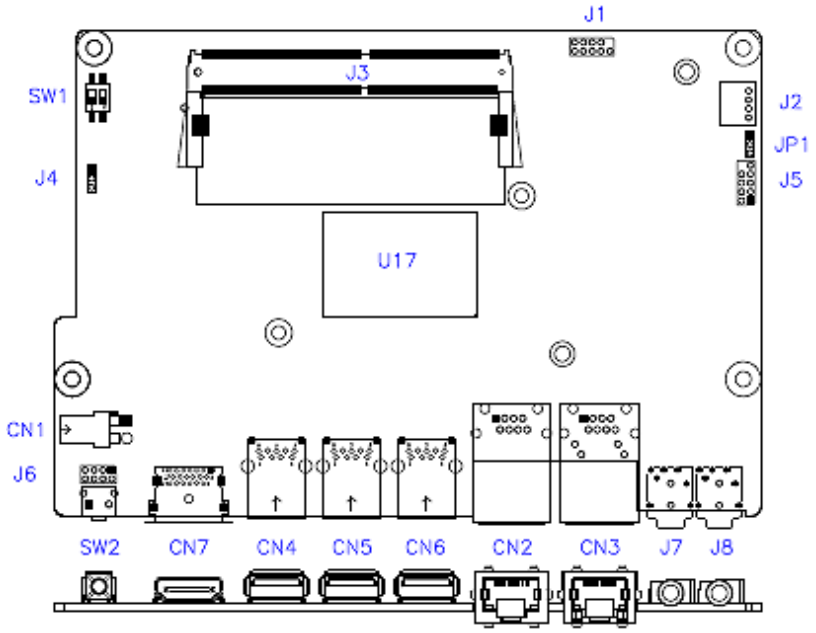
1. Thread and fasten the hex nut and washer. Then install the antenna.



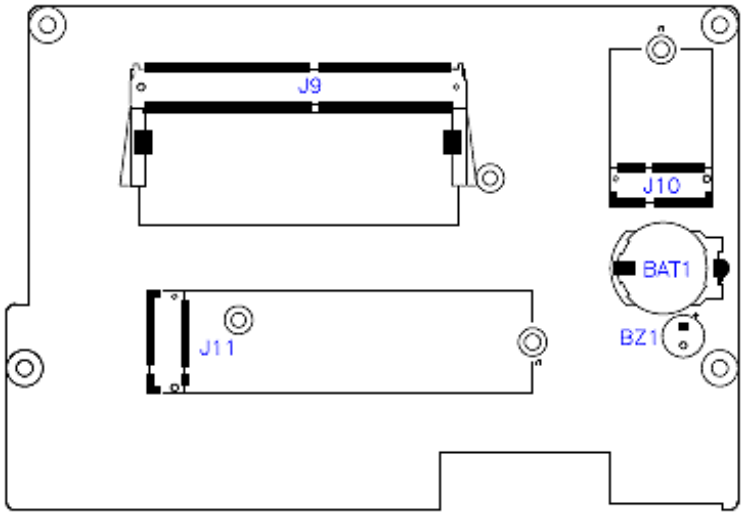
2. Apply adhesive around here.



2.2 Switches & Connectors on the Motherboard



MBD111 – top and I/O

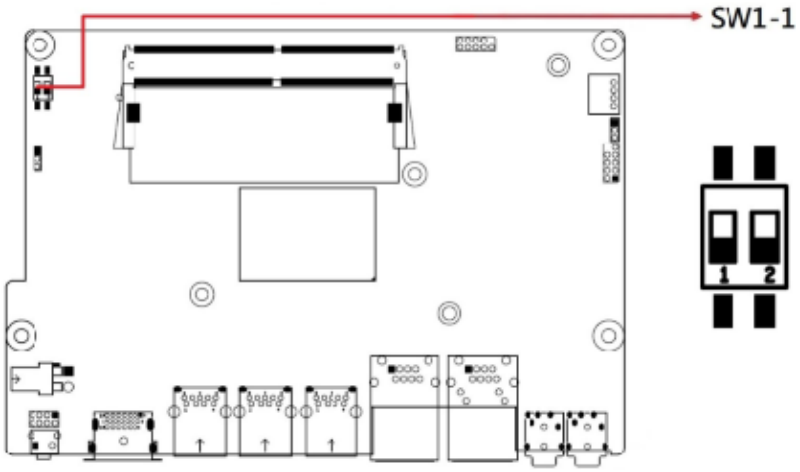


MBD111 – back and I/O

Switches on SI-111-N

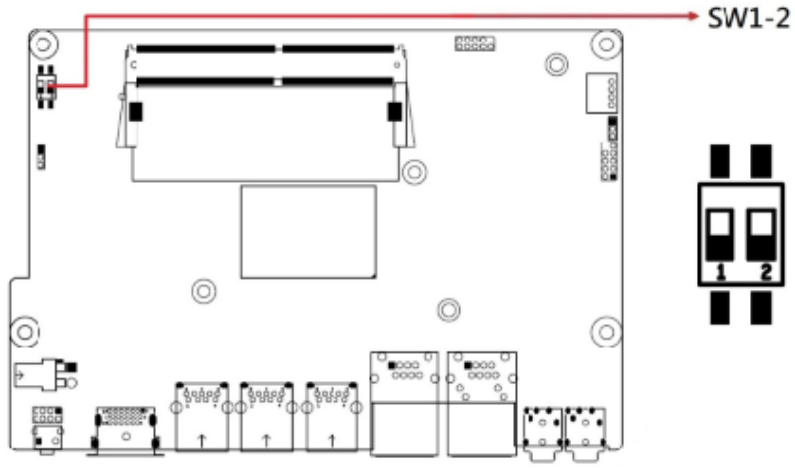
Switch	Function
SW1-1	Clear CMOS Data
SW1-2	Clear ME Register
SW2	Power Button

2.2.1 SW1-1: Clear CMOS Data



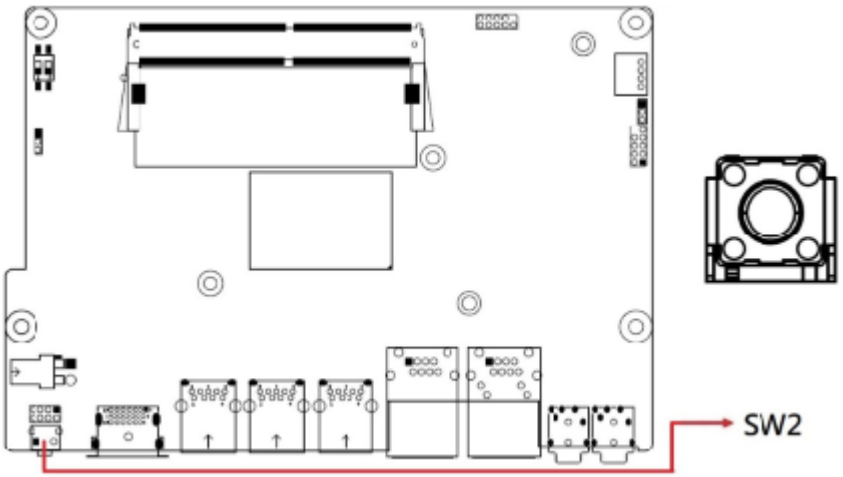
Function	Setting
Normal (default)	P1-OFF
Clear CMOS	P1-ON

2.2.2 SW1-2: Clear ME Register



Function	Setting
Normal (default)	P2-OFF
Clear ME	P2-ON

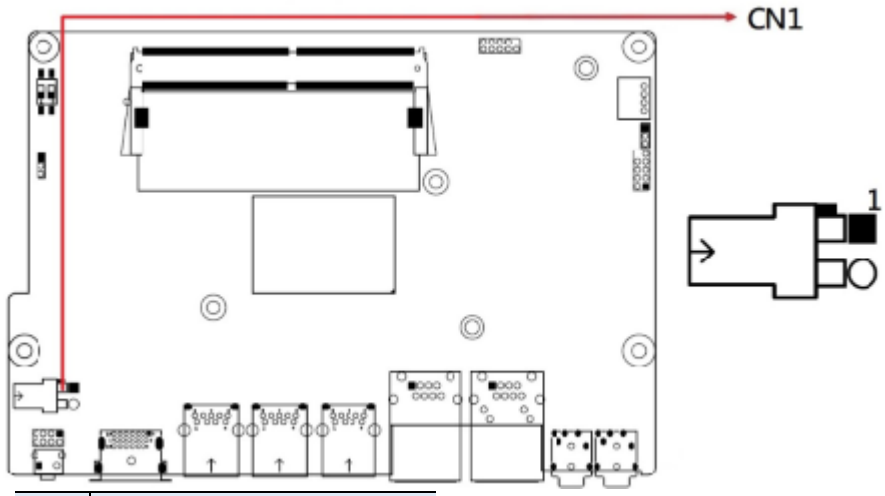
2.2.3 SW2: Power Button



Connectors on SI-111-N

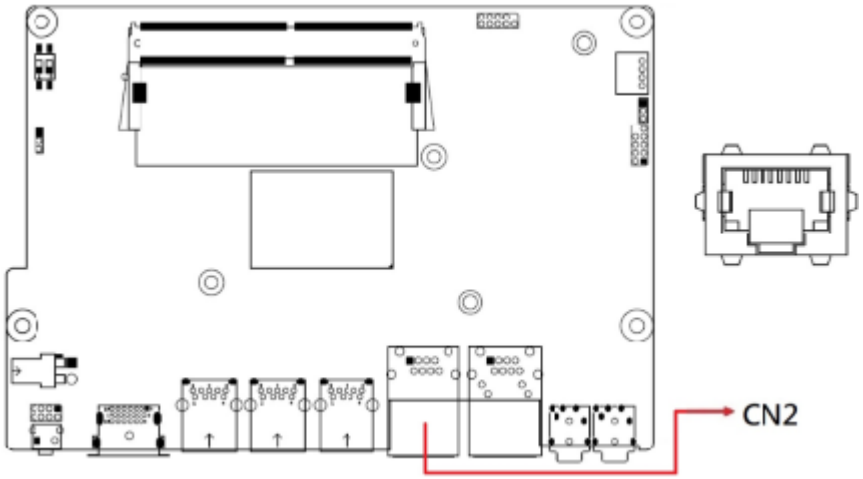
Connectors	Function
CN1	DC 12V Power Input Connector
CN2	COM1 RS232
CN3	LAN Connector
CN4, CN5, CN6	USB 3.1 Connectors
CN7	HDMI Connector
J1	SPI Flash Connector (Factory use only)
J2	Program iSmart MCU Connector (Factory use only)
J3	DDR4 Slot
J4	Program VCore Connector (Factory use only)
J5	Port 80 Connector (Factory use only)
J6	Front Panel Connector (optional)
J7	Audio Line-In Connector
J8	Audio Line-Out Connector
J9	DDR4 Slot
J10	M.2 E-Key Connector
J11	M.2 M-Key Connector

2.2.4 CN1: DC 12V Power Input Connector

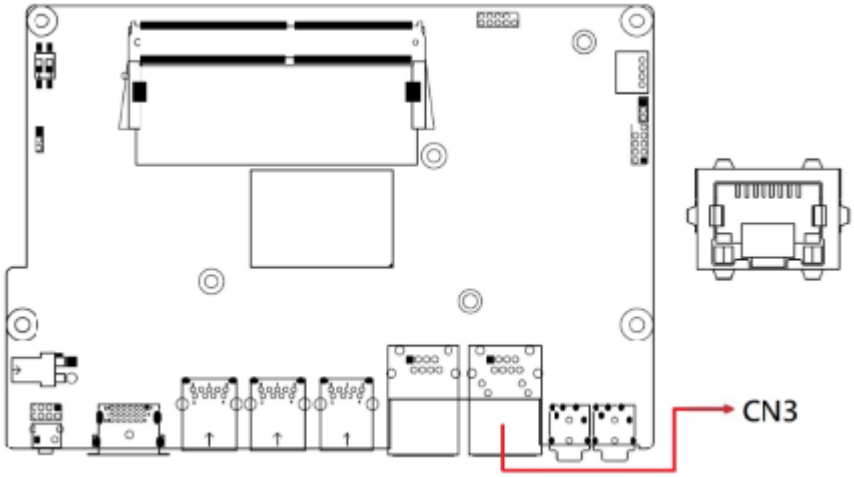


Pin	Assignment
1	Ground
2	+12V

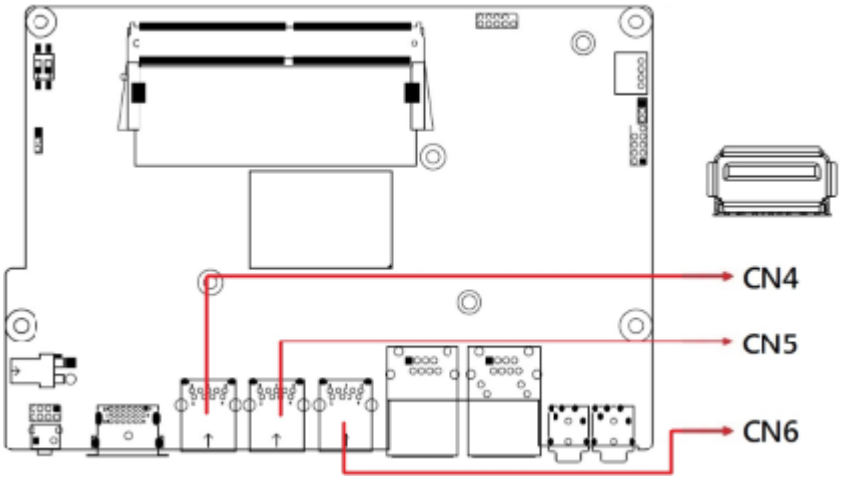
2.2.5 CN2: COM1 RS232



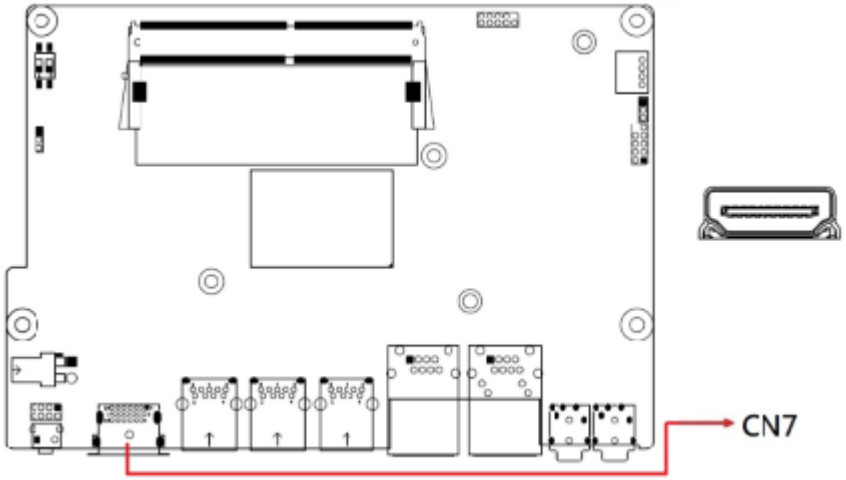
2.2.6 CN3: LAN Connector



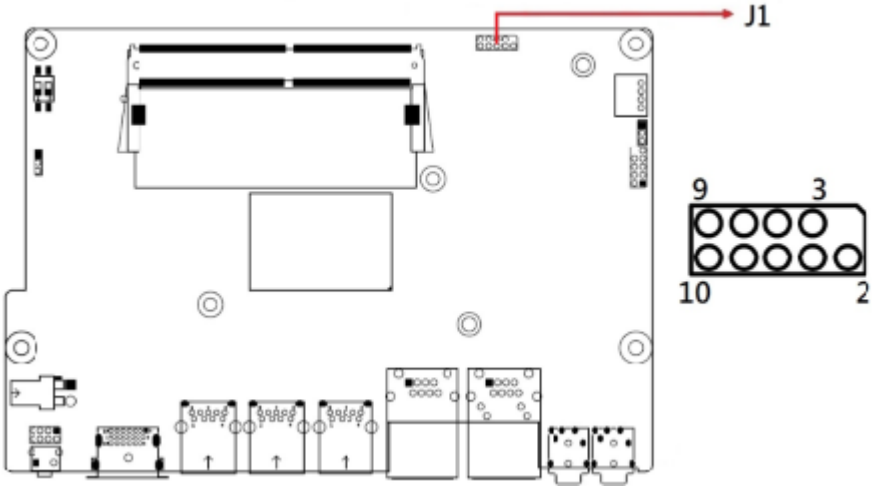
2.2.7 CN4, CN5, CN6: USB 3.1 Connectors



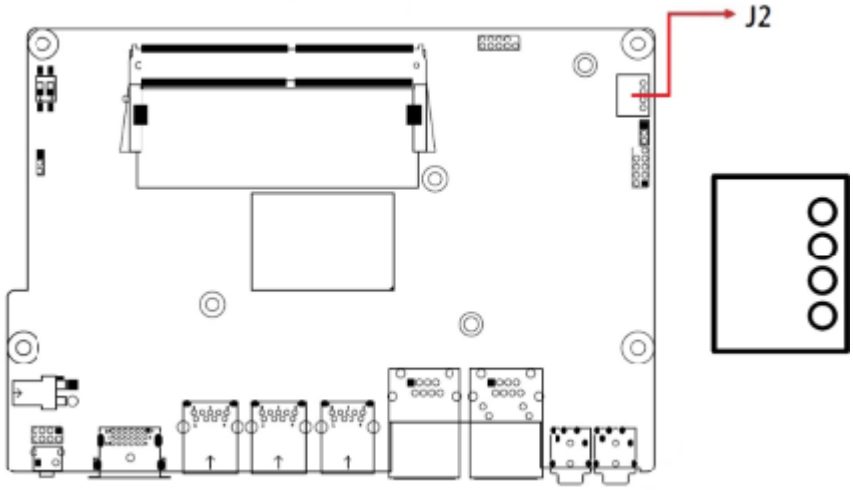
2.2.8 CN7: HDMI Connector



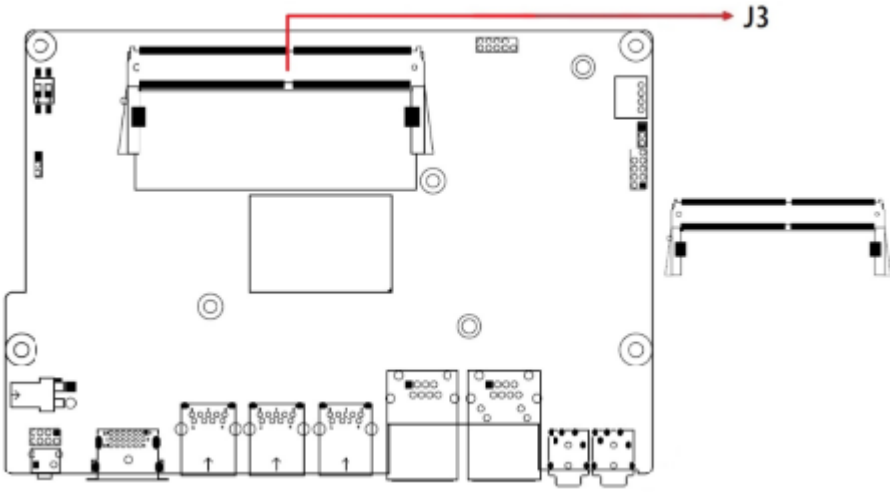
2.2.9 J1: SPI Flash Connector (Factory use only)



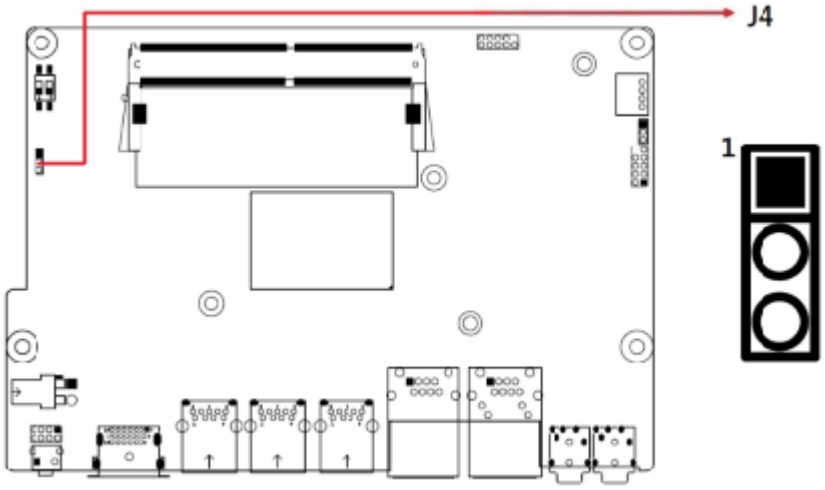
2.2.10 J2: Program iSmart MCU Connector (Factory use only)



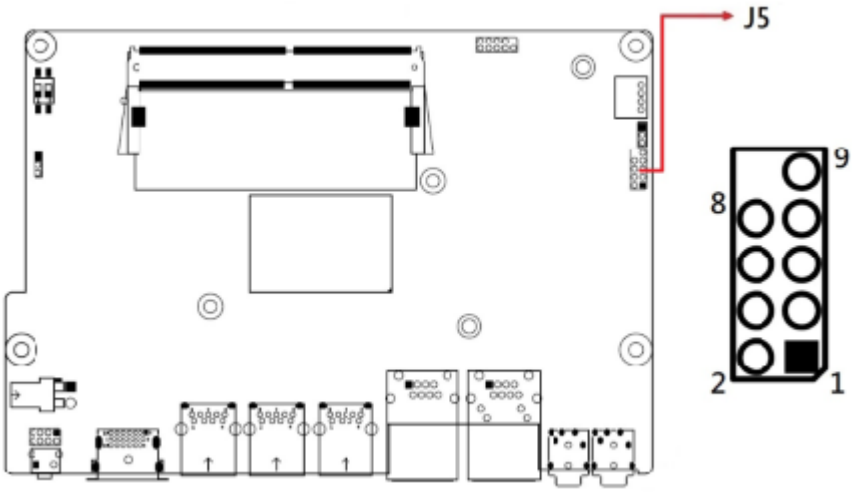
2.2.11 J3: DDR4 Slot



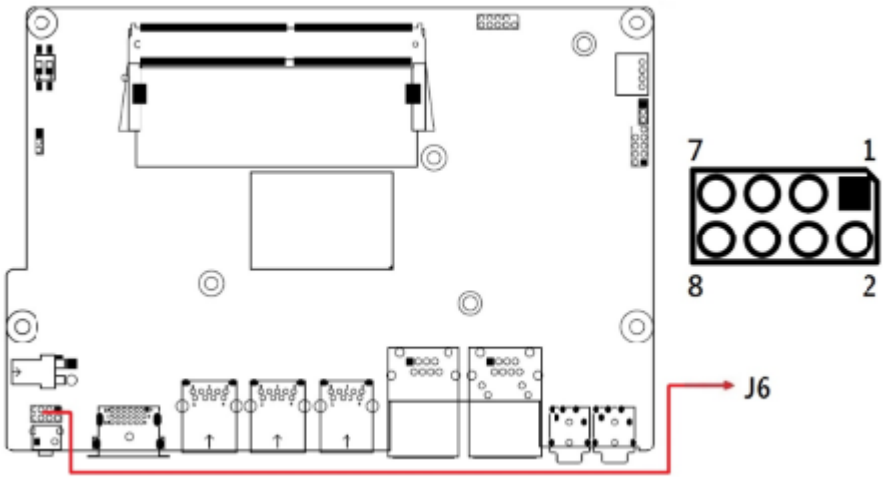
2.2.12 J4: Program VCore Connector (Factory use only)



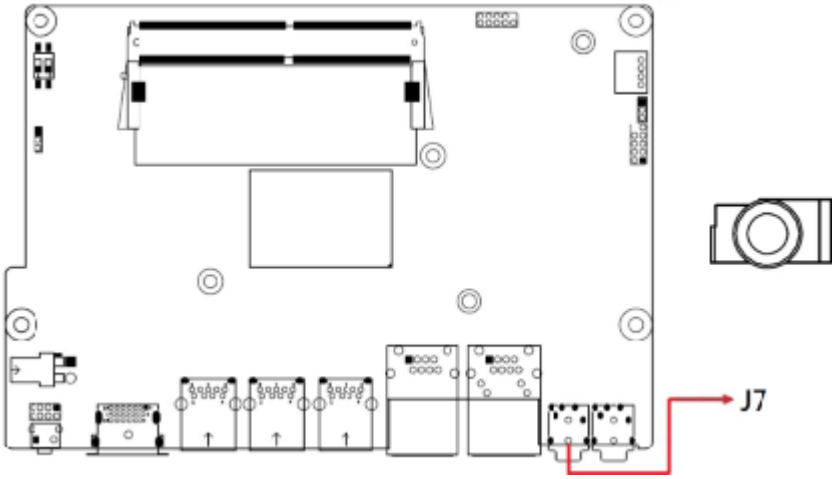
2.2.13 J5: Port 80 Connector (Factory use only)



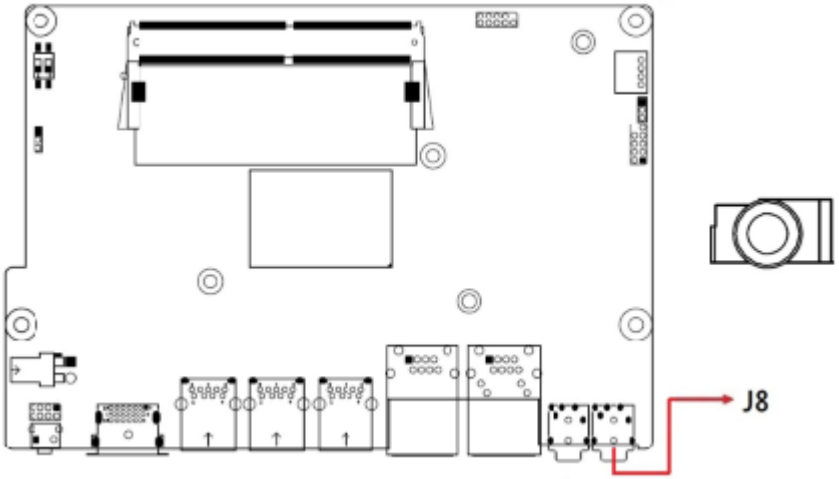
2.2.14 J6: Front Panel Connector (optional)



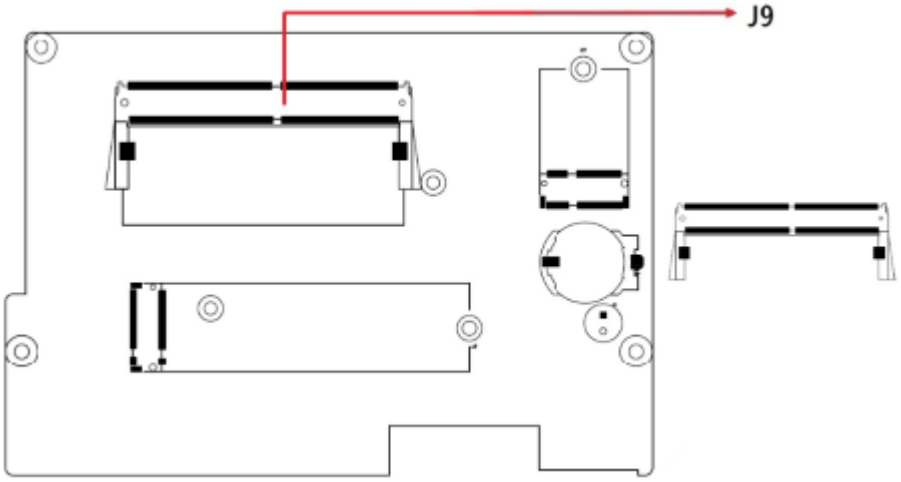
2.2.15 J7: Audio Line-In Connector



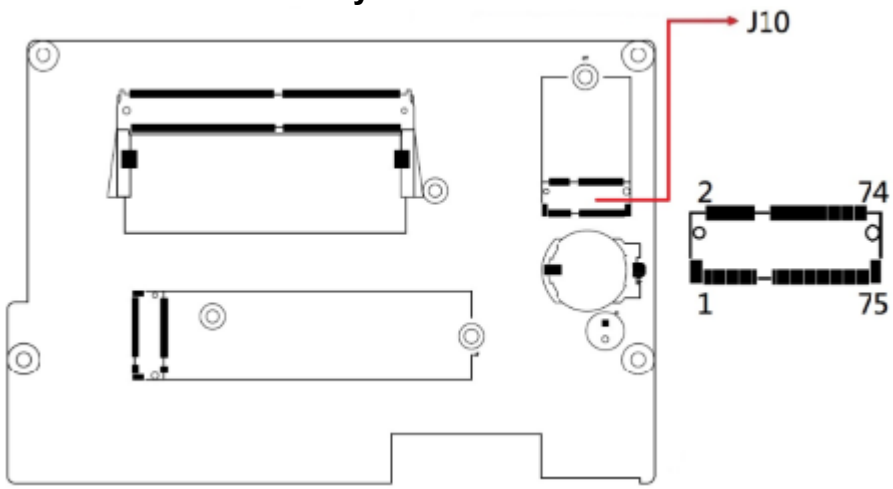
2.2.16 J8: Audio Line-Out Connector



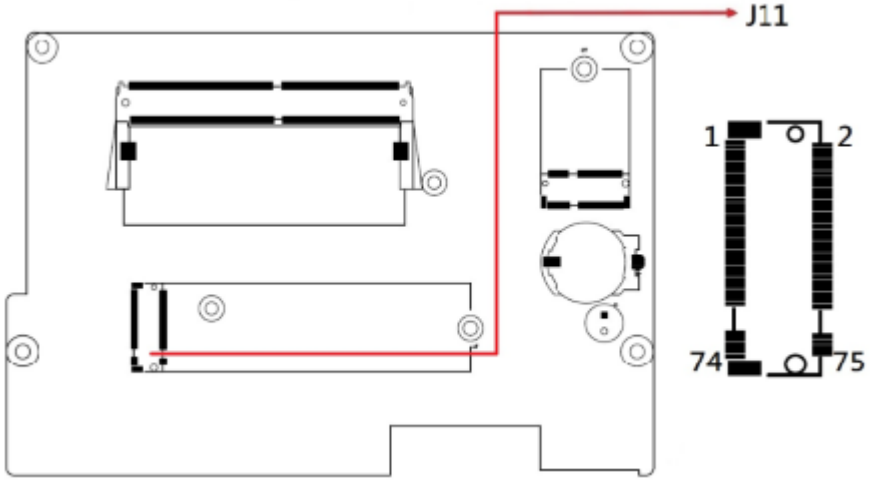
2.2.17 J9: DDR4 Slot



2.2.18 J10: M.2 E-Key Connector



2.2.19 J11: M.2 M-Key Connector



Chapter 3
Driver Installation

The information provided in this chapter includes:

- Intel® Chipset Software Installation Utility
- VGA Driver
- HD Audio Driver
- LAN Driver
- Intel® Management Engine Drivers Installation

3.1 Introduction

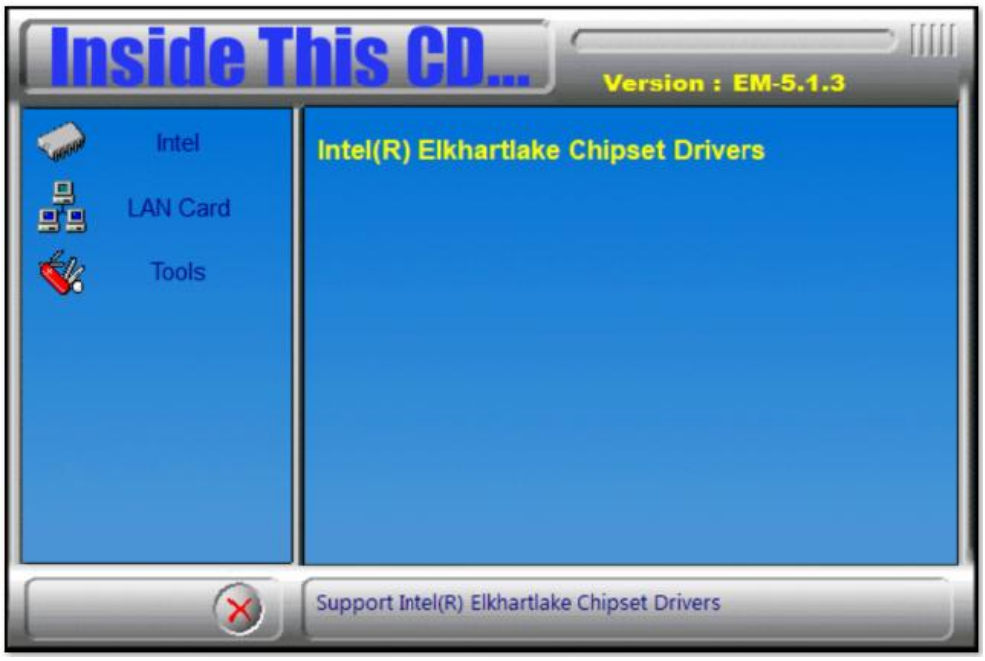
This section describes the installation procedures for the software drivers. The software drivers are also available at the IBASE website www.ibase.com.tw.

Note: After installing the Windows operating system, install the Intel® Chipset Software Installation Utility before proceeding with the driver installation.

3.2 Intel® Chipset Software Installation Utility

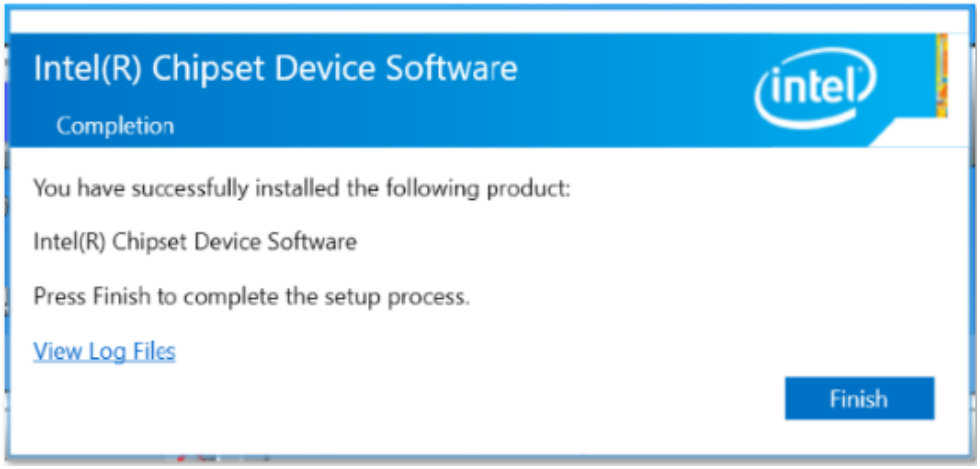
The Intel® Chipset drivers should be installed before the software drivers to install INF files for Plug & Play function for Intel chipset components.

1. Run the driver disk. Click **Intel** on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.



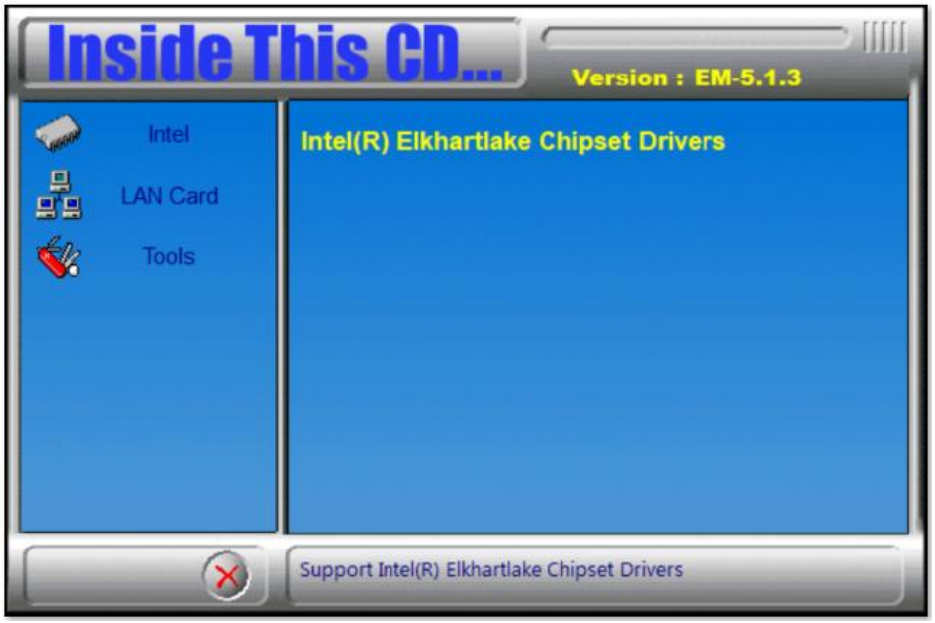
2. Click **Intel(R) Chipset Software Installation Utility**.

3. When the welcome screen of the Intel® Chipset Device Software appears, click **Next**.
4. Accept the software license agreement and proceed with the installation process.
5. On the *Readme File Information* screen, click **Install**.
6. After the installation, press **Finish** to complete the setup process.



3.3 VGA Driver Installation

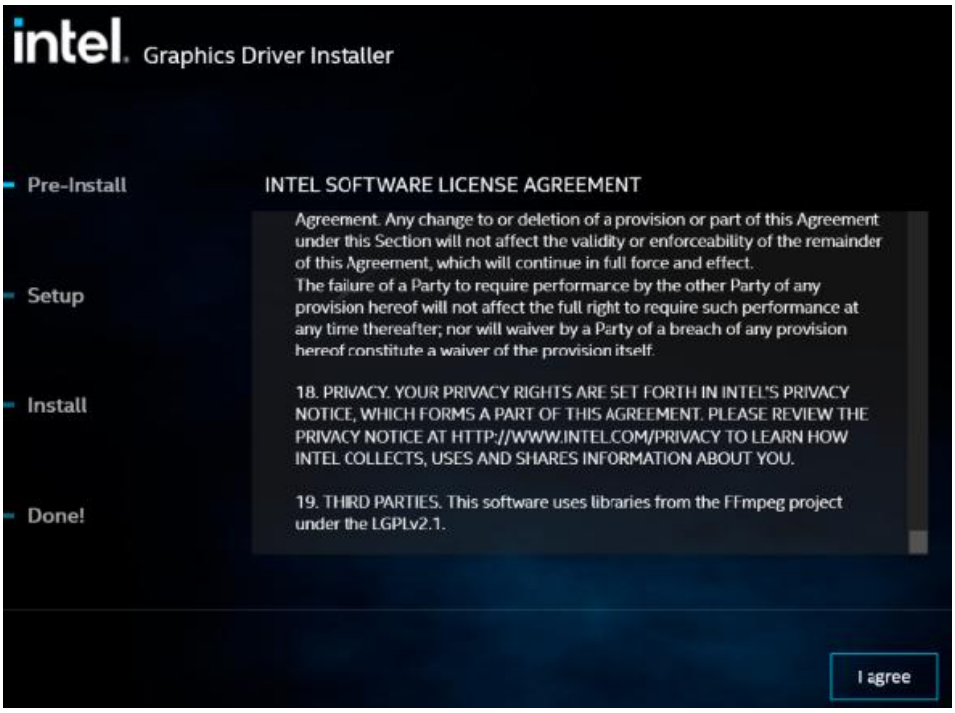
1. Run the driver disk. Click **Intel** on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.



2. Click **Intel(R) Elkhartlake Graphics Driver**.

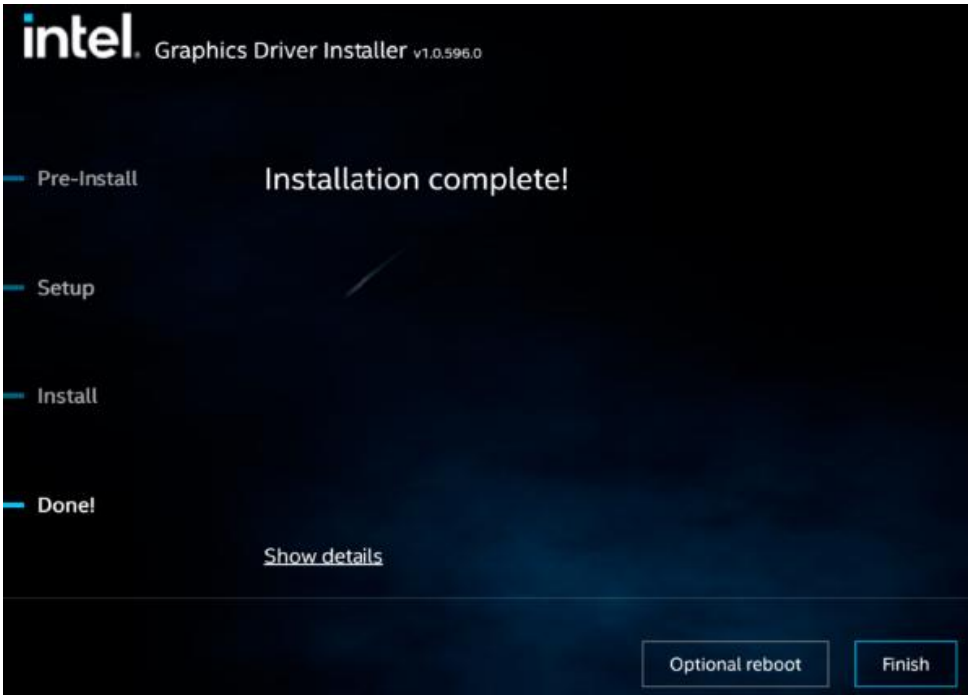
3. On the Intel Graphics Driver Installer screen, click **Begin installation**.

4. Click **I agree**.



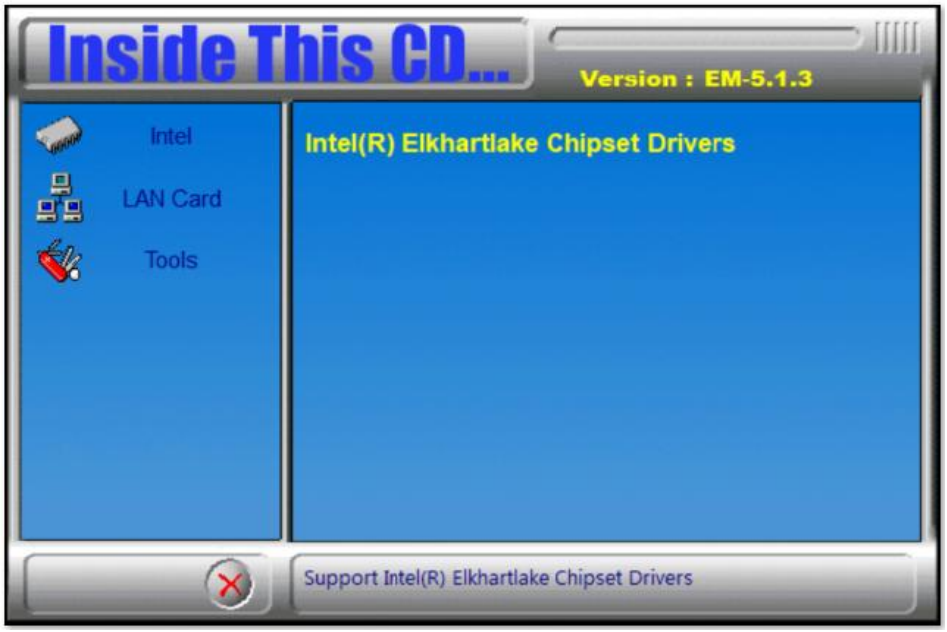
5. Click **Start**.

6. Click **Finish**.



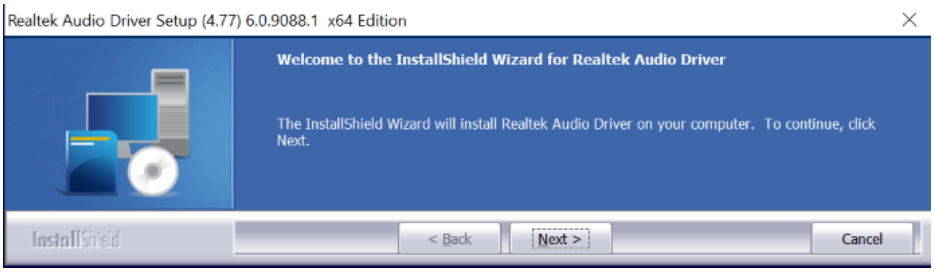
3.4 HD Audio Driver Installation

1. Run the driver disk. Click Intel on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.

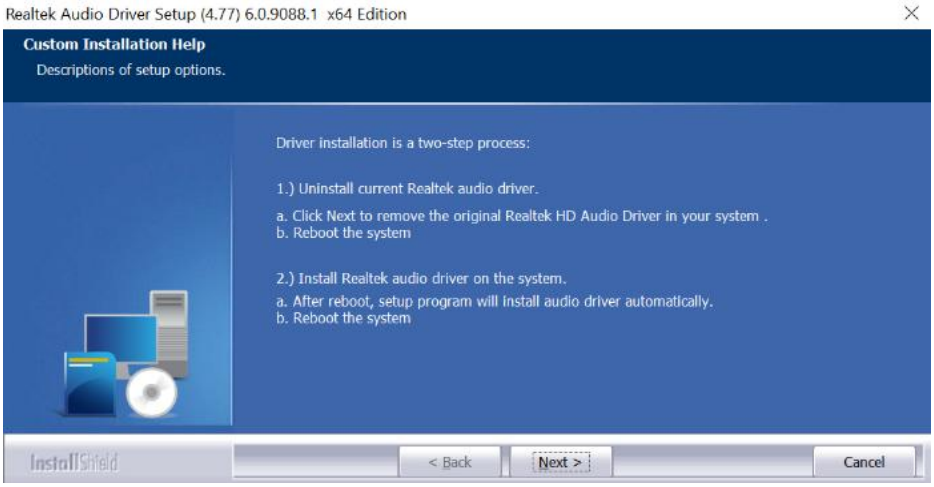


2. Click **Realtek High Definition Audio Driver**.

3. On the welcome screen, click **Next**.



4. On the Custom Installation Help screen, click **Next**.

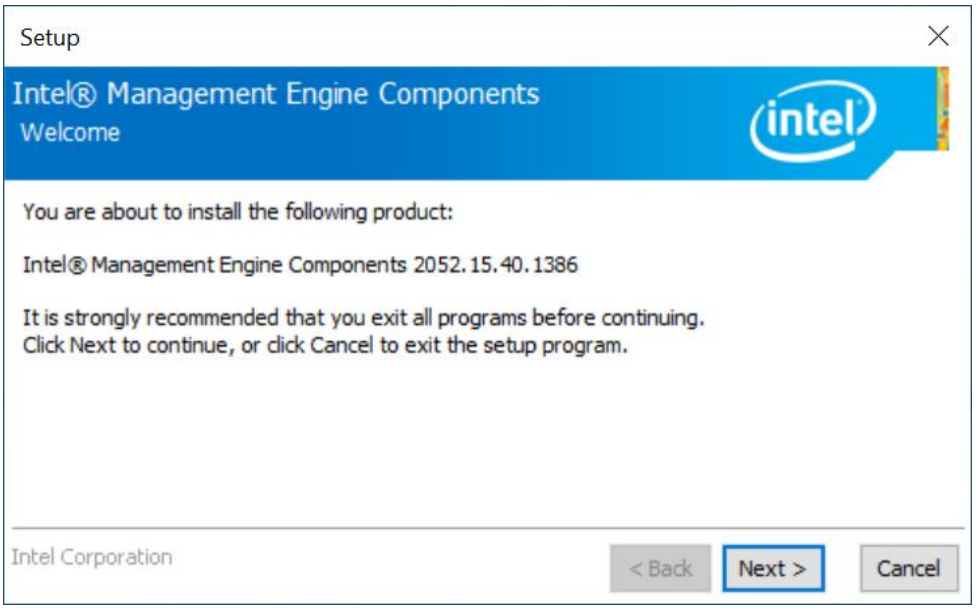


5. When InstallShield Wizard has finished the installation, click **Finish**.

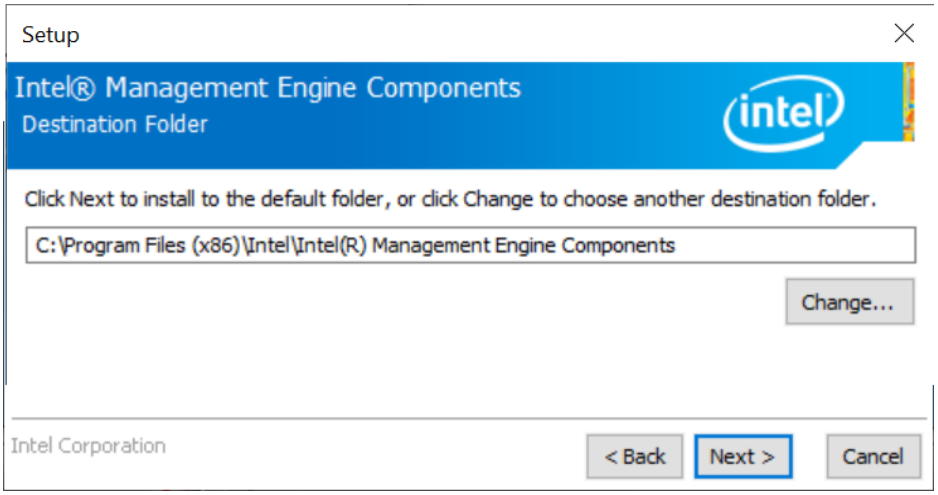
3.5 Intel® ME Drivers Installation

1. Run the driver disk. Click Intel on the left pane and then **Intel(R) ME Drivers**.

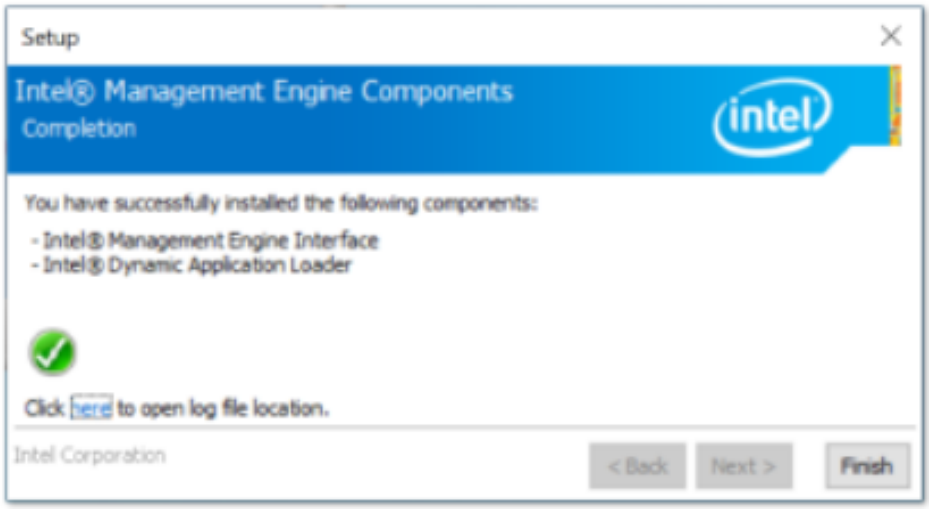
2. On the welcome screen of the Intel® Management Engine Components, click **Next**.



3. Accept the license agreement and click **Next**.
4. On the *Setup's Destination Folder* screen, click **Next** to install to the default folder, or click **Change** to choose another destination folder.



5. After the Intel® Management Engine Components have been installed, click **Finish**.



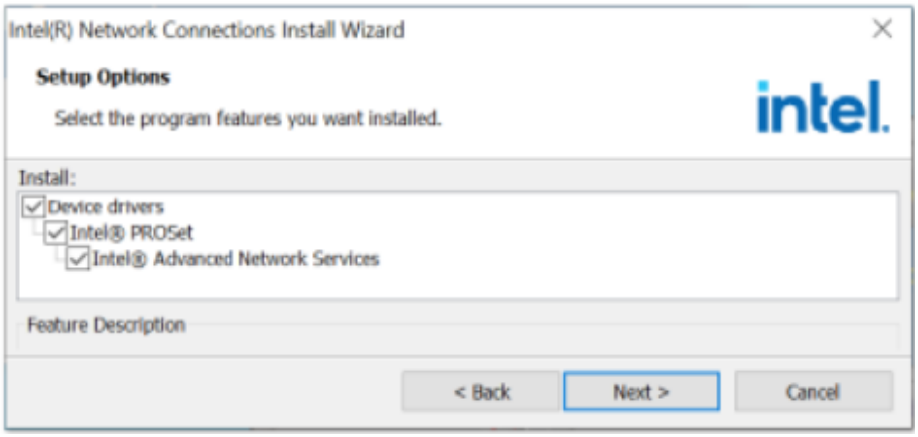
3.6 LAN Drivers Installation

1. Run the driver disk. Click **LAN Card** on the left pane and then **Intel LAN Controller Drivers** on the right pane.

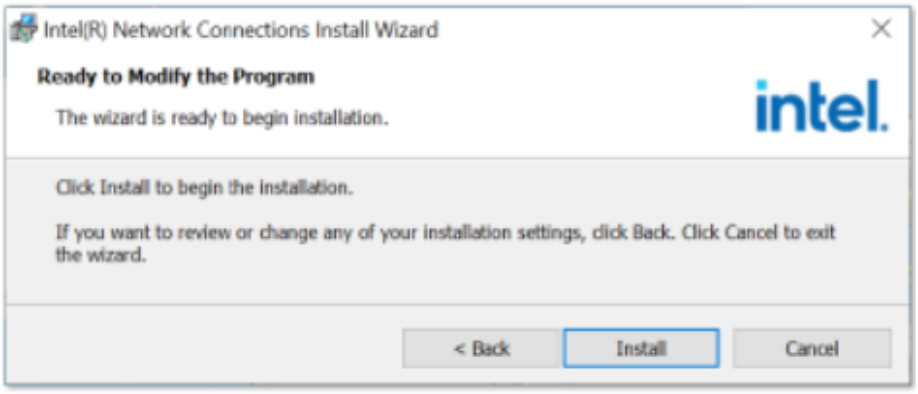
2. Choose **Intel(R) I21x/ I22x Gigabit Network Drivers**.

3. Click **Install Drivers and Software**.

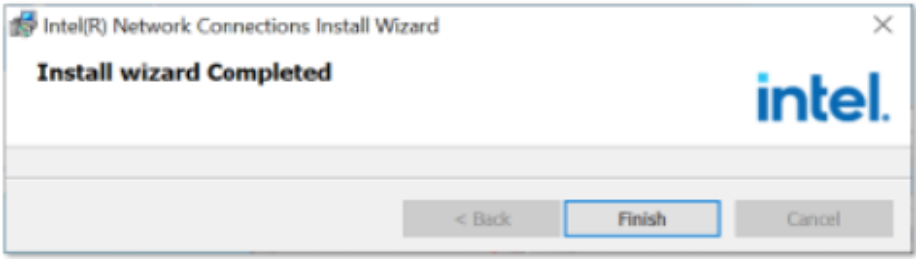
4. On the welcome screen of the Intel(R) Network Connections install wizard, click **Next**.
5. On the Setup Options screen, click **Next**.



6. Click **Install**.



7. When Install wizard has completed the installation, click **Finish**.



Chapter 4
BIOS Setup

This chapter describes the different settings available in the AMI BIOS. The topics covered in this chapter are as follows:

- Main Settings
- Advanced Settings
- Chipset Settings
- Security Settings
- Boot Settings
- Save & Exit

4.1 Introduction

The BIOS (Basic Input/Output System) installed in the ROM of the system supports Intel® processors. The BIOS provides critical low-level support for standard devices such as disk drives, serial ports and parallel ports. It also provides password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

4.2 BIOS Setup

The BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the BIOS is immediately activated. Press the <Del> key immediately allows you to enter the Setup utility. If you are a little bit late pressing the <Del> key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup.

If you still need to enter Setup, restart the system by pressing the ”Reset” button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again.

The following message will appear on the screen:

```
Press <DEL> to Enter Setup
```

In general, press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help, and <Esc> to quit.

When you enter the BIOS Setup utility, the *Main Menu* screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

Warning: It is strongly recommended that you avoid making any changes to the chipset defaults.

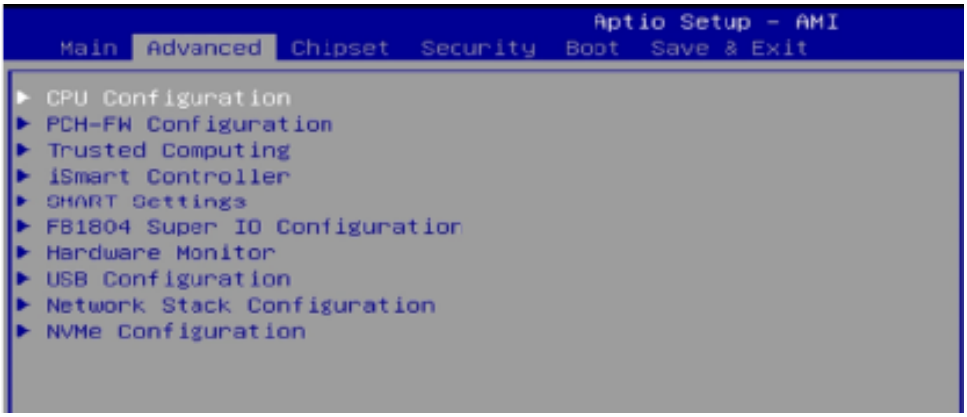
These defaults have been carefully chosen by both AMI and the system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could make the system unstable and crash in some cases.

4.3 Main Settings

BIOS Setting	Description
System Date	Sets the date. Use the <Tab> key to switch between the date elements.
System Time	Set the time. Use the <Tab> key to switch between the time elements.

4.4 Advanced Settings

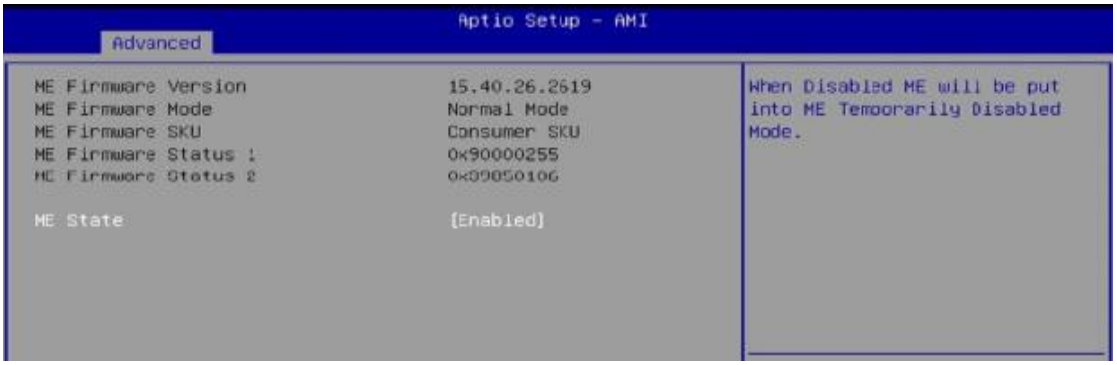
This section allows the configuration of the system and the selection of the system features according to your preference.



4.4.1 CPU Configuration

BIOS Setting	Description
Intel (VMX) Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.

4.4.2 PCH-FW Configuration

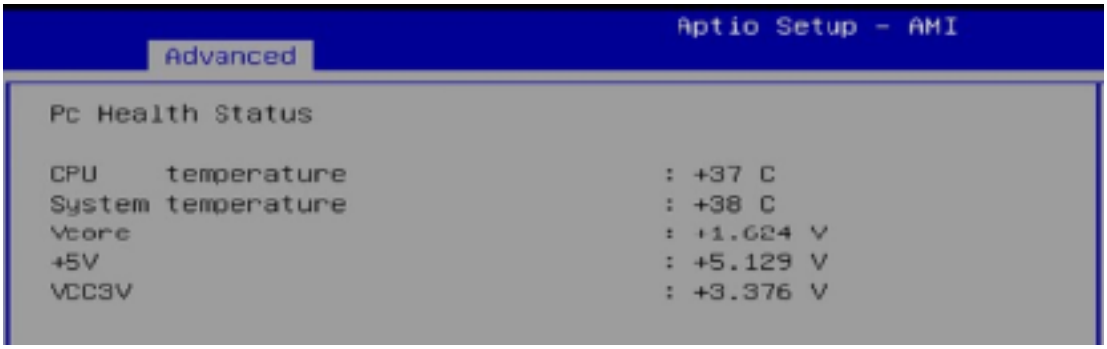


BIOS Setting	Description
ME State	When disabled, ME will be put into ME Temporarily Disabled Mode.
AMT BIOS Features	When disabled, AMT BIOS Features are no longer supported and user is no longer able to access MEBx Setup. Note: This option does not disable Manageability Features in FW.

4.4.3 Trusted Computing

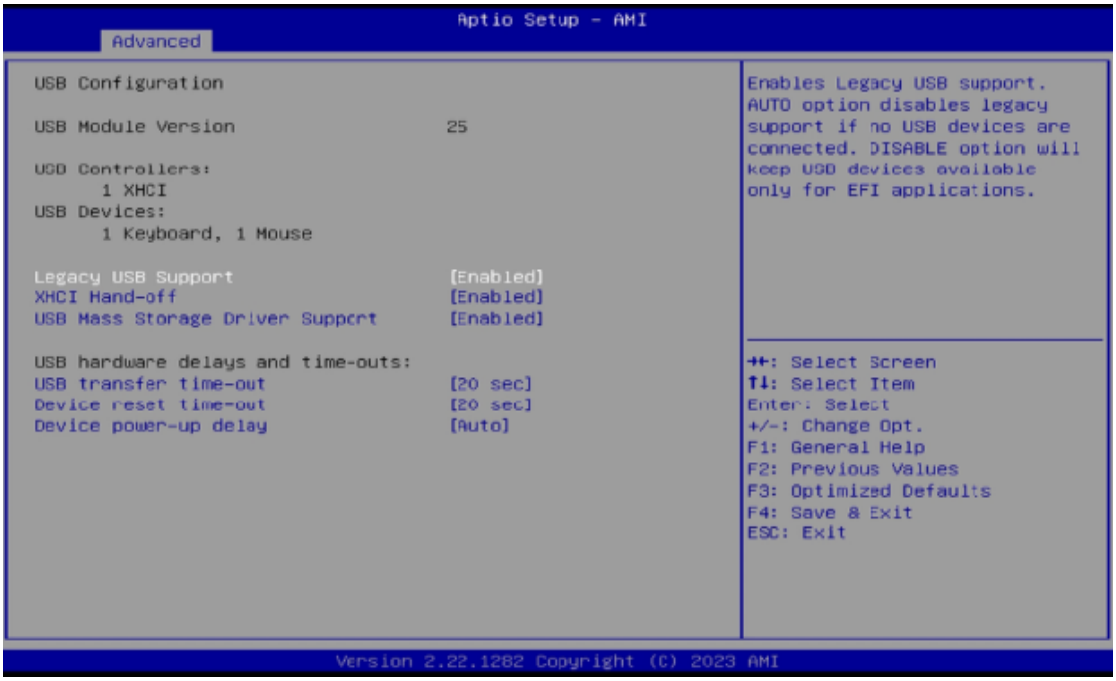
BIOS Setting	Description
Security Device Support	Option: Enable / Disable. OS will not show security device. TCG EFI protocol and INTIA interface will not be available.
SHA256 PCR Bank	Enables / Disables SHA-1 PCR Bank.
Pending operation	Schedule an operation for the security device. Note: Your computer will reboot during restart in order to change state of security device.
Platform Hierarchy	Enables / Disables platform hierarchy.
Storage Hierarchy	Enables / Disables storage hierarchy.
Endorsement Hierarchy	Enables / Disables endorsement hierarchy.
Physical Presence Spec Version	Selects to show the PPI Spec Version (1.2 or 1.3) that the OS supports. **Note:** Some HCK tests might not support 1.3.
Device Select	**TPM 1.2** will restrict support to TPM 1.2 devices only. **TPM 2.0** will restrict support to TPM 2.0 devices only. **Auto** will support both with the default being set to TPM 2.0 deices if not found, and TPM 1.2 device will be enumerated.

4.4.4 Hardware Monitor



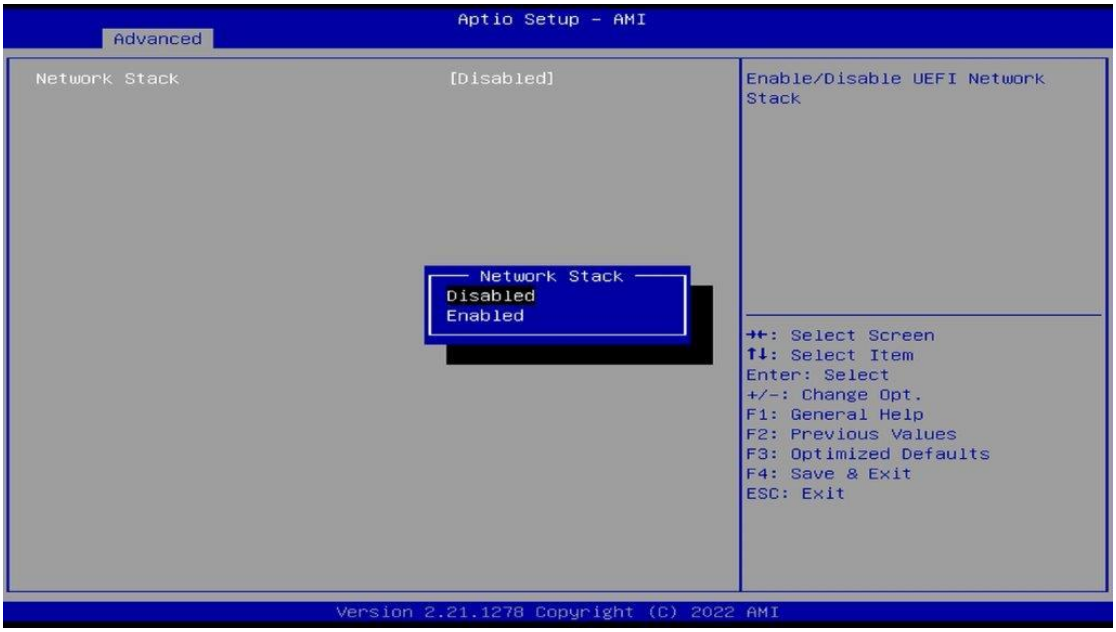
BIOS Setting	Description
Temperatures / Voltages	These fields are the parameters of the hardware monitoring function feature of the motherboard. The values are read-only values as monitored by the system and show the PC health status.

4.4.5 USB Configuration



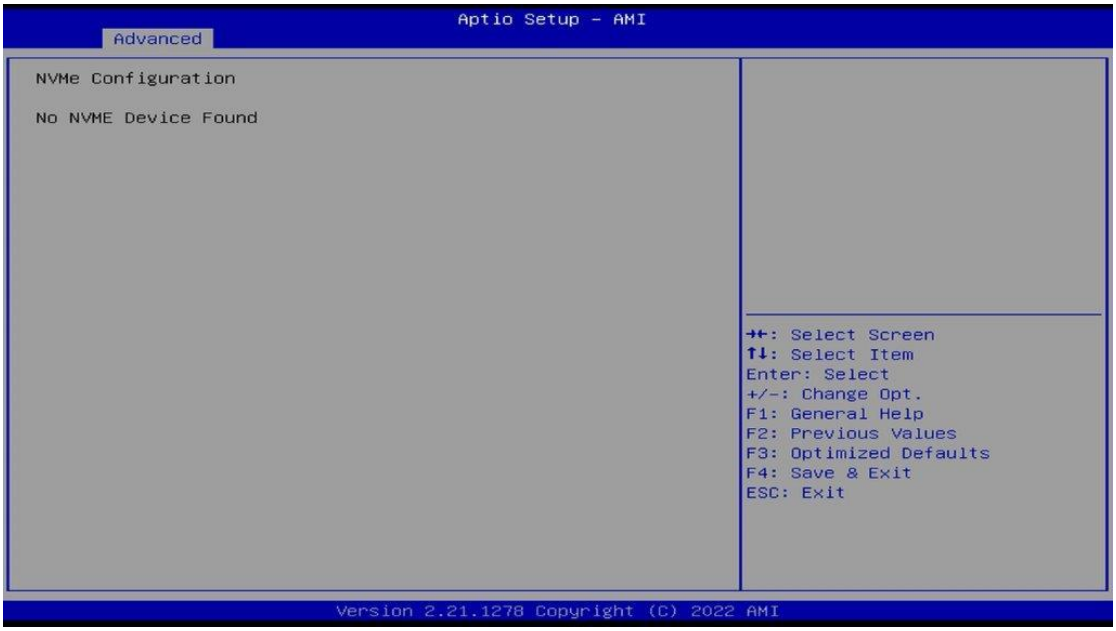
BIOS Setting	Description
Legacy USB Support	• **Enable:** Enables Legacy USB Support. • **Auto:** Disables legacy support if no USB devices are connected. • **Disable:** Keeps USB devices available only for EFI applications.
XHCI Hand-off	This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	Enables / Disables the support for USB mass storage driver.
USB Transfer time-out	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	Seconds of delaying execution of start unit command to USB mass storage device.
Device power-up delay	The maximum time the device will take before it properly reports itself to the Host Controller. “Auto” uses default value for a Root port it is 100ms. But for a Hub port, the delay is taken from Hub descriptor.

4.4.6 Network Stack Configuration

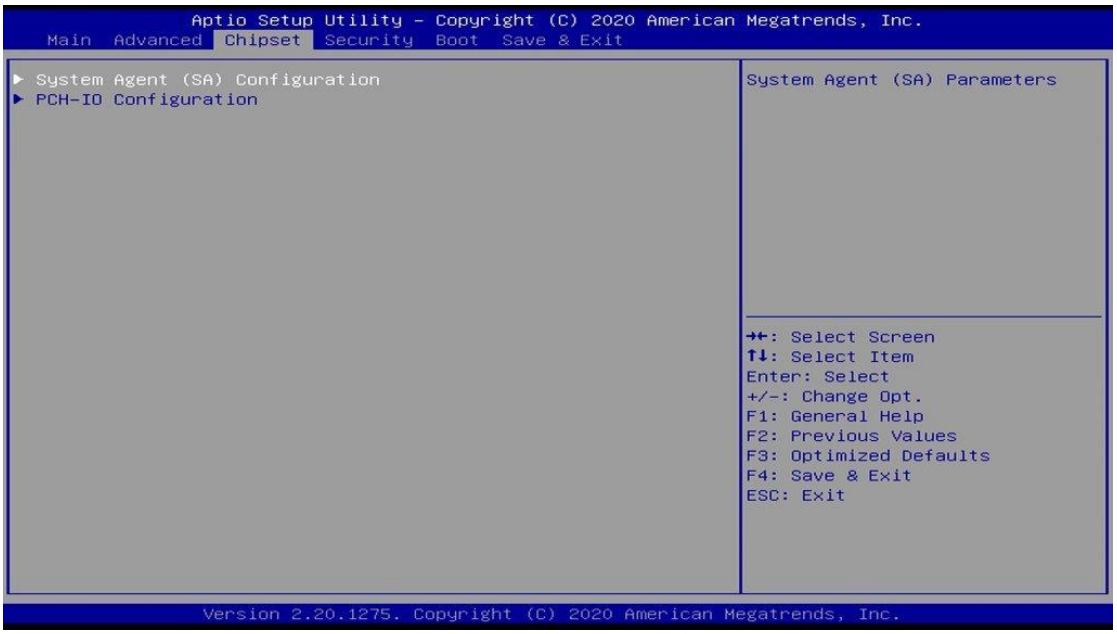


BIOS Setting	Description
Network Stack	Enables / Disables UEFI Network Stack.

4.4.7 NVMe Configuration



4.5 Chipset Settings

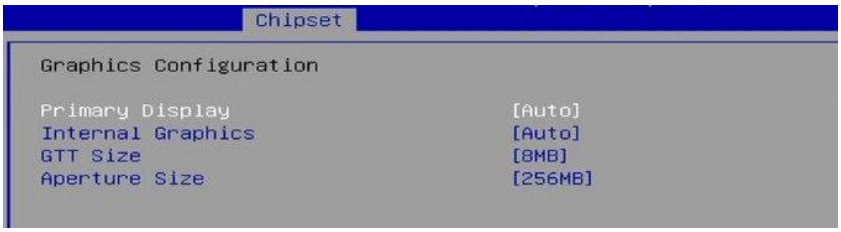


BIOS Setting	Description
System Agent (SA) Configuration	System Agent (SA) parameters
PCH-IO Configuration	PCH parameters

4.5.1 System Agent (SA) Configuration

BIOS Setting	Description
Graphics Configuration	Configures the graphics settings.
VT-d	Checks if VT-d function on MCH is supported.

4.5.1.1. Graphics Configuration

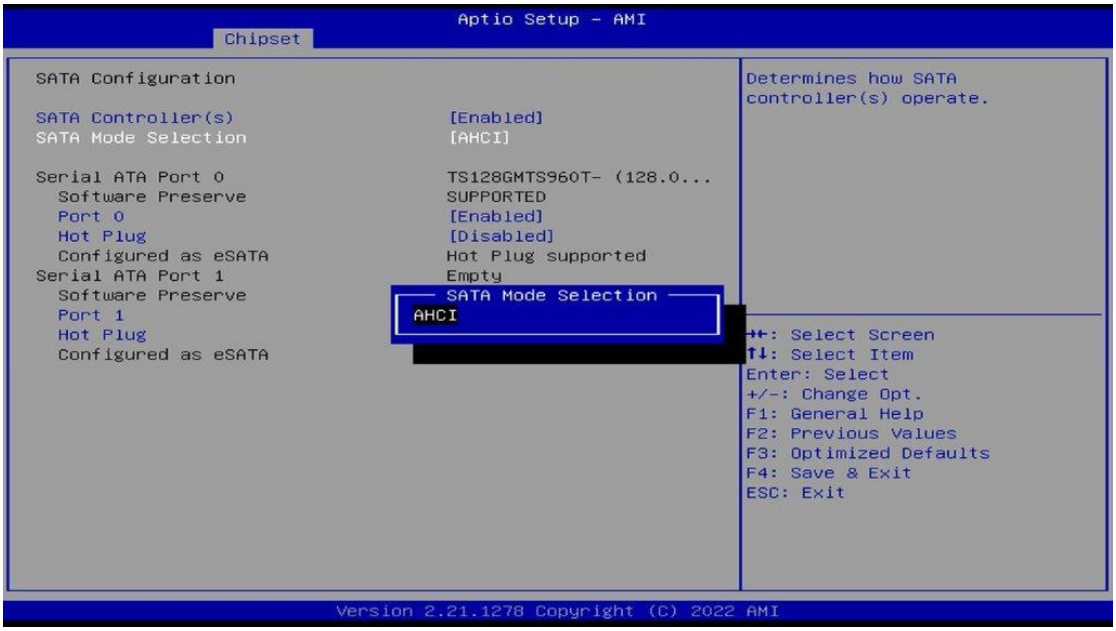


BIOS Setting	Description
Primary Display	Select which of IGFX/PEG/PCI Graphics device should be primary display or select SG for switchable Gfx. Options: Auto, IGFX, PEG, PCI, SG
Select PCIE Card	Selects the card used on the platform. **Auto** skips GPIO based Power Enable to dGPU. **E1k Creek 4:** DGPU Power Enable = Active Low. **PEG Eva1:** DGPU Power Enable = Active High.
Internal Graphics	Keep IGFX enabled based on the setup options. Options: Auto, Disabled, Enabled
GTT Size	Sets the GTT size as 2 MB, 4 MB, or 8 MB.
Aperture Size	Sets the aperture size as 128 MB, 256 MB, 512 MB, 1024 MB or 2048 MB. **Note:** Above 4GB MMIO BIOS assignment is automatically enabled when selecting 2048 MB aperture. To use this feature, disable CSM support.

4.5.2 PCH-IO Configuration

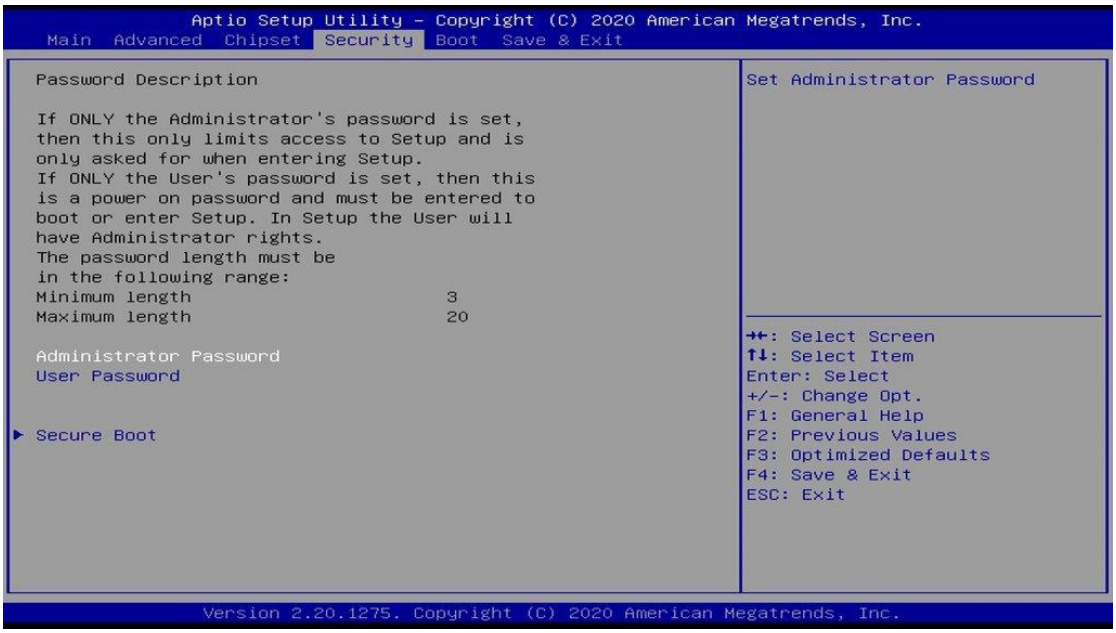
BIOS Setting	Description
SATA and RST Configuration	Configures SATA devices.
PCH LAN Controller	Enables / Disables the onboard NIC.
Wake on LAN Enable	Enables / Disables the integrated LAN to wake up the system.
PS_ON Enable	Enables / Disables PS_ON support a new C10 state from the CPU on desktop SKUs that enables a lower power target that will be required by the California Energy commission (CEC).

4.5.2.1. SATA and RST Configuration:



BIOS Setting	Description
SATA Controller(s)	Enables / Disables the SATA device.
SATA Mode Selection	Determines how SATA controller(s) operate. Options: AHCI / Intel RST Premium
Serial ATA Ports	Enables / Disables serial ports.
SATA Ports Hot Plug	Enables / Disables SATA Ports HotPlug.

4.6 Security Settings

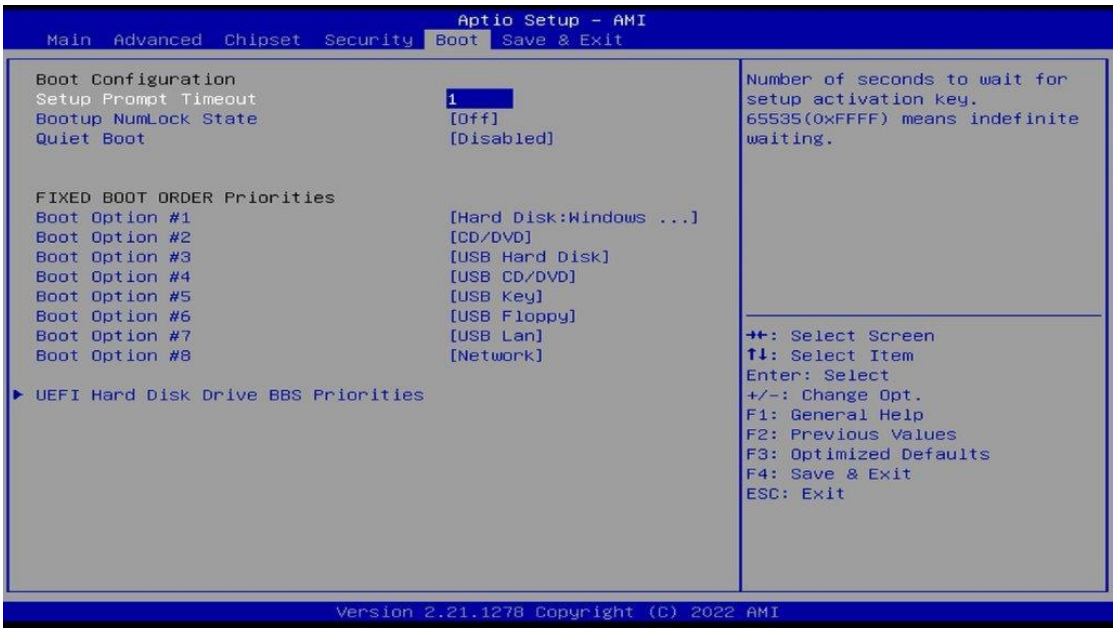


BIOS Setting	Description
Administrator Password	Sets an administrator password for the setup utility.
User Password	Sets a user password.
Secure Boot	Configures Secure Boot.

4.6.1 Secure Boot

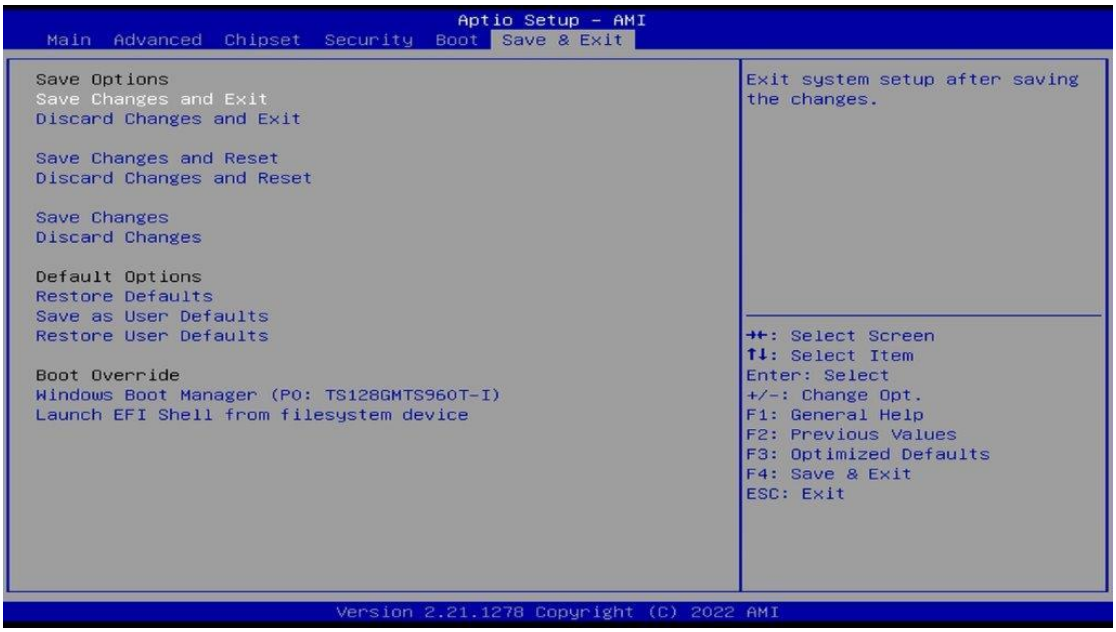
BIOS Setting	Description
Secure Boot	Secure Boot feature is Active if Secure Boot is enabled. Platform Key (PK) Is enrolled and the system is in User mode. The mode change requires platform reset.
Secure Boot Mode	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.
Restore Factory Keys	Forces system to user mode. Install factory default Secure Boot key databases.
Key Management	Enables expert users to modify Secure Boot Policy variables without full authentication.

4.7 Boot Settings



BIOS Setting	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	Selects the keyboard NumLock state.
Quiet Boot	Enables / Disables Quiet Boot option.
Boot mode select	Selects a Boot mode, Legacy / UEFI.
Boot Option Priorities	Sets the system boot order.

4.8 Save & Exit Settings



BIOS Setting	Description
Save Changes and Exit	Exits system setup after saving the changes.
Discard Changes and Exit	Exits system setup without saving any changes.
Save Changes and Reset	Resets the system after saving the changes.
Discard Changes and Reset	Resets system setup without saving any changes.
Save Changes	Saves changes done so far to any of the setup options.
Discard Changes	Discards changes done so far to any of the setup options.
Restore Defaults	Restores / Loads defaults values for all the setup options.
Save as User Defaults	Saves the changes done so far as User Defaults.
Restore User Defaults	Restores the user defaults to all the setup options.

Appendix

This section provides the mapping addresses of peripheral devices and the sample code of watchdog timer configuration.

- I/O Port Address Map
- Interrupt Request Lines (IRQ)

A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. The following table lists the I/O port addresses used.

Address	Device Description
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x0000002E-0x0000002F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000B2-0x000000B3	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x0000164E-0x0000164F	Motherboard resources
0x000003F8-0x000003FF	Communications Port (COM1)
0x00001800-0x000018FE	Motherboard resources
0x00003090-0x00003097	Standard SATA AHCI Controller
0x00003080-0x00003083	Standard SATA AHCI Controller
0x00003060-0x0000307F	Standard SATA AHCI Controller
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller

Address	Device Description
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x00001854-0x00001857	Motherboard resources
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000D00-0x0000FFFF	PCI Express Root Complex
0x0000EFA0-0x0000EFBF	Intel(R) SMBus Controller - 4B23
0x00002000-0x000020FE	Motherboard resources
0x00003000-0x0000303F	Intel(R) UHD Graphics
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer

B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
IRQ 16	High Definition Audio Controller
IRQ 4294967290	Intel(R) Ethernet Controller I226-V
IRQ 4294967289	Intel(R) Ethernet Controller I226-V
IRQ 4294967288	Intel(R) Ethernet Controller I226-V
IRQ 4	Communications Port (COM1)
IRQ 4294967294	Standard SATA AHCI Controller
IRQ 4294967293	Intel(R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
IRQ 55~204	Microsoft ACPI-Compliant System
IRQ 256~511	Microsoft ACPI-Compliant System
IRQ 4294967291	Intel(R) Management Engine Interface #1
IRQ 4294967292	Intel(R) UHD Graphics
IRQ 0	System timer

C. Watchdog Timer

The Watchdog Timer (WDT) is used to generate a variety of output signals after a user programmable count. The WDT is suitable for the use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sorts of circumstances, the timer will count to zero and the selected outputs will be driven. Under normal circumstance, you will need to restart the WDT at regular intervals before the timer counts to zero.

Sample Code:

```
//---------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//---------------------------------------------------------------------------
#include <dos.h>
#include <conio.h>
#include <stdio.h>
#include <stdlib.h>
#include "F81804.H"
//---------------------------------------------------------------------------
int main (int argc, char *argv[]);
void EnableWDT(int);
void DisableWDT(void);
//---------------------------------------------------------------------------
int main (int argc, char *argv[])
{
        unsigned char bBuf;
        unsigned char bTime;
        char **endptr;

        char SIO;

        printf("Fintek 81804 watch dog program\n");
        SIO = Init_F81804();
        if (SIO == 0)
        {
                printf("Can not detect Fintek 81804, program abort.\n");
                return(1);
        }//if (SIO == 0)

        if (argc != 2)
        {
                printf(" Parameter incorrect!!\n");
                return (1);
        }

        bTime = strtol (argv[1], endptr, 10);
```

```
        printf("System will reset after %d seconds\n", bTime);

        if (bTime)
        {  EnableWDT(bTime); }
        else
        {  DisableWDT();  }
        return 0;
}
//----------------------------------------------------------------------
void EnableWDT(int interval)
{
        unsigned char bBuf;

        bBuf = Get_F81804_Reg(0x2B);
        bBuf &= (~0x20);
        Set_F81804_Reg(0x2B,    bBuf);  //Enable WDTO

        Set_F81804_LD(0x07);        //switch to logic device 7
        Set_F81804_Reg(0x30,    0x01);  //enable timer

        bBuf = Get_F81804_Reg(0xF5);
        bBuf &= (~0x0F);
        bBuf |= 0x52;
        Set_F81804_Reg(0xF5,    bBuf);  //count mode is second

        Set_F81804_Reg(0xF6,    interval);      //set timer

        bBuf = Get_F81804_Reg(0xFA);
        bBuf |= 0x01;
        Set_F81804_Reg(0xFA,    bBuf);  //enable WDTO output

        bBuf = Get_F81804_Reg(0xF5);
        bBuf |= 0x20;
        Set_F81804_Reg(0xF5,    bBuf);  //start counting
}
//----------------------------------------------------------------------
void DisableWDT(void)
{
        unsigned char bBuf;

        Set_F81804_LD(0x07);        //switch to logic device 7

        bBuf = Get_F81804_Reg(0xFA);
        bBuf &= ~0x01;
        Set_F81804_Reg(0xFA,    bBuf);  //disable WDTO output

        bBuf = Get_F81804_Reg(0xF5);
        bBuf &= ~0x20;
        bBuf |= 0x40;
        Set_F81804_Reg(0xF5,    bBuf);  //disable WDT
}
//----------------------------------------------------------------------
```

```
//---------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//---------------------------------------------------------------------------
#include "F81804.H"
#include <dos.h>
//---------------------------------------------------------------------------
unsigned int F81804_BASE;
void Unlock_F81804 (void);
void Lock_F81804 (void);
//---------------------------------------------------------------------------
unsigned int Init_F81804(void)
{
        unsigned int result;
        unsigned char ucDid;

        F81804_BASE = 0x4E;
        result = F81804_BASE;

        ucDid = Get_F81804_Reg(0x20);
        if   (ucDid == 0x07)            //Fintek 81804
        { goto     Init_Finish;    }

        F81804_BASE = 0x2E;
        result = F81804_BASE;

        ucDid = Get_F81804_Reg(0x20);
        if   (ucDid == 0x07)            //Fintek 81804
        { goto     Init_Finish;    }

        F81804_BASE = 0x00;
        result = F81804_BASE;

Init_Finish:
        return (result);
}
//---------------------------------------------------------------------------
void Unlock_F81804 (void)
{
        outportb(F81804_INDEX_PORT, F81804_UNLOCK);
        outportb(F81804_INDEX_PORT, F81804_UNLOCK);
}
//---------------------------------------------------------------------------
void Lock_F81804 (void)
{
        outportb(F81804_INDEX_PORT, F81804_LOCK);
}
//---------------------------------------------------------------------------
void Set_F81804_LD( unsigned char LD)
{
```

```
        Unlock_F81804();
        outportb(F81804_INDEX_PORT, F81804_REG_LD);
        outportb(F81804_DATA_PORT, LD);
        Lock_F81804();
}
//-----------------------------------------------------------------------
void Set_F81804_Reg( unsigned char REG, unsigned char DATA)
{
        Unlock_F81804();
        outportb(F81804_INDEX_PORT, REG);
        outportb(F81804_DATA_PORT, DATA);
        Lock_F81804();
}
//-----------------------------------------------------------------------
unsigned char Get_F81804_Reg(unsigned char REG)
{
        unsigned char Result;
        Unlock_F81804();
        outportb(F81804_INDEX_PORT, REG);
        Result = inportb(F81804_DATA_PORT);
        Lock_F81804();
        return Result;
}
//-----------------------------------------------------------------------
```

```
//---------------------------------------------------------------------------
//
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//
//---------------------------------------------------------------------------
#ifndef       F81804_H
#define       F81804_H              1
//---------------------------------------------------------------------------
#define     F81804_INDEX_PORT      (F81804_BASE)
#define     F81804_DATA_PORT       (F81804_BASE+1)
//---------------------------------------------------------------------------
#define     F81804_REG_LD      0x07
//---------------------------------------------------------------------------
#define     F81804_UNLOCK      0x87
#define     F81804_LOCK  0xAA
//---------------------------------------------------------------------------
unsigned int Init_F81804(void);
void Set_F81804_LD( unsigned char);
void Set_F81804_Reg( unsigned char,
unsigned char); unsigned char
Get_F81804_Reg( unsigned char);
//---------------------------------------------------------------------------
#endif    //      F81804_H
```