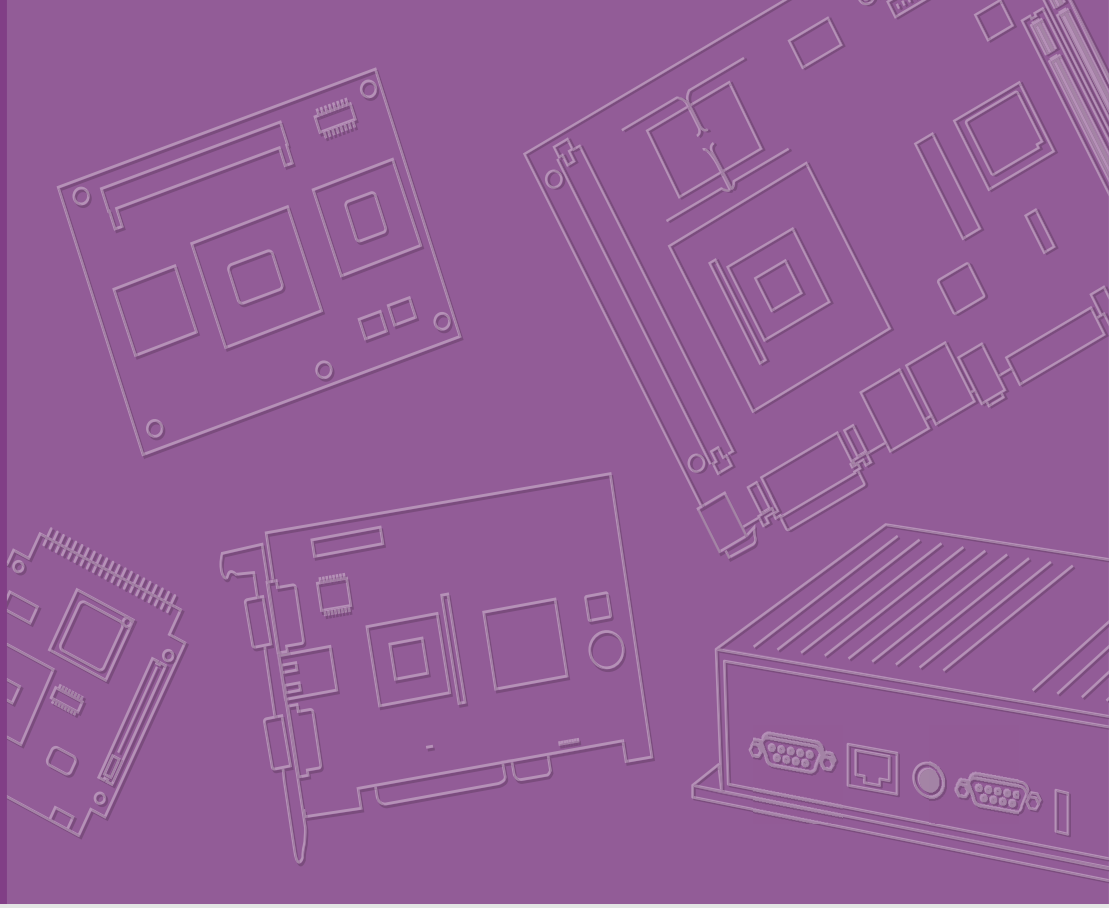

User Manual

ASMB-817 Series

Single LGA4677 4th/5th Gen Intel® Xeon® Scalable ATX Server Board with 8 DDR5, 3 PCIe x16, 8 SATA 3.0, 4 USB 3.2 (Gen1), Dual 10GbE, and IPMI

ADVANTECH

Enabling an Intelligent Planet

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Part No. 2002092700 Edition 1

May 2025

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FCC Class B

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation.

This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for assistance.

Initial Inspection

Before installing motherboard, please make sure that the following materials have been shipped:

- 1 x ASMB-817 Startup Manual
- 2 x SATA data cables
- 2 x SATA power cables
- 2 x CPU power cables (8P)
- 2 x CPU carrier (E1A and E1B)
- 1 x I/O shield
- 1 x M.2 screw
- 1 x front panel convert cable

If any of these items are missing or damaged, contact distributor or sales representative immediately. We have carefully inspected the ASMB-817 mechanically and electrically before shipment. It should be free of marks and scratches and in perfect working order upon receipt. When unpacking the ASMB-817, check it for signs of shipping damage. (For example, damaged box, scratches, dents, etc.) If it is damaged or it fails to meet the specifications, notify our service department or local sales representative immediately. Also notify the carrier. Retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

Order Information

Part Number	Chipset	Memory	GbE/10GbE LAN	IPMI	Graphics
ASMB-817I-00A1	C741	8 x DDR5 RDIMM	-/2	Yes	AST2600
ASMB-817T2-00A1	C741	8 x DDR5 RDIMM	2/2	Yes	AST2600

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Chapter 1

Overview

1.1 Introduction

The ASMB-817 serverboard is the most advanced 4th/5th Gen Intel Xeon Scalable series board for industrial and medical equipment, as well as HPC applications that require high-performance computing power and multi-expansion slots. This serverboard supports the 4th/5th Gen Intel Xeon Scalable series processor and DDR5 RDIMM 5200MHz memory up to 2TB. ASMB-817 provides three PCIex16 slots, two PCIe x8 slots and one PCIe x4 slot in Gen5 high speed and one PCIe x1 slot in Gen3 speed. All of the PCIe x16 slots support Compute Express Link (CXL) as well. Additionally, the ASMB-817 features dual 10GbE Ethernet LAN ports (T2 SKU only) that eliminate network bottlenecks.

Utilizing the Intel C741 chipset, the ASMB-817 boasts a variety of high-end features. With 4 USB3.2 Gen1 and 9 USB 2.0 ports, 8 onboard SATA III connectors, and 1 M.2 2280 connector, this board offers unparalleled I/O capabilities. Furthermore, it supports software RAID 0, 1, 5, and 10, thereby ensuring superior data storage and high-speed peripheral connectivity. These advanced features make the ASMB-817 the ideal choice for industrial and medical equipment, as well as HPC applications that require top-tier performance and reliability.

1.2 Features

General

- **Intel Xeon Processor Scalable Family support:** ASMB-817 is equipped with single CPU socket, which enables it to support the 4th/5th Gen Intel Xeon Scalable series processors.
- **High performance I/O capability:** 2 x 10GbE (T2 SKU), 2 x GbE, 3 x PCIe x16 slot + 2 x PCIe x8 slot + 1 x PCIe x4 slot + 1 x PCIe x1 slot, 8 x SATA and 1 x M.2 connector, 4 x USB3.2 Gen1 and 9 x USB 2.0 ports.
- **Standard ATX form factor with industrial features:** ASMB-817 provides industrial features like long product lifecycle, watchdog timer, reliable operation under wide temperature ranges, etc.
- **IPMI 2.0 support:** ASMB-817 equipped with AST2600 BMC chip and supports IPMI 2.0 (Intelligent Platform Management Interface 2.0) via dedicated and shared LAN port.
- **KVM over IP:** KVM over IP function allows BIOS level remote control of ASMB-817 through your own computer.

1.3 Specifications

Table 1.1: Specifications

Processor	
CPU	■ Single Intel LGA4677 Xeon processor socket ■ Supports Intel 4th/5th Gen Xeon Scalable family, up to 60 cores ■ Supports the TDP of processor up to 225W (consider extended air thermal solution while using CPU> 205W TDP)
System Memory	
Total slots	8 (1 DIMM per channel)
Capacity	Maximum 2TB ECC RDIMM
Memory type	DDR5 ECC-REG 4400/4800/5200 MHz
Memory size	Each memory slot supports 16GB, 32GB, 64GB, 128GB and 256GB memory module.
Memory Voltage	1.2 V
Error Detection	■ Corrects single-bit errors ■ Detects double-bit errors (using ECC memory)
On-Board Devices	
Chipsets	Intel C741 PCH
Network Controllers	Intel X710, support dual 10GbE ports (T2 SKU); dual Intel I210 support dual GbE ports
VGA	Aspeed AST2600 with 64 MB VGA memory provides basic 2D VGA function.
EC	ITE IT5121VG provide motherboard keyboard mouse, RS-232, and hardware monitor functions.
BMC	Realtek RTL8211 for dedicated BMC LAN
Input/Output	
Storage	■ 8 x SATA3.0, support software RAID 0,1,5,10 ■ 1 x M.2 2280/22110 (SATA or PCIe).
LAN	■ 4 x RJ-45 LAN ports (2 x 10GbE LAN(T2 SKU only) + 2 x GbE LAN) ■ 1 x RJ-45 Dedicated IPMI LAN port (10/100/1000 Base-T) for IPMI only - there is no regular LAN function.
USB	■ 4 x USB3.2 Gen1 (2 Rear/2 onboard) ■ 9 x USB2.0 (4 on board pin header / 4 rear / 1 Type A).
Graphic	1 x VGA
Keyboard/Mouse	PS/2 keyboard and mouse via internal header
Serial port	1 x RS232 port at rear window, 1 x internal header (2 x 5P pitch: 2.50 mm), both ports are RS-232 (5V).
Power Connector	
System Power	1 x 24-pin SSI EPS 12V power connector (Input 12V, 5V, 3.3V, 5Vsb).
CPU Power	2 x 8-pin SSI EPS 12V power connector for CPU & Memory power (12V).
PCIe slot power	1 x 4-pin 12V power connector for PCIe slot 12V input.

Table 1.1: Specifications	
Expansion Slots	
PCI-express	■ 3 x PCIe Gen5 X16 slots – Slot 1: PCIe X16 slot, signal from CPU, CXL support – Slot 2: PCIe X16 slot, signal from CPU, CXL support – Slot 4: PCIe X16 slot, signal from CPU, CXL support ■ 2 x PCIe Gen5 X8 slots – Slot 5: PCIe X8 slot, signal from CPU – Slot 7: PCIe X8 slot, signal from CPU ■ 1 x PCIe Gen5 X4 slot – Slot 3: PCIe X4 slot, signal from CPU ■ 1 x PCIe Gen3 X1 slot – Slot 1: PCIe X1 slot, signal from PCH
System BIOS	
BIOS Type	256 Mb SPI Flash EEPROM with AMI BIOS
PC Health Monitoring	
Voltage	Monitors for CPU Cores, +3.3V, +5V, +12V, +5VSB, VBAT
FAN	■ One pin headers for CPU cooler ■ 5 x 4-pin headers for system fans ■ All fans with tachometer status monitoring ■ Thermal control for all fan connectors
Temperature	■ Monitoring for CPU ■ Monitoring for system external thermal sensor
Other Features (Case Open)	■ Chassis intrusion detection
Operating Environment/Compliance	
RoHS	RoHS Directive 2011/65/EU and (EU) 2015/863
Environmental Spec.	■ Operating Temperature: 0 to 60°C ■ Non-operating Temperature: -40 to 85°C ■ Operating Relative Humidity: 10% to 90% (non-condensing) ■ Non-operating Relative Humidity: 10% to 95% (non-condensing)

1.4 Board Layout, Jumpers and Connectors

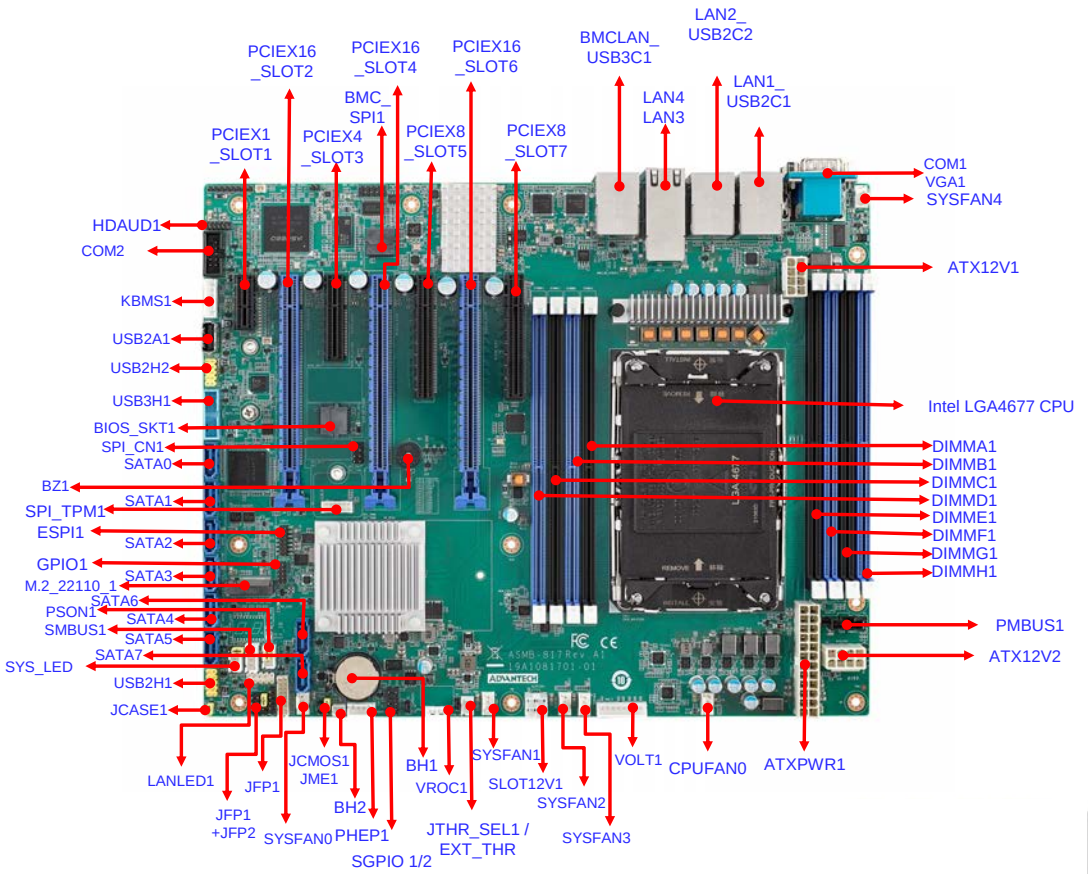


Figure 1.1 Board Layout

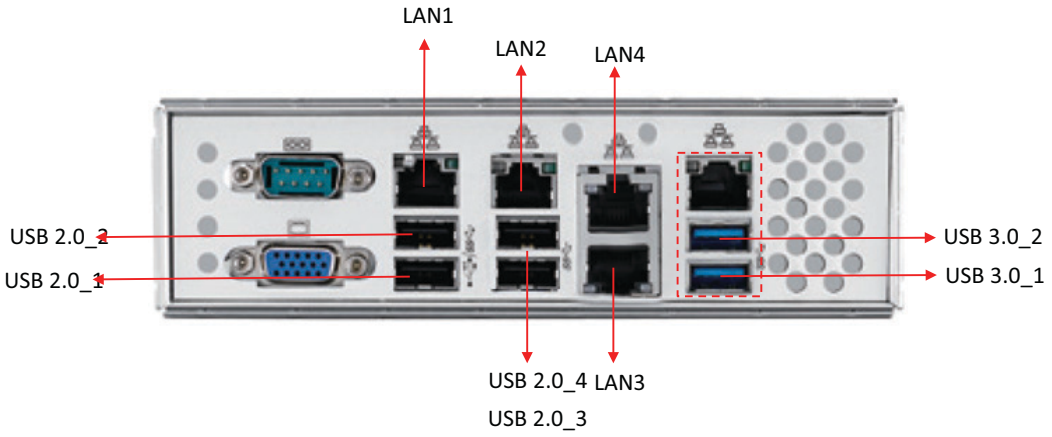


Figure 1.2 Rear I/O of T2 SKU (ASMB-817T2-00A1)

Table 1.2: Onboard LAN LED Color Definition

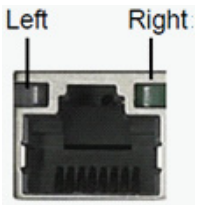
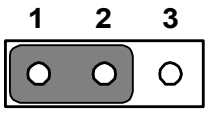
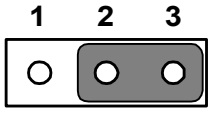
100 Mbps/1 Gbps/10 Gbps LAN Link/Activity LED Scheme			
		LAN3 & LAN4	
		Left LED	Right LED
100 Mbps	Link Active		
1 Gbps	Link Active	Amber Amber	Green Blinking green
10 Gbps	Link Active	Green Green	Green Blinking green

Table 1.3: Jumpers

Label	Function	Default
JCASE1	Chassis case open alarm	1-2
JCMOS1	CMOS clear	1-2
JME1	ME update	1-2
PSON1	AT(1-2)/ATX(2-3)	2-3
JTHR_SEL	Internal (1-2) and external (2-3) thermistor selection	1-2
JWDT1	Watchdog reset	1-2



Keep CMOS data/
Disable ME update/



Clear CMOS data/
Enable ME update/

Table 1.4: Connectors

Label	Function
ATXPWR1	ATX 24-pin main power connector
ATX12V1	Processor power connector (mandatory)
ATX12V2	Processor power connector (reserved)
BH2	For optional battery kit
BIOS_SKT1	BIOS SPI ROM
BMC_SPI1	BMC SPI ROM
BMCLAN_USB3C1	IPMI dedicated LAN connector and USB 3.2 Gen1 dual ports
COM1	RS-232 connector
COM2	RS-232 header
CPUFAN0	CPU FAN connector
DIMMA1, DIMMB1, DIMMC1, DIMMD1, DIMME1, DIMMF1, DIMMG1, DIMMH1	DDR5 from CPU
ESPI1	eSPI connector
EX_THR1	Connector for external thermistor
GPIO1	GPIO connector
HDAUD1	Audio header
JFP1, JFP1+JPF2	Front panel header
KBMS1	External keyboard and mouse connector
LAN2_USB2C1, LAN2_USB2C2	10 Gbps LAN connector and USB 2.0 ports
LANLED1	LAN LED extension connector
M2_22110	M.2 connector (SATA & PCIe x4)
PCIEX1_SLOT1	PCIe x1 slot (PCH)
PCIEX16_SLOT2	PCIe x16 slot (CPU)
PCIEX4_SLOT3	PCIe x8 slot (CPU)
PCIEX16_SLOT4	PCIe x16 slot (CPU)
PCIEX8_SLOT5	PCIe x8 slot (CPU)
PCIEX16_SLOT6	PCIe x16 slot (CPU)
PEHP1	NVMe RAID LED control
PMBUS1	PMBUS connector to communicate with the power supply
SATA0~SATA7	SATA3 connectors
SGPIO1/2	SATA0~7 SGPIO header
SLOT12V1	For PCIe slot 12V input only
SMBUS1	SMBus header
SYSFAN0~SYSFAN4	System FAN connector
SYS_LED1	System LED connector
USB2C1, USB2C2	USB2.0 port 1,2 (Rear I/O); USB2.0 port 3,4 (Rear I/O)
USB2H1, USB2H2	USB 2.0 port (9-pin header)
USB2A1	USB 2.0 port (Type-A)
USB3C1, USB3H1	USB 3.2 port 1, 2(Rear I/O); USB 3.2 port (20-pin header)
VGA1	VGA connector

Table 1.5: Onboard LED			
LED	**Description**	**LED Definition**	
+5V_LED1	Power on LED	Off: Power off	On (Green): System on
+5V_SB_LED1	Standby LED	Off: No input AC Power	On (Green): System on, in sleep mode, or in soft-off mode
BMC_HBLED1	BMC heartbeat LED	Blinking (Green): Controller is working normally	

1.5 Block Diagram

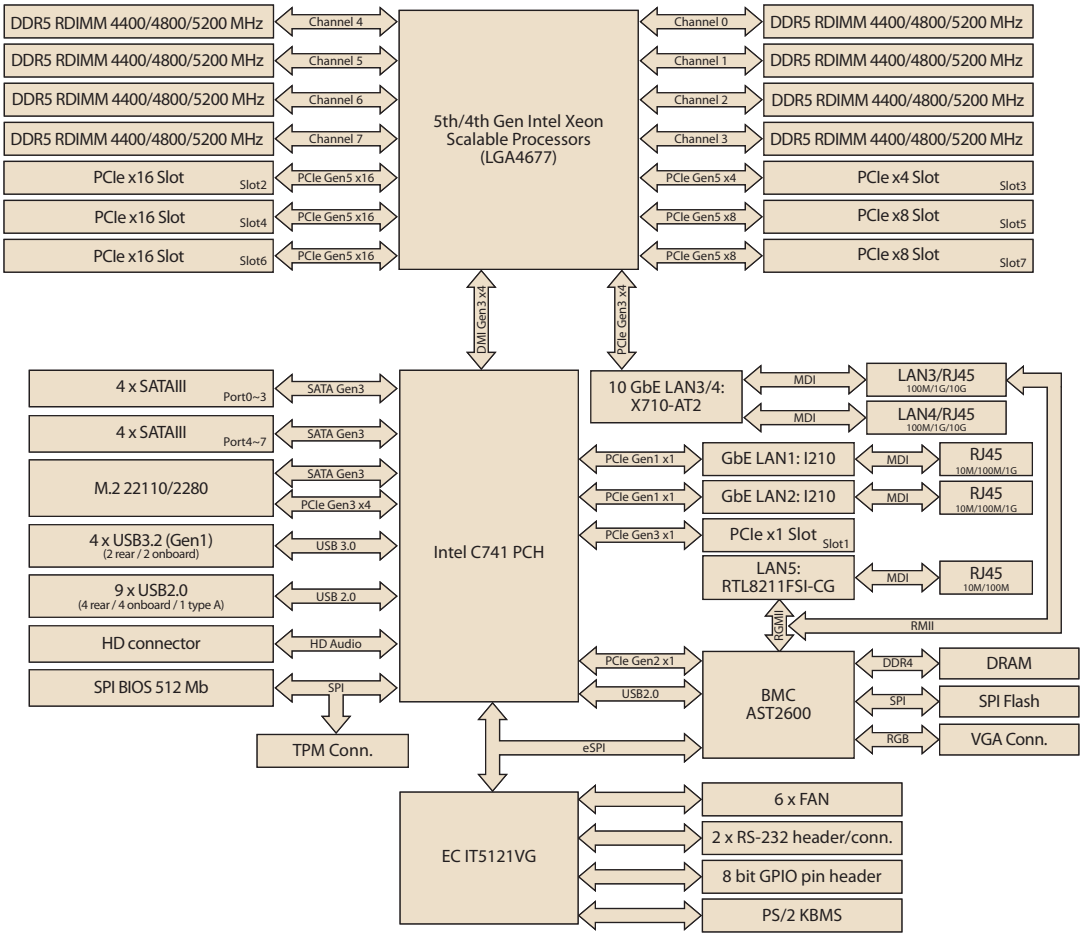


Figure 1.3 Block Diagram

1.6 System Memory

ASMB-817 has eight 288-pin memory slots for DDR5 4400/4800/5200 MHz memory modules with maximum capacity of 2TB (Maximum 256GB for each DIMM). ASMB-817 supports registered DIMM memory modules.

1.7 Memory Installation

Memory performance is affected by different DIMM configurations. To reach optimal memory interleaving, be sure to install identical DIMM types with the same size, speed, and number of ranks on those memory slots corresponding to the correct processor.

The following table indicates recommended DIMM configurations with a single and dual processor, based on the guideline, you may adjust your memory configuration according to your PCIe expansion card configuration.

Table 1.6: DIMM Configuration with Single CPU

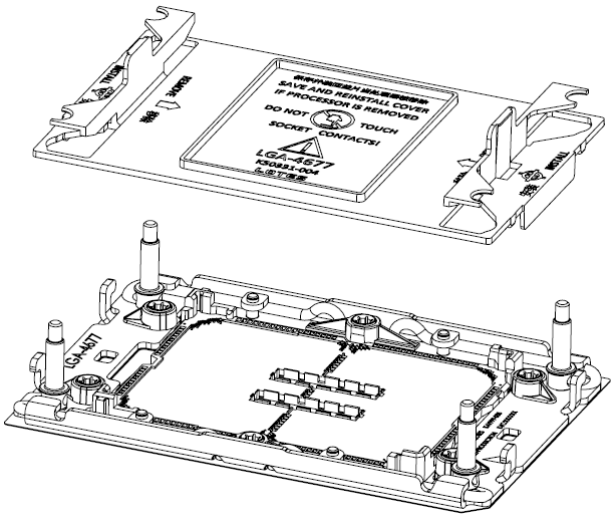
Channel		DIMMA1	DIMMB1	DIMMC1	DIMMD1	DIMME1	DIMMF1	DIMMG1	DIMMH1
Quantity of memory installed	1	v							
			v						
						v			
							v		
	2	v						v	
				v		v			
	4	v		v		v		v	
	6	v		v	v	v	v	v	
		v	v	v		v		v	v
			v	v	v	v	v		v
		v	v		v		v	v	v
	8	v	v	v	v	v	v	v	v

Note! *3, 5, 7 DIMMs are not recommended DIMM population.*

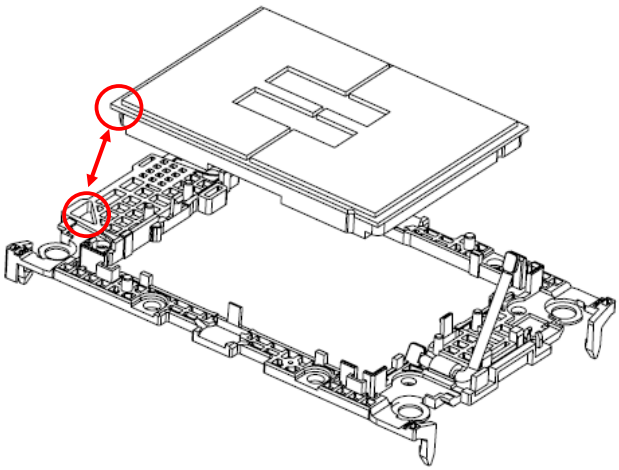
1.8 Processor Installation

The ASMB-817 is designed for Intel Xeon processor scalable family.

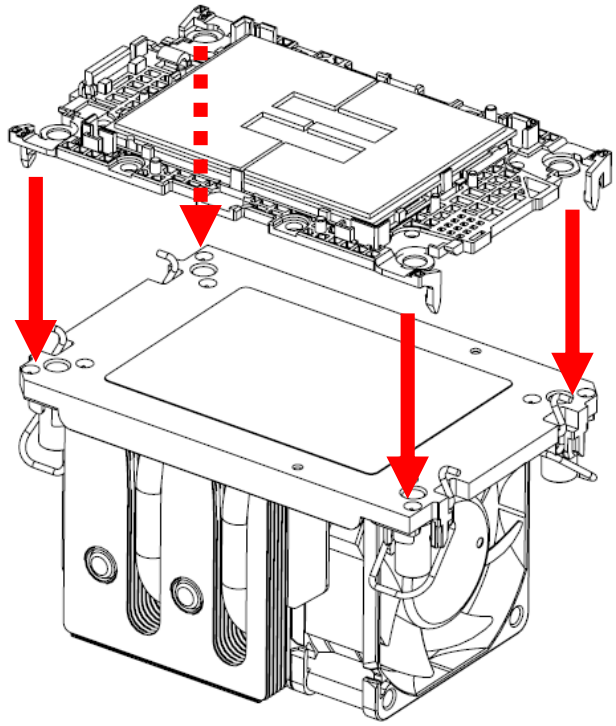
1. Remove dust cover.



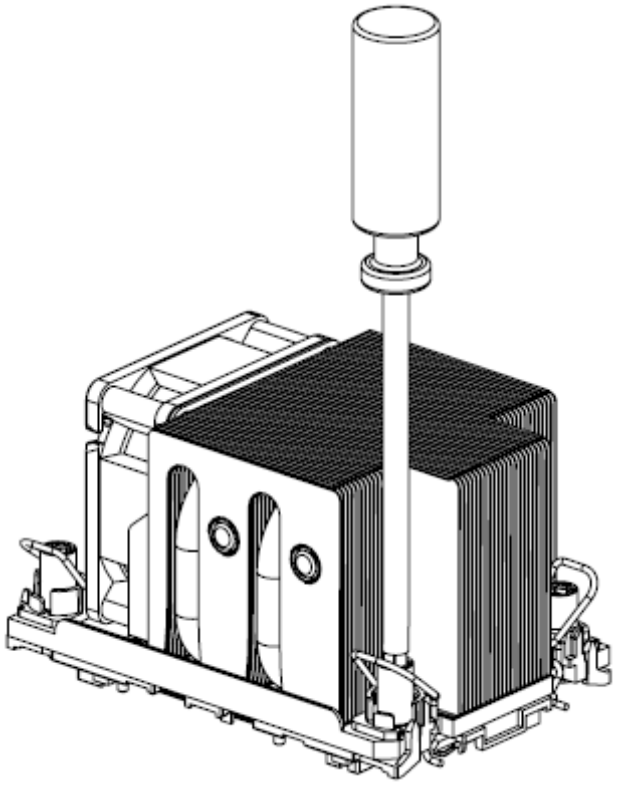
2. Install CPU on CPU clip and align pin 1 mark.



3. Install the CPU clip assembly on the heatsink as a processor + heatsink module.



4. Put the processor heatsink module into the motherboard bolster plate by using a T-30 screw driver (follow heatsink label direction 1-2-3-4). For best durability, 8.0 in-lbf torque is recommended.



Chapter 2

Connections

2.1 Introduction

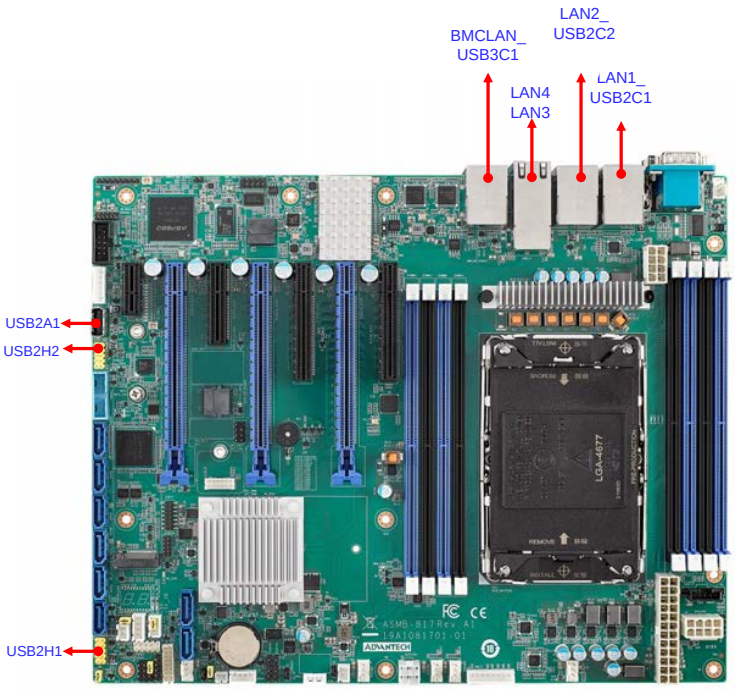
You can access most of the connectors from the top of the board as it is being installed in the chassis. If you have a number of cards installed, you may need to partially remove a card to make all the connections.

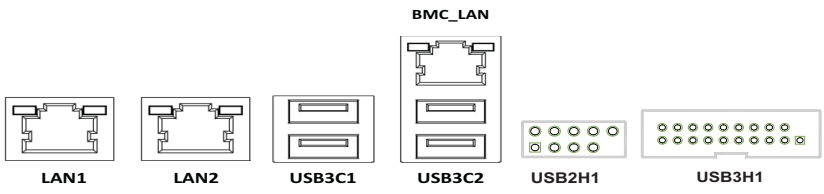
2.2 USB Ports and LAN Port

The USB ports comply with USB 2.0 and USB 3.2 Gen1. Transmission rates of up to 480 Mbps (USB 2.0)/5Gbps (USB 3.2 Gen1) and fuse protection are supported. The USB interface can be disabled in the system BIOS setup.

ASMB-817 is equipped with two 10Gbe (T2 version) and two GbE. They are all with RJ-45 jacks and supported by all major network operating systems. BMC_LAN is the dedicated BMC LAN, where LAN4 is the shared BMC LAN.

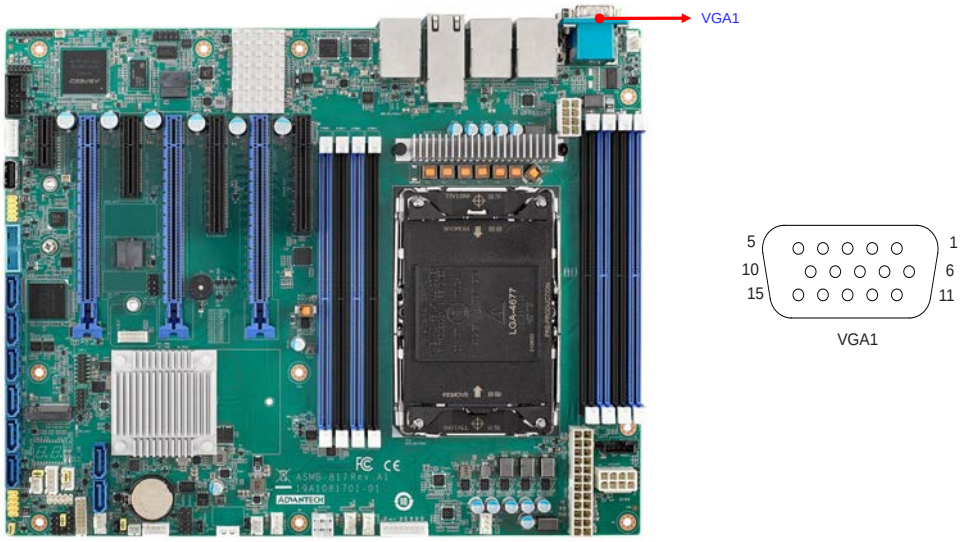
For the USB cable used by USB3H1 connector, please refer to Optional Accessories in the ASMB-817 datasheet.





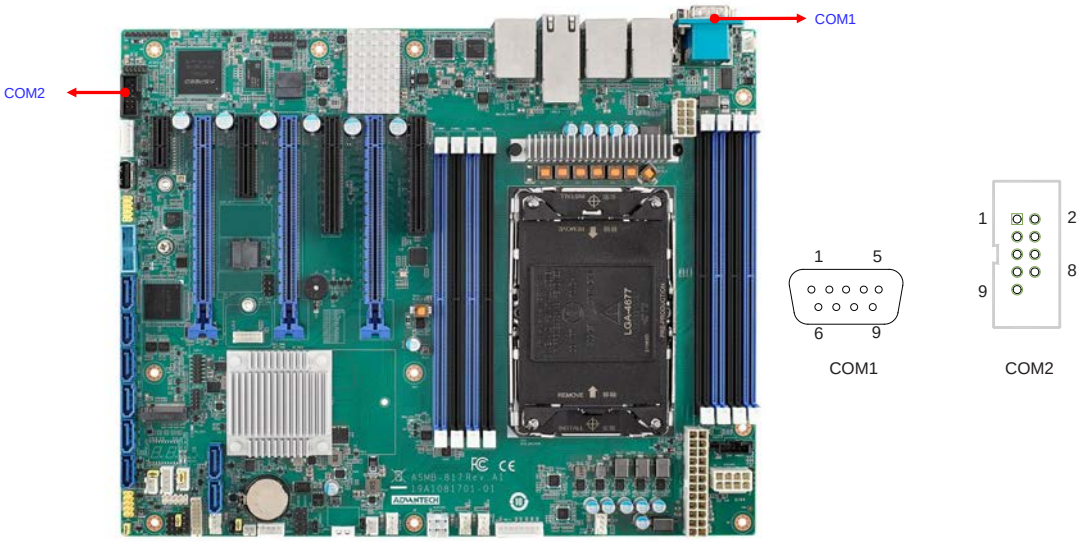
2.3 VGA Connector (VGA1)

The ASMB-817 includes a VGA interface that can drive conventional CRT and LCD displays.



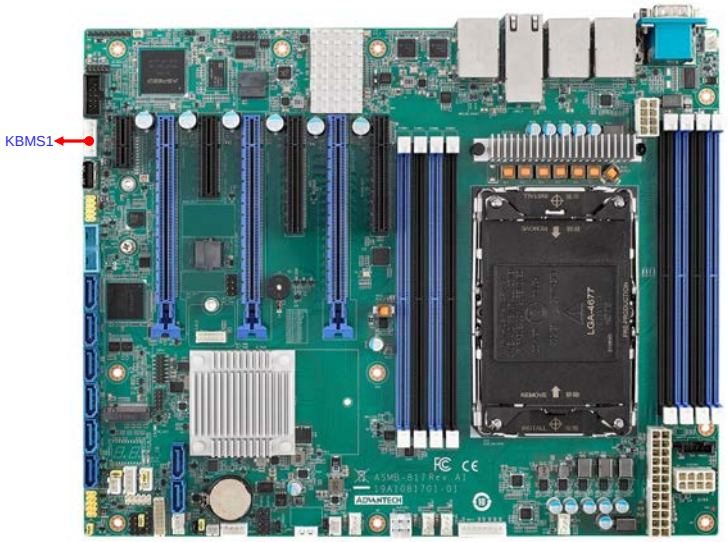
2.4 Serial Ports (COM1~2)

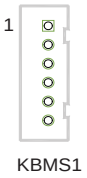
The ASMB-817 offers one serial port on the rear plate and one 2.50mm onboard with 2 x 5-pin pitch.



2.5 PS2 Keyboard and Mouse Connectors (KBMS1)

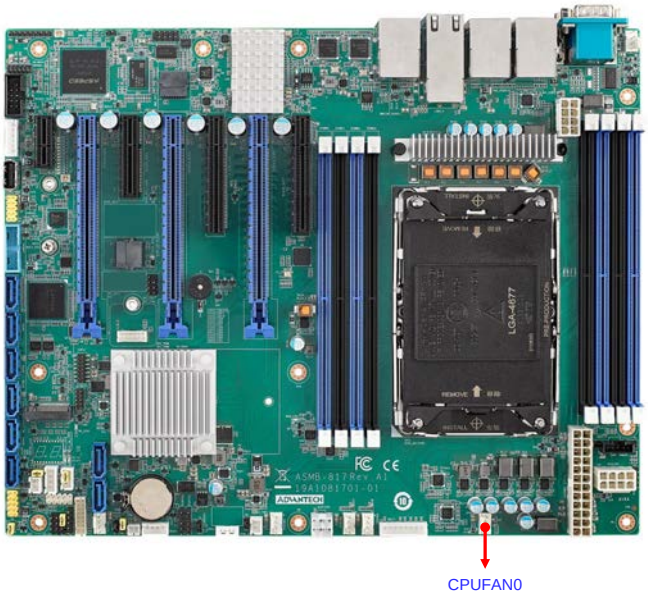
The 6-pin KBMS1 connector is for additional keyboard & mouse device usage.

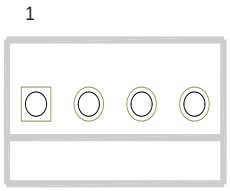




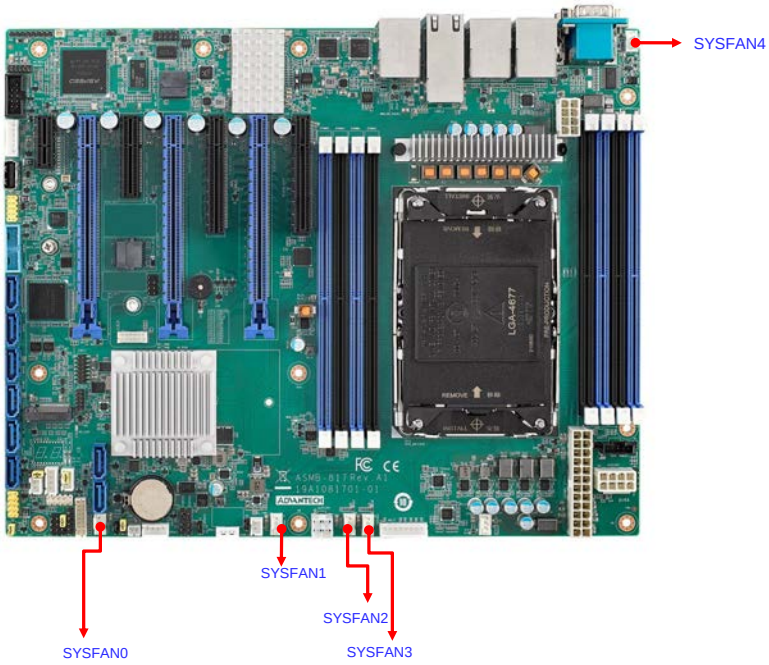
2.6 CPU Fan Connector (CPUFAN0)

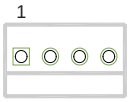
If a fan is used, this connector supports cooling fans that draw up to 1.5A (18W).





2.7 System Fan Connector (SYSFAN0~3, REAR_FAN)

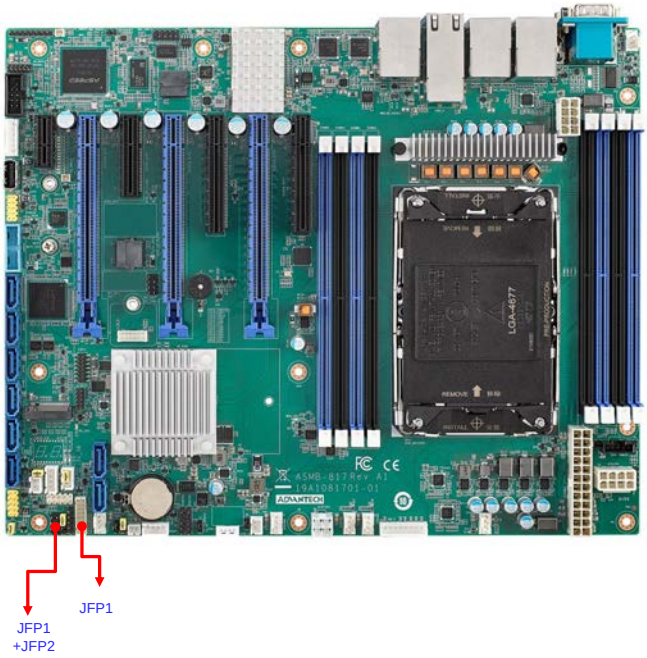


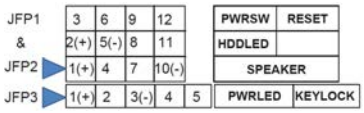


SYSFAN0~4

2.8 Front Panel Connector (JFP1+JFP2, JFP3)

There are several external switches and LEDs to monitor and control ASMB-817.





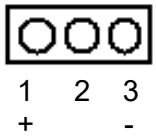
JFP1+JFP2
JFP3

2.8.1 Power LED (JFP3)

JFP3 pin 1 and pin 3 are for the power LED. Refer to Appendix B for detailed information on the pin assignments. If an ATX power supply is used, the system's power LED status will be as indicated.

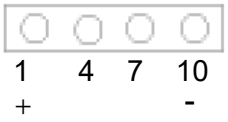
Table 2.1: ATX Power Supply LED Status

ACPI Power Mode	LED (ATX power)
System On (S0)	On
System Off (S5)	Off



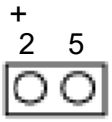
2.8.2 External Speaker (JFP1+JFP2 Pins 1, 4, 7, 10)

JFP2 pins 1, 4, 7, 10 connects to an external speaker. ASMB-817 provides an onboard buzzer as an alternative. To enable the buzzer, set pins 7-10 closed.



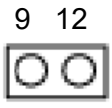
2.8.3 HDD LED Connector (JFP1+JFP2 Pin 2&5)

You can connect an LED to JFP1 to indicate when the HDD is active.



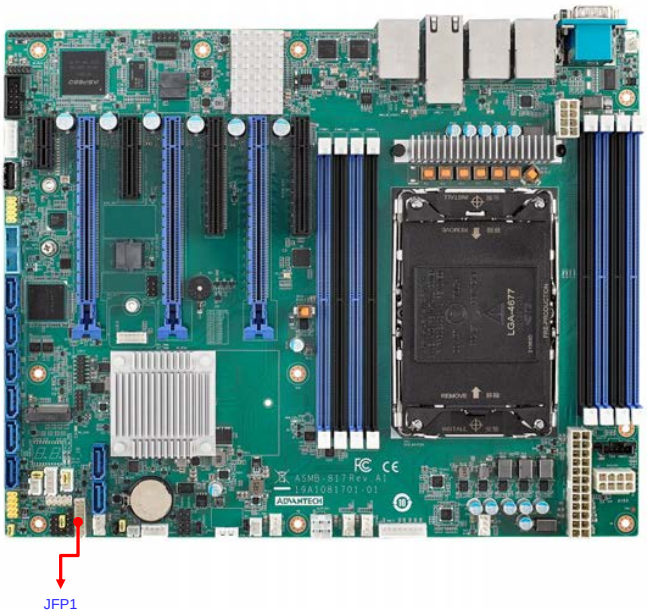
2.8.4 Reset Connector (JFP1+JFP2 Pin 9 & 12)

Many computer cases offer the convenience of a reset button.



2.9 Front Panel Connector (JFP1)

There are several external switches and LEDs to monitor and control the ASMB-817.



2.0 mm JPF on board			
Description	Pin Number		Description
RST BTN	2	▼1	PWR BTN
RST GND	4	3	PWR GND
LAN1_LED+	6	5	LAN2_LED+
LAN1_LED-	8	7	LAN2_LED-
CRPS Detect (Reserved)	10	9	SYS_LED+ (Reserved)
GND	12	11	SYS_LED- (Reserved)
PWR LED+	14	13	HDD_LED+
PWR LED-	16	15	HDD_LED-

2.0 mm JPF to 2.54 mm Pitch Header			
Description	Pin Number		Description
(Red) PWR BTN	▼1	2	RST BTN (White)
(Black) PWR GND	3	4	RST GND (Black)
(Blue) LAN1_LED+	5	6	LAN2_LED+ (Brown)
(Red) LAN1_LED-	7	8	LAN2_LED- (Black)
		Key	
(Orang) HDD_LED+	13	14	PWR LED+ (Red)
(Black) HDD_LED-	15		
	Key	16	PWR LED- (Black)

2.9.1 ATX Soft Power Switch (Pins 1, 3)

If your computer case is equipped with an ATX power supply, you should connect the power on/off button on your computer case to pins 1 and 3 on JFP1. This connection enables you to turn your computer on and off.

2.9.2 Reset Connector (Pins 2, 4)

JFP1 pins 2 & 4 are for a reset button.

2.9.3 Front Panel LAN Indicator Connector (Pins 5, 6, 7, 8)

You can connect an LED to connector JFP1 to indicate when LAN1 & LAN2 are active.

2.9.4 HDD LED Connector (Pins 13, 15)

You can connect an LED to connector JFP1 to indicate when the HDD is active.

2.9.5 Power LED (Pins 14, 16)

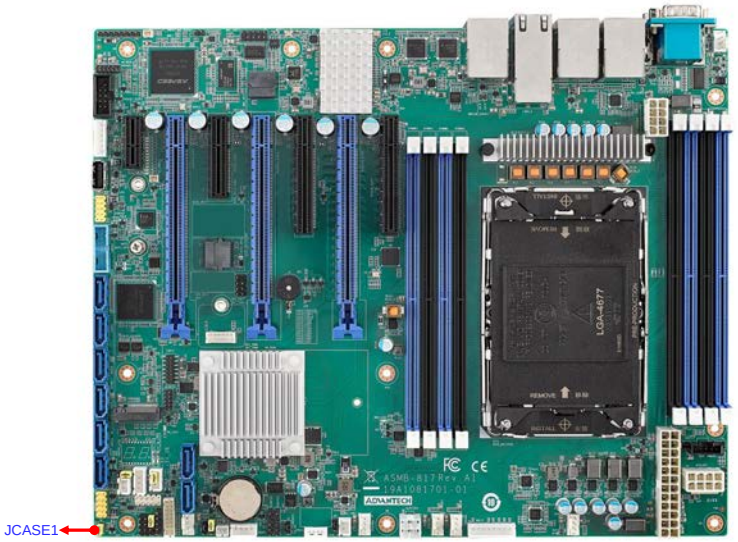
Refer to Appendix A for detailed information on the pin assignments. If an ATX power supply is used, the system's power LED status is as follows.

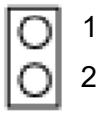
Table 2.2: System's power LED status

Power Mode	LED Status
System On	On
System Off	Off

2.10 Case Open (JCASE1)

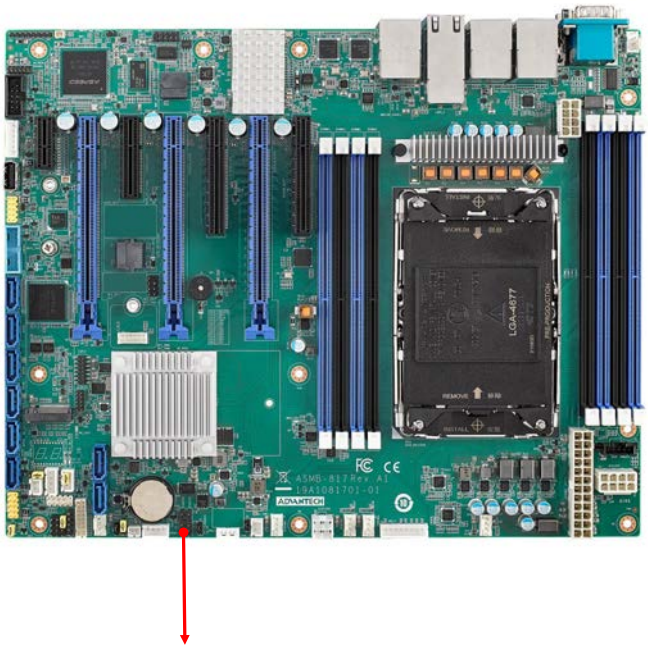
A Chassis Intrusion header is located at JCASE1 on the motherboard. Attach the appropriate cable from the chassis to be informed of a chassis intrusion when the chassis has been opened. The default function is disabled and Pin 1-2 is bridged by a jumper cap.

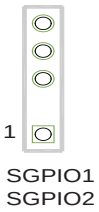




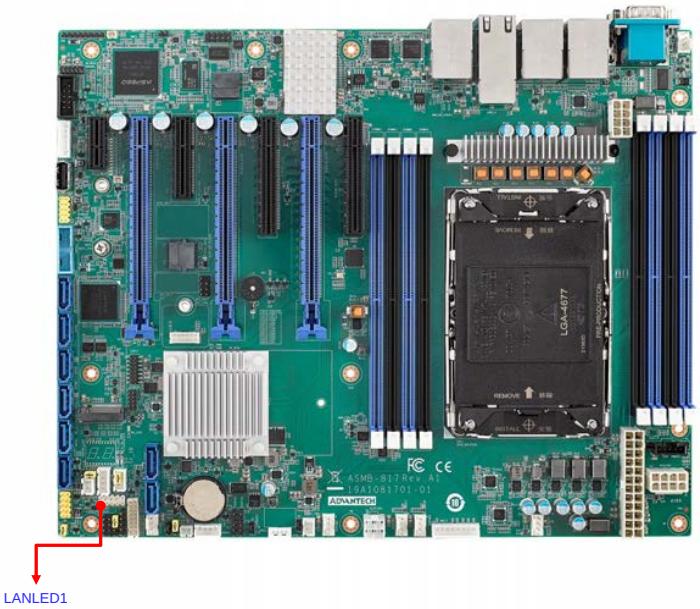
2.11 SATA SGPIO (SGPIO1/2)

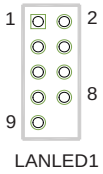
SGPIO1 and SGPIO2 are connected to the hard drive backplane for interpreting hard drive LED signals.





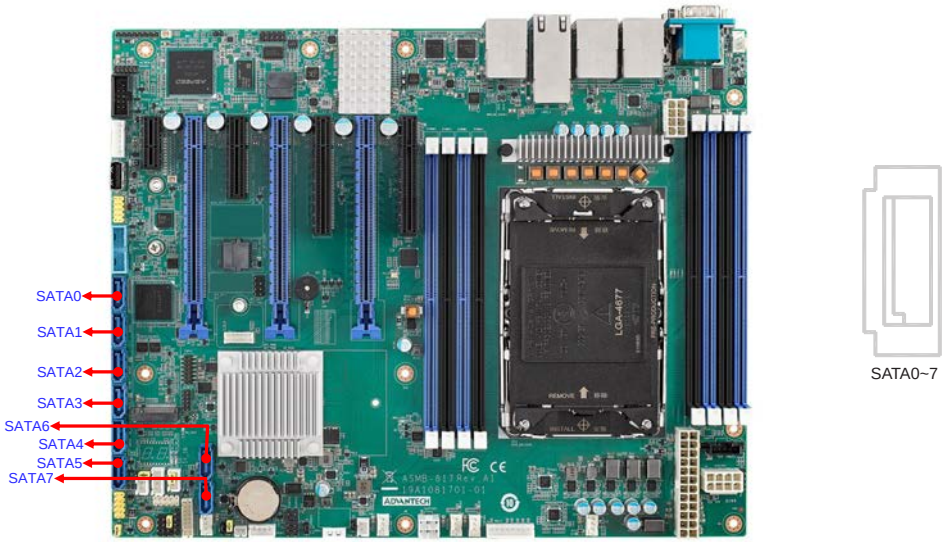
2.12 Front Panel LAN Indicator Connector (LANLED1)





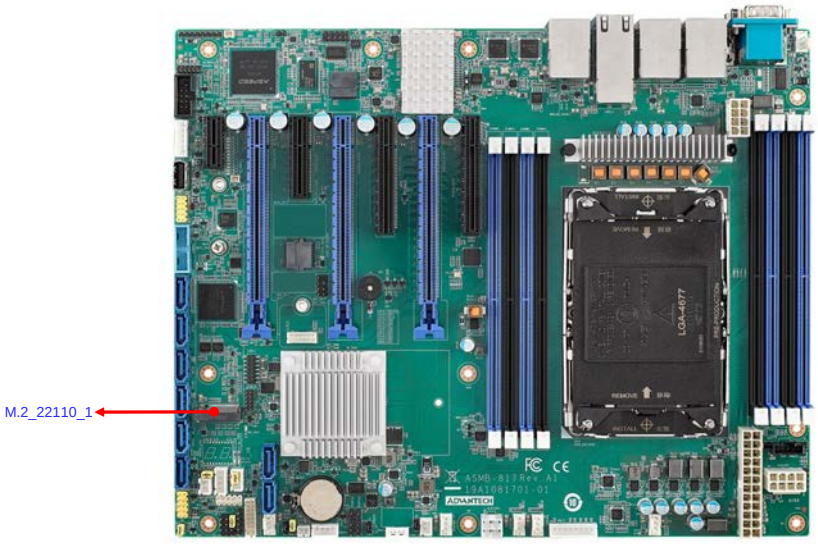
2.13 SATA and sSATA (SATA0~7)

ASMB-817 features eight SATA III (6 Gbps) ports.



2.14 M.2 Connector (M2_22110_1)

The M.2 2280 connector supports both SATA and PCIe SSD devices.



2.15 PCIe Expansion Slots

ASMB-817 provides seven expansion slots that can support up to three double-deck cards. The riser card for 1U or 2U chassis can be used in slot-6 only, and the PCIe link is from CPU.

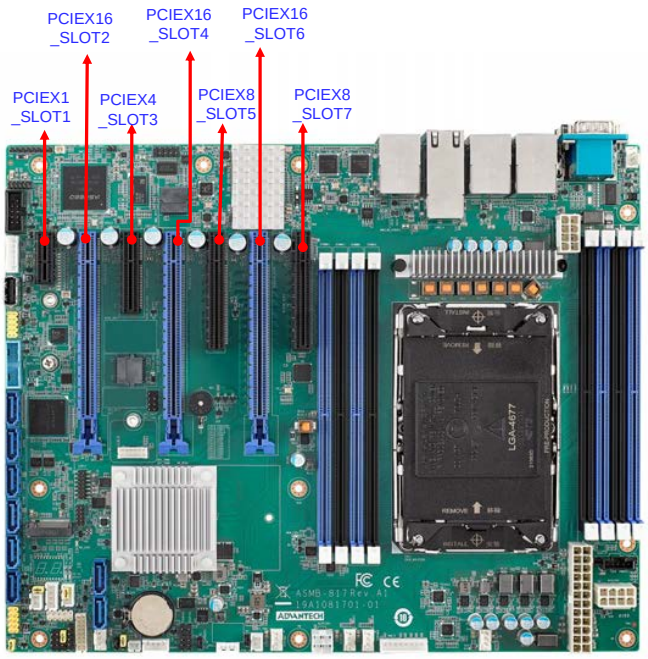


Table 2.3:

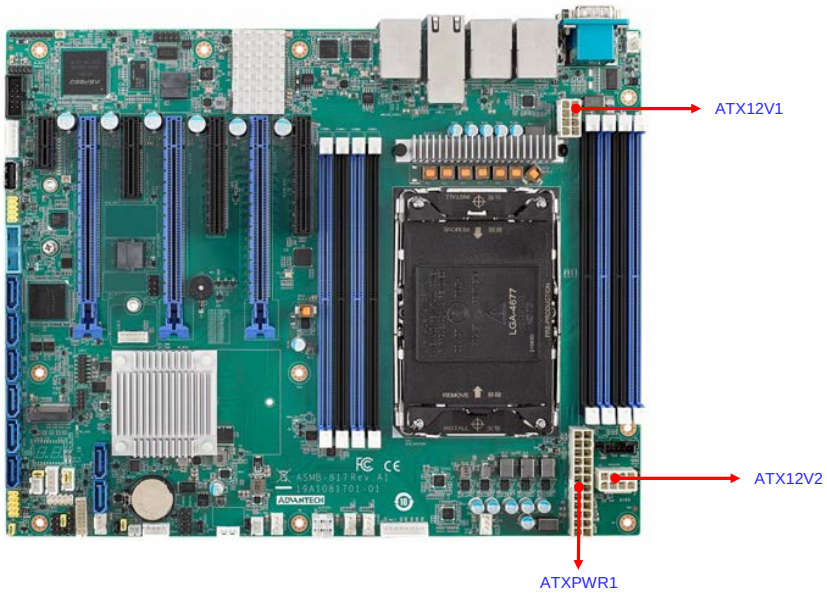
	Slot Length	Link	PCI-E Generation	PCIe link from
SLOT1	PCI-E x1	PCI-E x1	3	PCH
SLOT2	PCI-E x16	PCI-E x16	5	CPU
SLOT3	PCI-E x4	PCI-E x4	5	CPU
SLOT4	PCI-E x16	PCI-E x16	5	CPU
SLOT5	PCI-E x8	PCI-E x8	5	CPU
SLOT6	PCI-E x16	PCI-E x16	5	CPU
SLOT7	PCI-E x8	PCI-E x8	5	CPU

Table 2.4: PCIe Expansion Slots

	Part Number	Description	Remarks
Riser Card	ASMB-RF28-20B1	ASMB-RF28 (2U Gen5 riser card)	2*PCI-E x8
	ASMB-RF2F-10B1	ASMB-RF2F (2U Gen5 riser card)	1*PCI-E x16
	ASMB-RF1F-10B1	ASMB-RF1F (1U Gen5 riser card)	1*PCI-E x16

Note! *Refer to page 67. BIOS setting [X8,X8] is required for Slot 6 when using ASMB-RF28-20B1.*

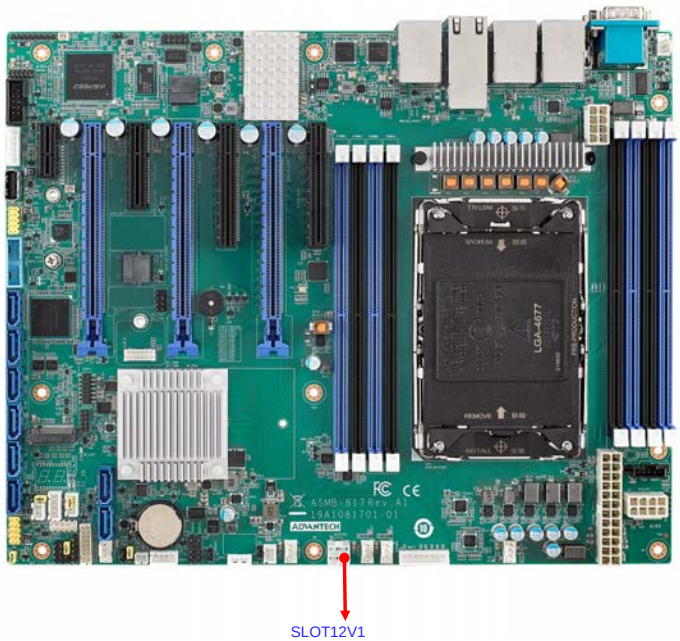
2.16 Auxiliary Power Connector (ATXPWR1/ ATX12V1/ATX12V2)



Note!

1. *Please use a power supply of SSI type; minimum output should be at least 700W with 5Vsb @2.5A.*
2. *ATXPWR1 & ATX12V1 should be all connected with power supply, otherwise ASMB-817 will not boot up normally.*

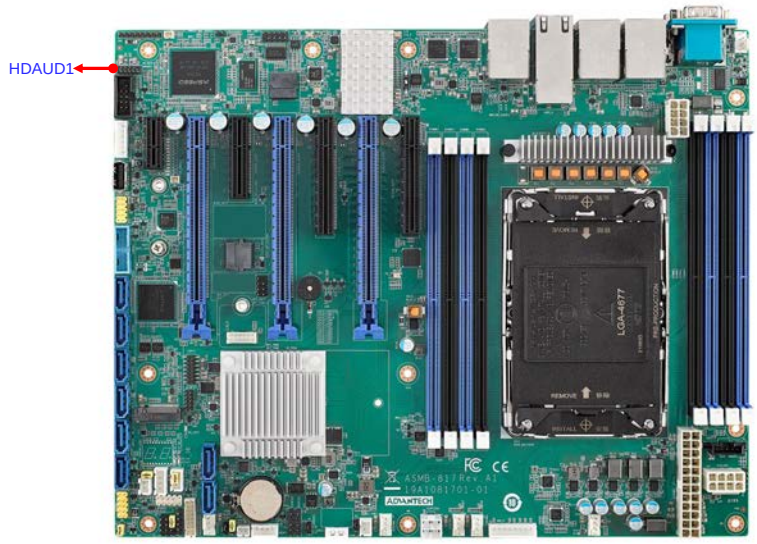
2.17 PCIe Power Connector (SLOT12V1)

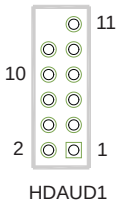


Note! *This SLOT12V1 connector is only necessary if PCIe cards that draw more than 70 watts from PCIe bus are installed on the motherboard.*

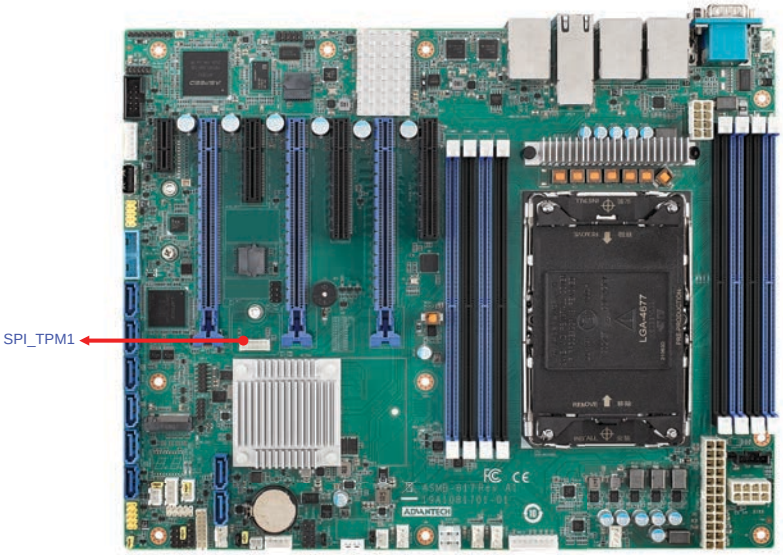
2.18 HD Audio Interface Connector (HDAUD1)

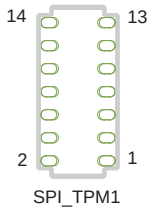
ASMB-817 has one audio connector for Advantech's audio board (P/N: PCA-AUDIO-HDB1E) installation.





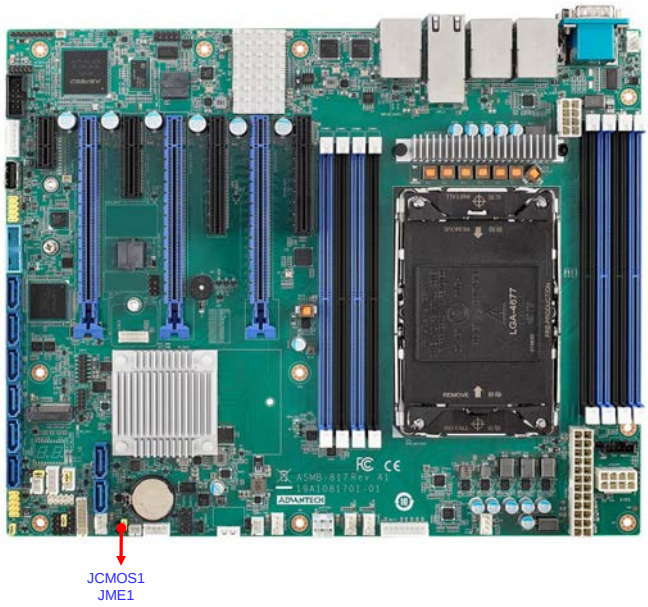
2.19 SPI Connector (SPI_TPM1)



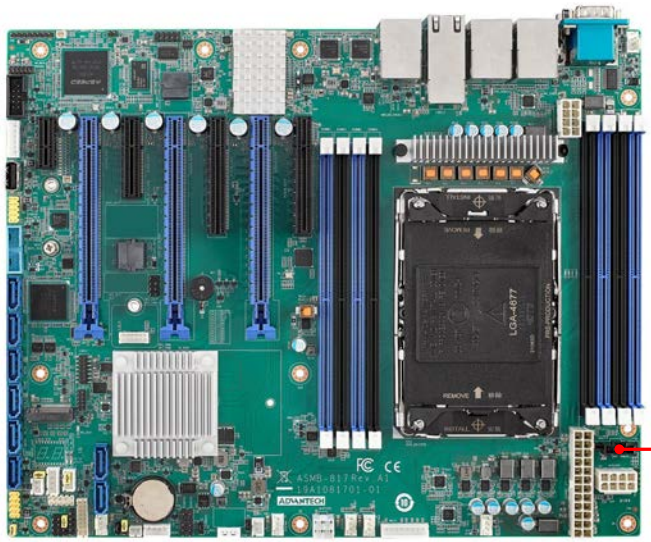


2.20 CMOS Clear and ME Update Connector (JCMOS1, JME1)

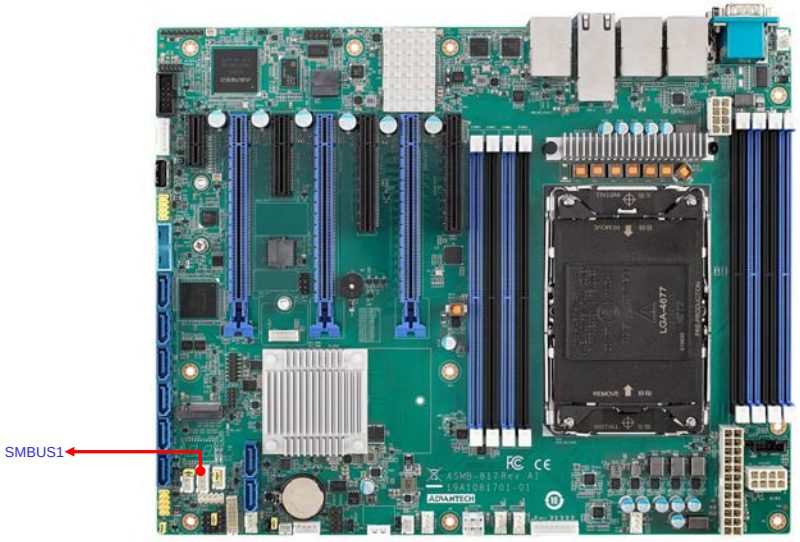
Setting jumper from pin 1-2 to pin 2-3, then back to pin 1-2 to reset CMOS data and enable ME update.

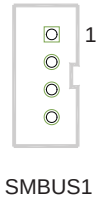


2.21 PMBUS Connector (PMBUS1)



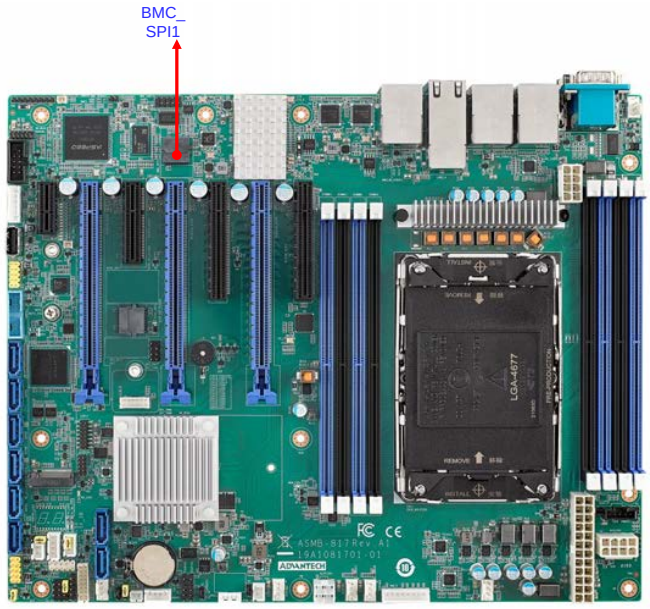
2.22 Front Panel SMBUS Connector (SMBUS1)



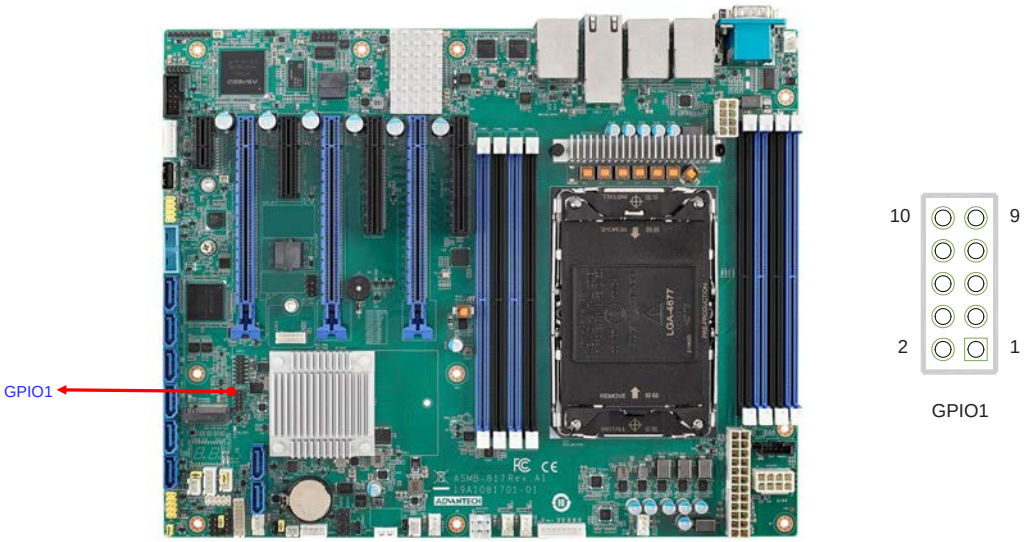


2.23 BMC IC Socket (BMC_SPI1)

Enabling IPMI feature through BMC_SPI1. The BMC image has already been pre-installed on ASMB-817.

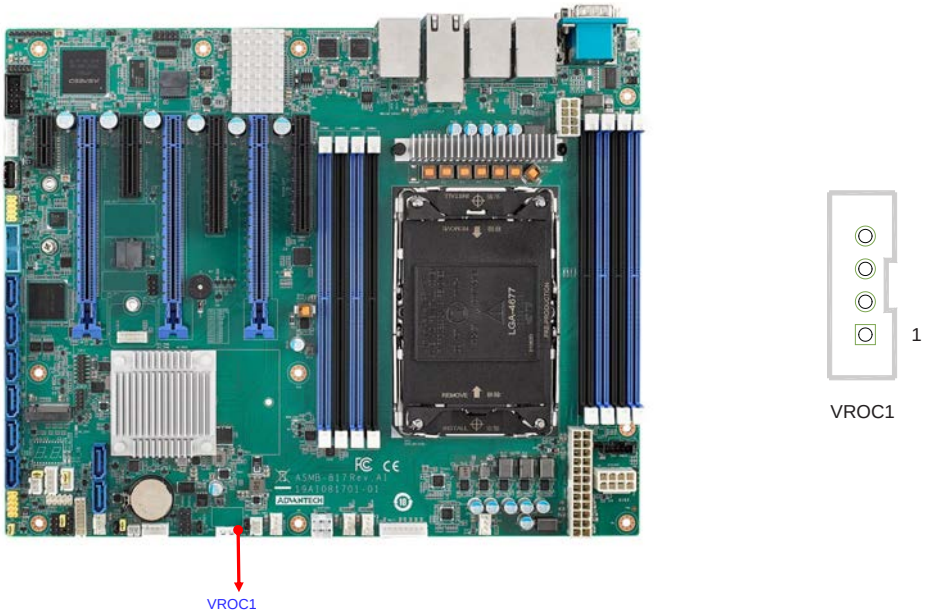


2.24 GPIO Connector (GPIO1)



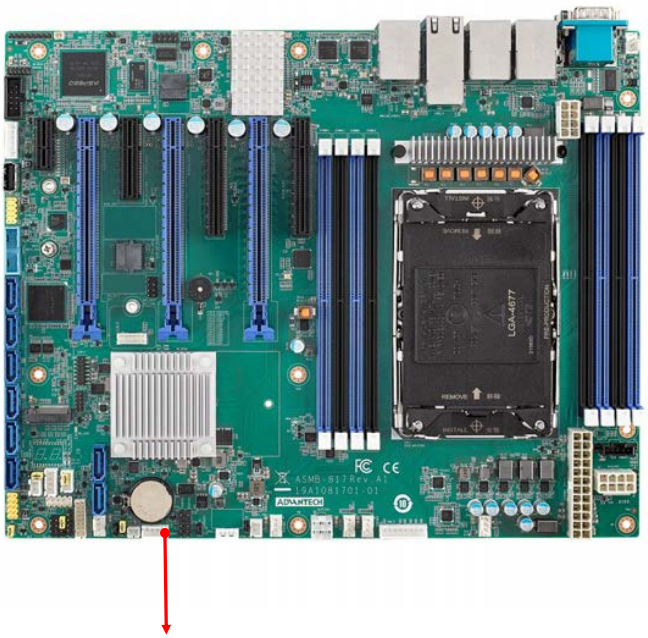
2.25 Intel Virtual RAID (VROC1)

Intel VROC license key of VMD allows NVMe SSDs to connect via PCIe and directly manages the CPU for better RAID performance. Enable NVMe SSD RAID, hot-plug and LED management features via VROC connector.



2.26 NVMe RAID LED Control (PEHP1)

Connect to storage chassis to enable NVMe RAID LED control feature.

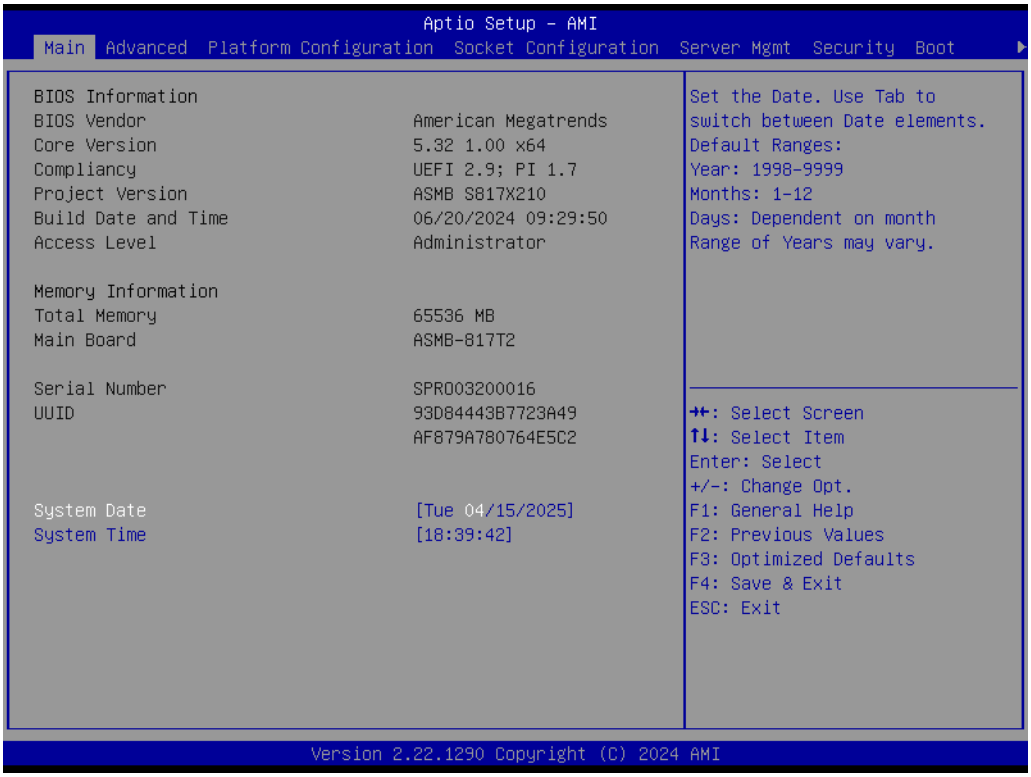


Chapter 3

AMI BIOS

3.1 Introduction

With the AMI BIOS Setup program, you can modify BIOS settings and control the special features of your computer. The Setup program uses a number of menus for making changes and turning the special features on or off. This chapter describes the basic navigation of the ASMB-817 setup screens.



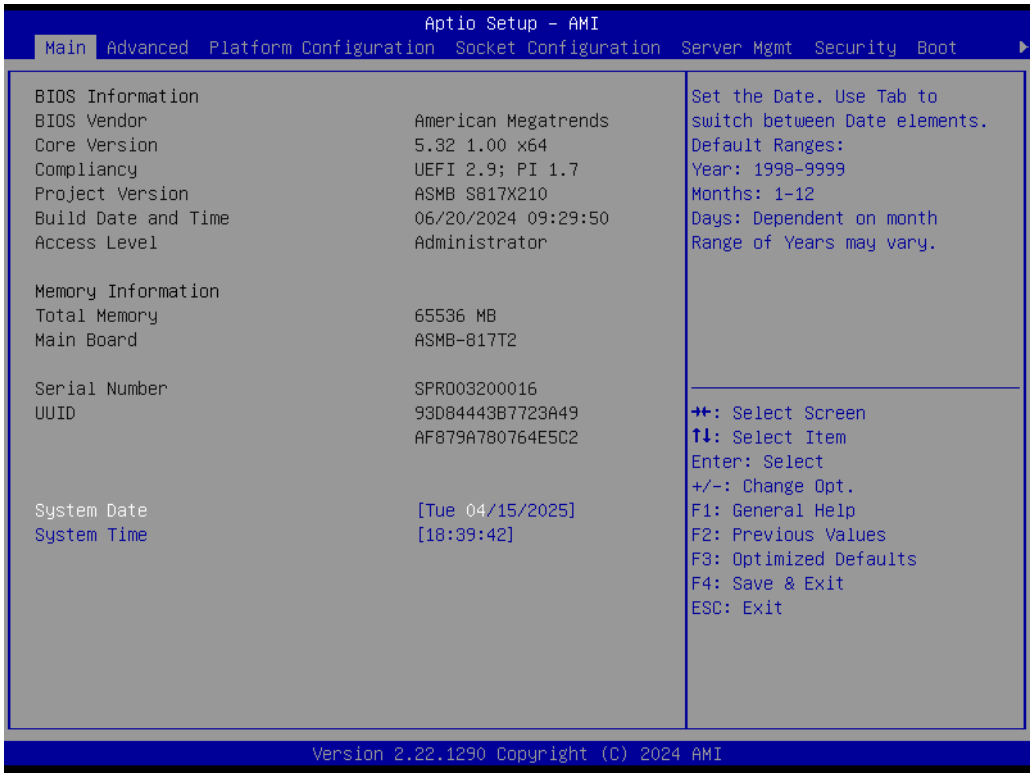
AMI's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed up CMOS so it retains the Setup information when the power is turned off.

Note! *The BIOS setup screens shown in this chapter are for reference only, they may not exactly match what you see on your display.*

3.2 BIOS Setup

3.2.1 Main Menu

Press <Del> during bootup to enter AMI BIOS CMOS Setup Utility; the Main Menu will appear on the screen. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.



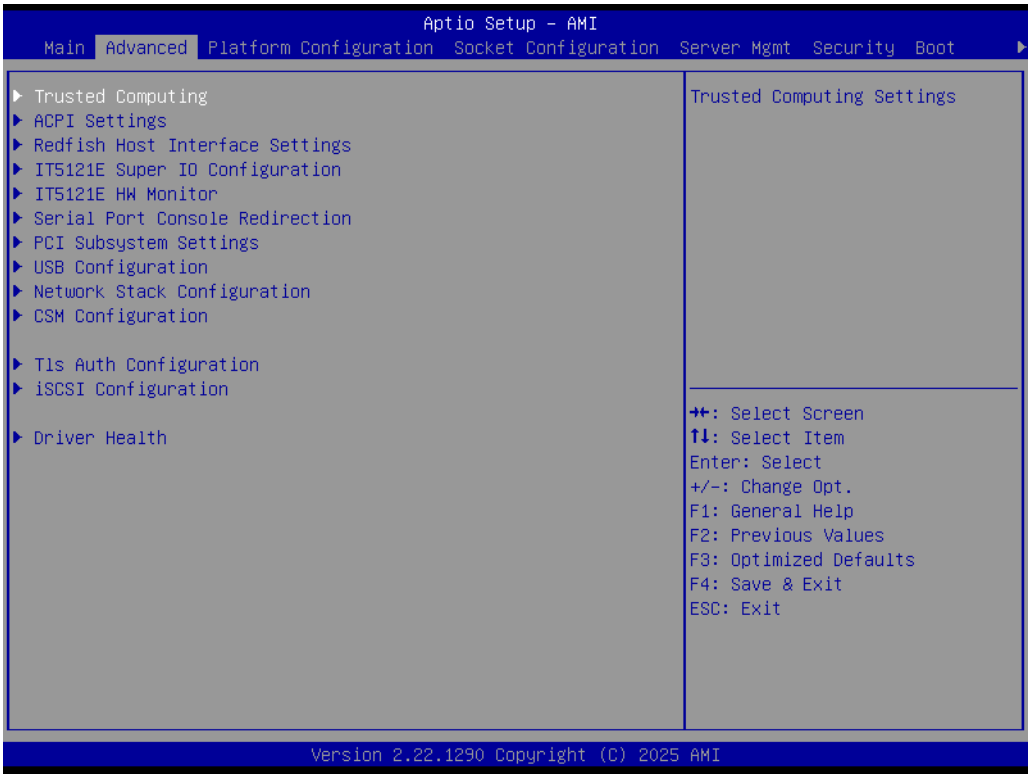
The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can be. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

- **System Date/System Time**

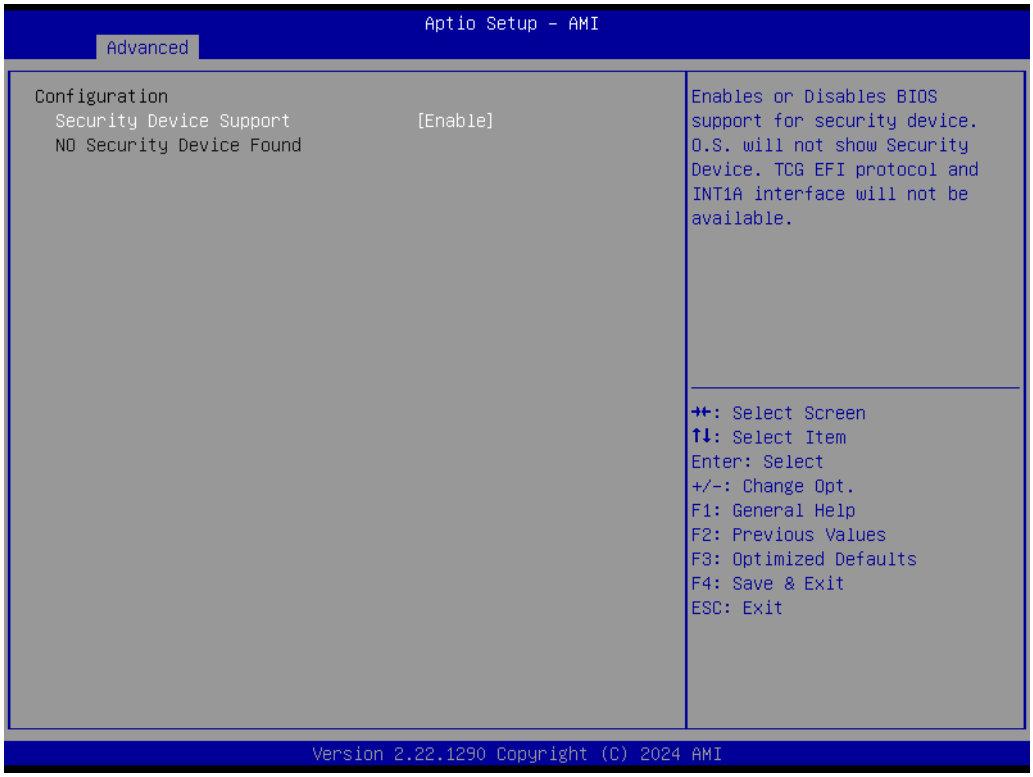
  Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.2.2 Advanced BIOS Features Setup

Select the Advanced tab from the ASMB-817 setup screen to enter the Advanced BIOS setup screen. You can select any of the items in the left frame of the screen, such as CPU configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screens are shown below. The sub menus are described on the following pages.

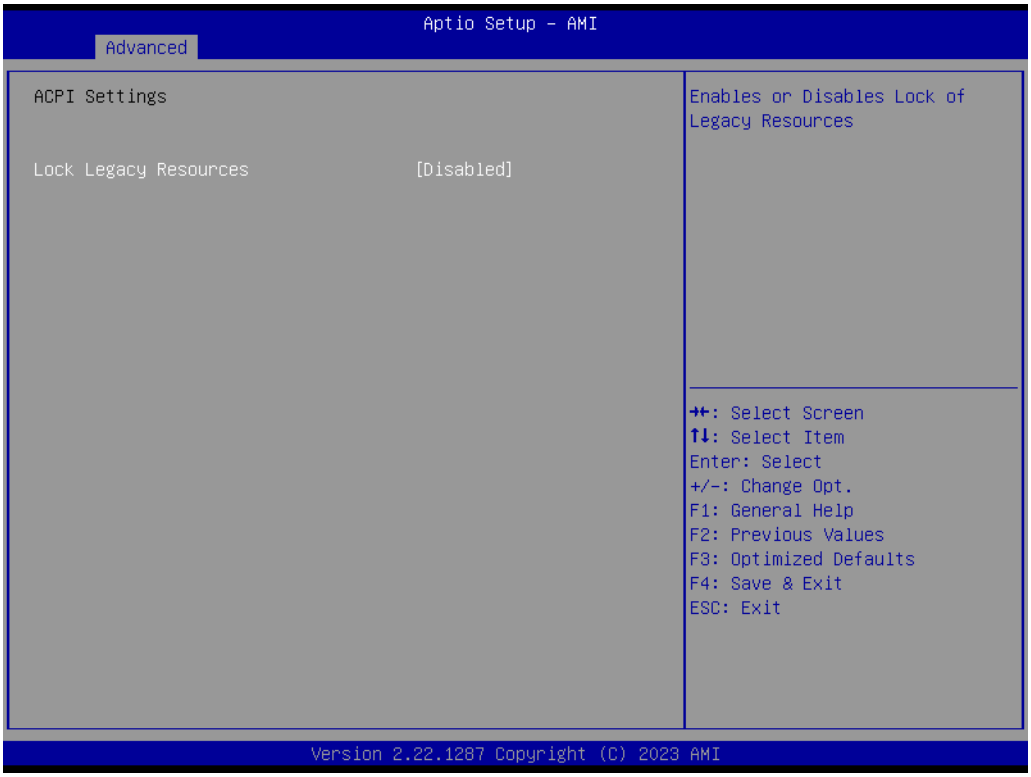


3.2.2.1 Trusted Computing



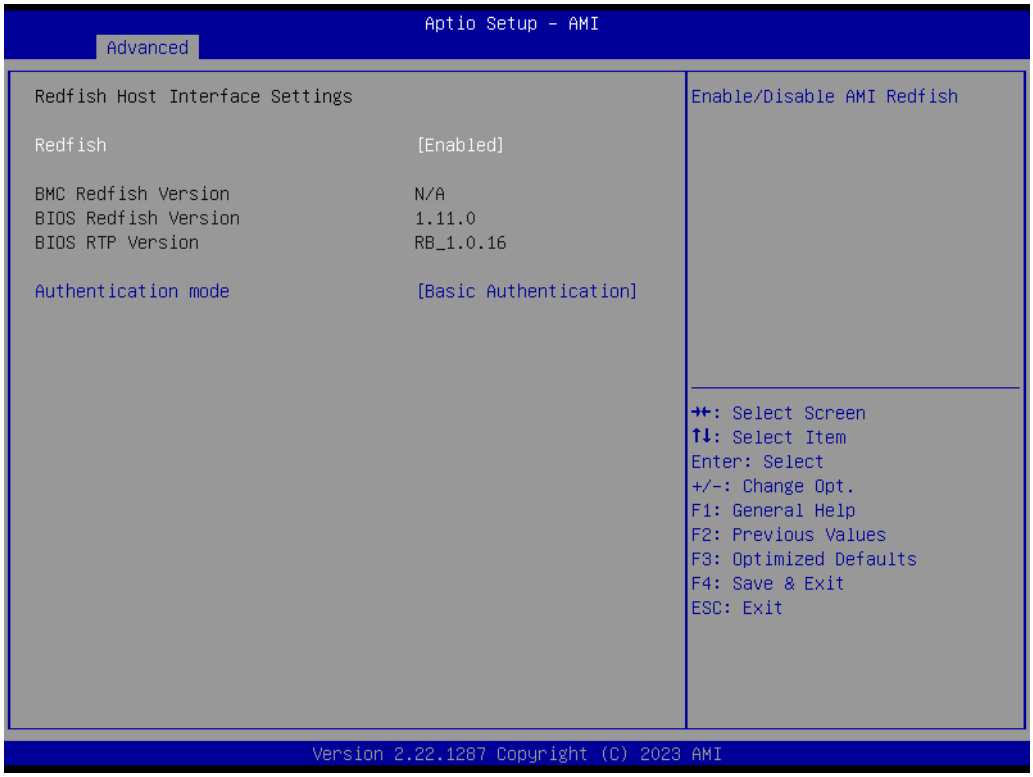
- **Security Device Support [Enable]**
  Enables or disables BIOS support for security devices.This feature requires additional option module PCA-TPMSPI-00A1.

3.2.2.2 ACPI Settings



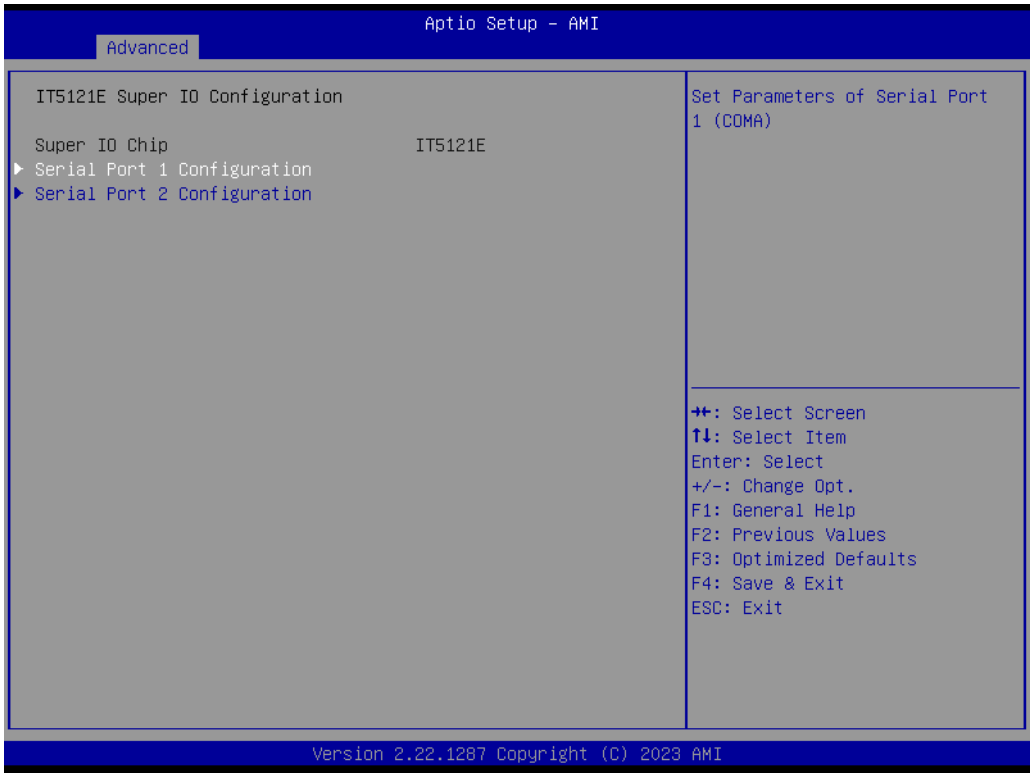
- **Lock Legacy Resources [Disable]**
  Enable or disable lock legacy resources feature.

3.2.2.3 Redfish Host Interface Settings

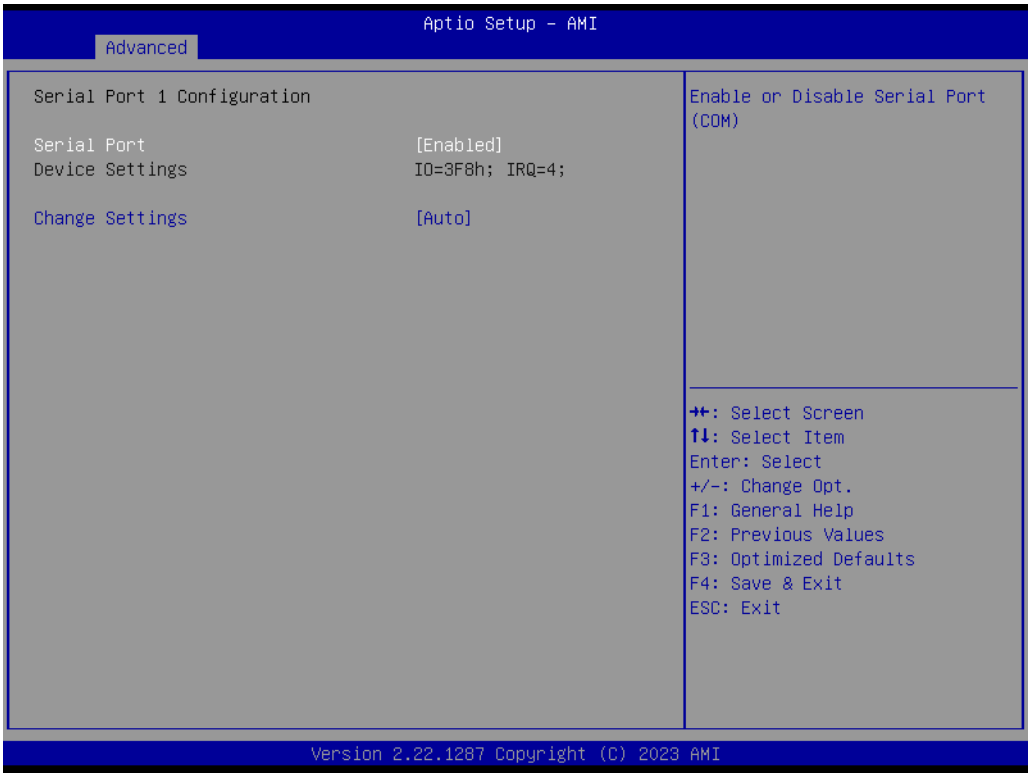


- **Redfish [Enable]**
  Enable or disable BMC Redfish feature.
- **Authenication mode**
  Select the authenication mode for Redfish

3.2.2.4 IT5121E Super IO Configuration

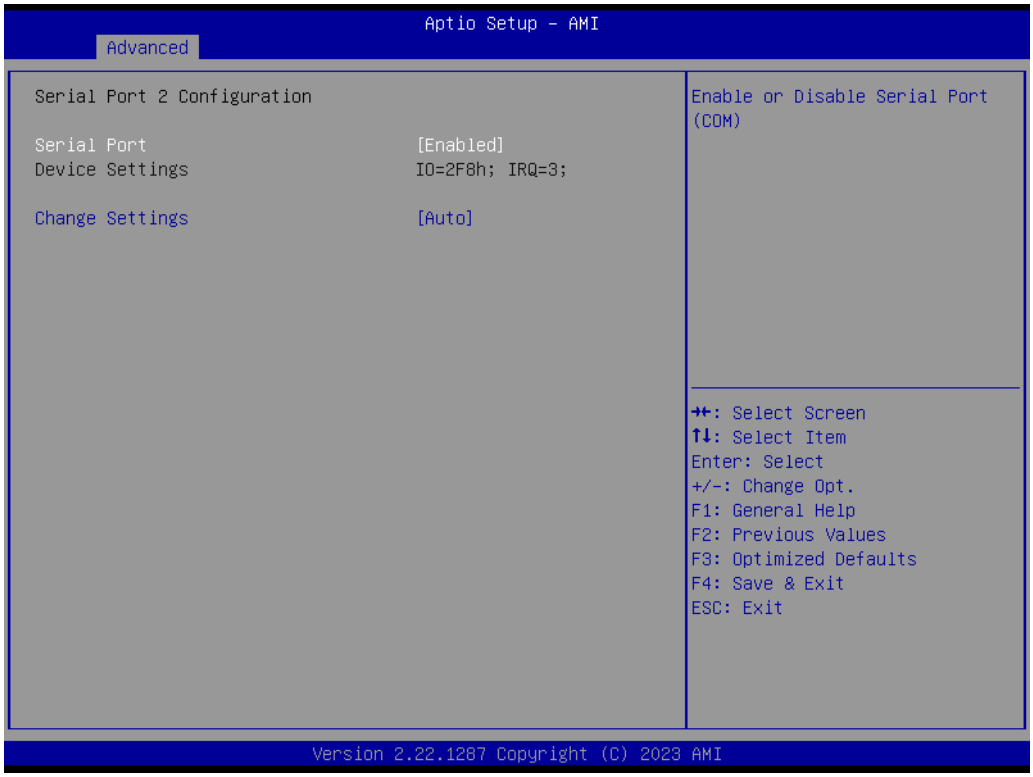


Serial Port 1 Configuration



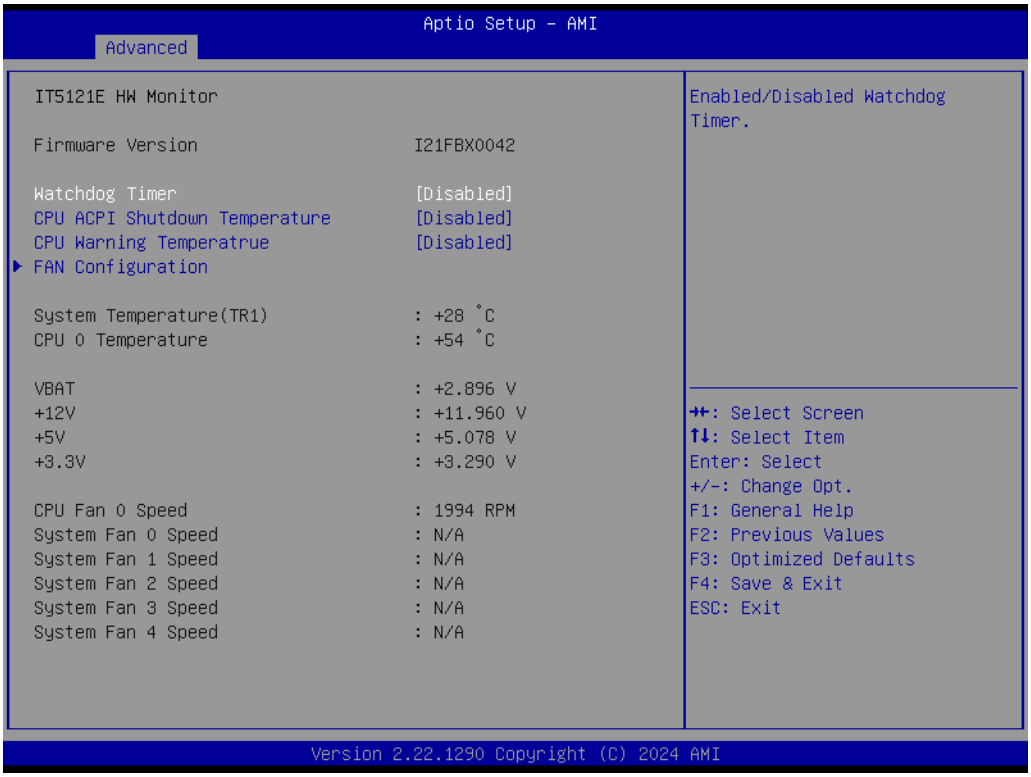
- **Serial Port [Enable]**
  Enable or disable serial port 1.
- **Change Settings [Auto]**
  To select an optimal setting for serial port 1. Default setting is "Auto".

Serial Port 2 Configuration

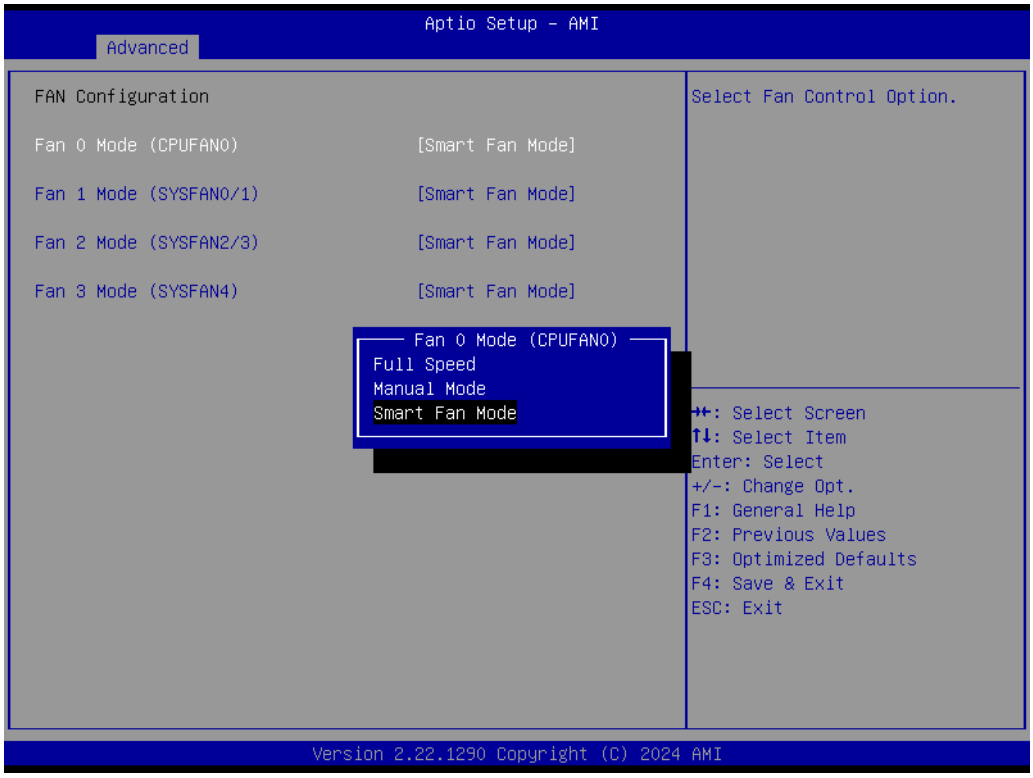


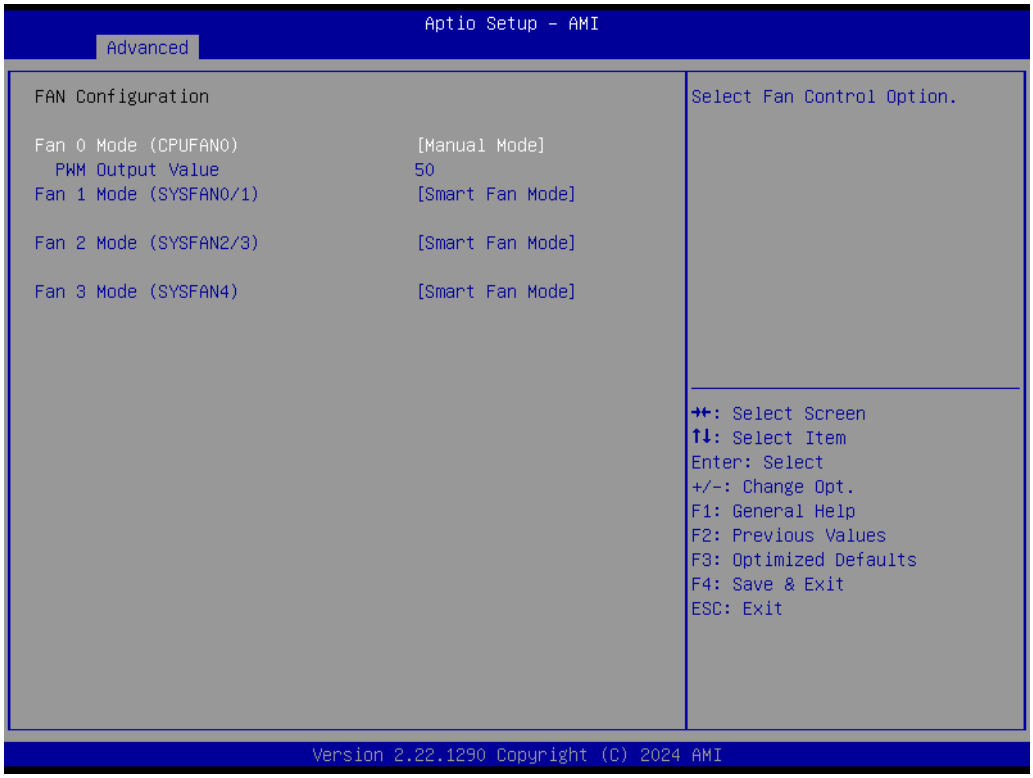
- **Serial Port [Enable]**
  Enable or disable serial Port 2.
- **Change Settings [Auto]**
  To select an optimal setting for serial port 2. Default setting is "Auto".

3.2.2.5 IT5121E HW Monitor

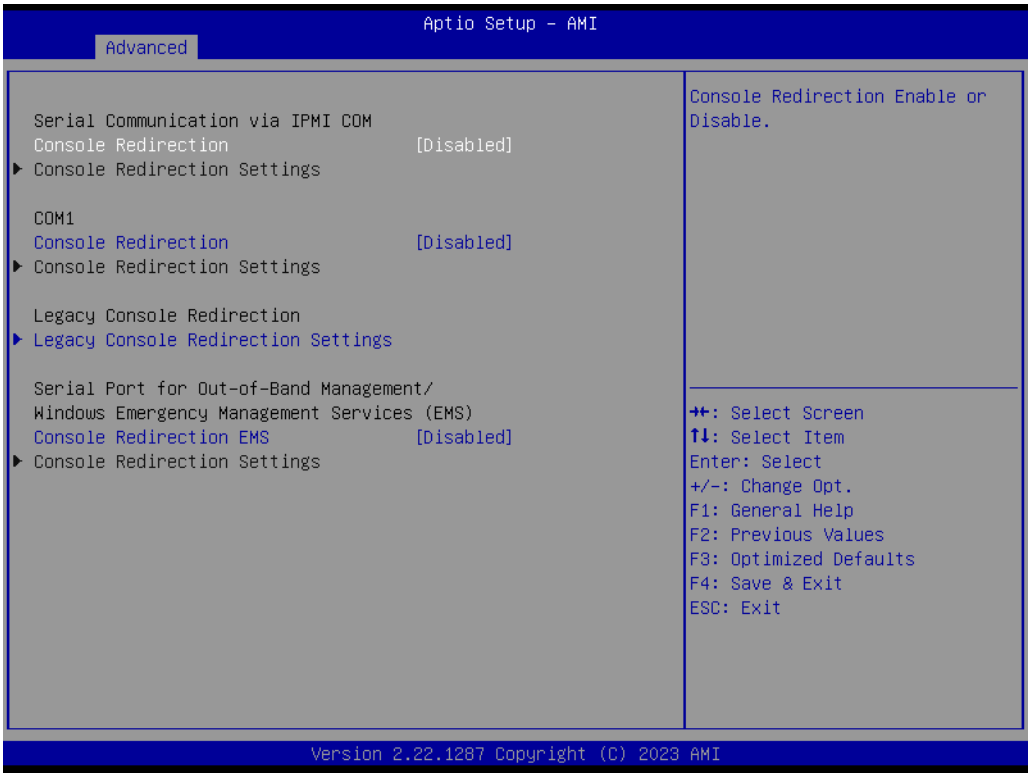


- **Watchdog Timer [Disable]**
  Enable or disable the watchdog timer function.
- **CPU ACPI Shutdown Temperature [Disable]**
  Enable or disable the ACPI shutdown temperature threshold. When the system reaches the shutdown temperature, it will be automatically shut down by ACPI OS to protect the system from overheat damage.
- **CPU Warning Temperature [Disable]**
  Enable or disable the CPU warning temperature threshold. When the system reaches the warning temperature, the speaker will beep.
- **Fan Configuration**
  Fan 0 mode controls CPUFAN0, Fan 1 mode controls SYSFAN0/1, FAN 2 mode controls SYSFAN2/3, FAN 3 mode controls SYSFAN4.
  The default of CPU/System FAN is Smart FAN mode and the BIOS will automatically control the FAN speed by CPU temperature.
  When set to manual mode, fan duty setting can be changed; the range is from 20%~100%, default setting is 50%.

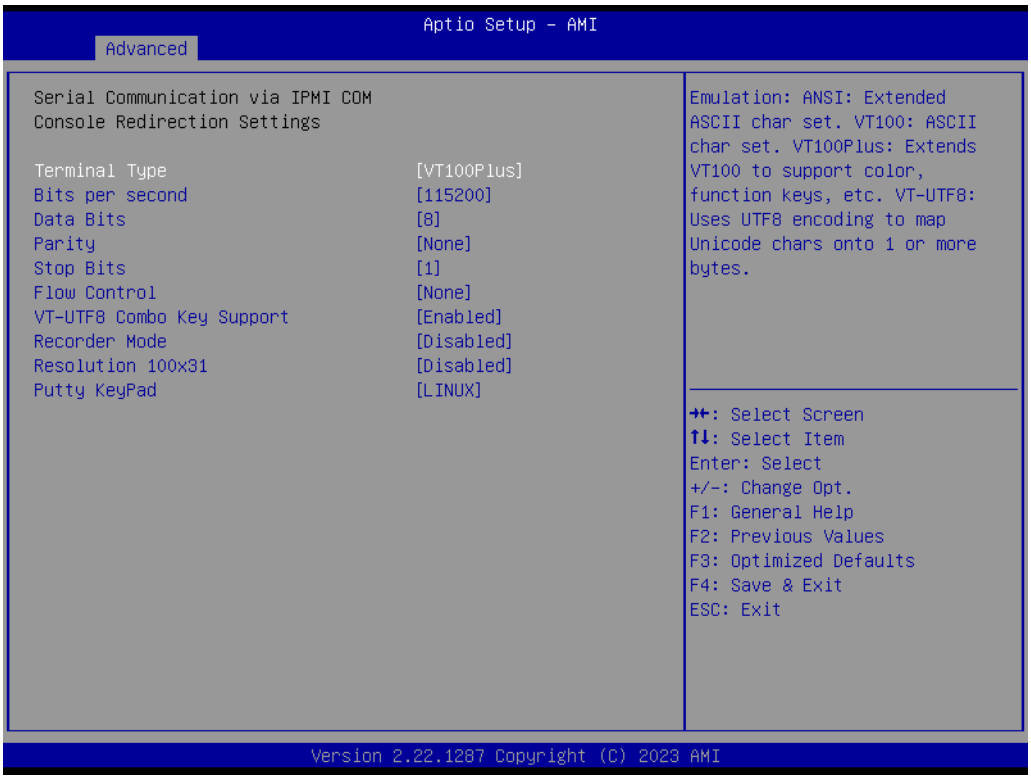

Aptio Setup – AMI
Advanced
FAN Configuration
Fan 0 Mode (CPUFAN0) [Smart Fan Mode]
Fan 1 Mode (SYSFAN0/1) [Smart Fan Mode]
Fan 2 Mode (SYSFAN2/3) [Smart Fan Mode]
Fan 3 Mode (SYSFAN4) [Smart Fan Mode]
Fan 0 Mode (CPUFAN0)
Full Speed
Manual Mode
Smart Fan Mode
Select Fan Control Option.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.22.1290 Copyright (C) 2024 AMI


Aptio Setup – AMI
Advanced
FAN Configuration
Fan 0 Mode (CPUFAN0) [Manual Mode]
PWM Output Value 50
Fan 1 Mode (SYSFAN0/1) [Smart Fan Mode]
Fan 2 Mode (SYSFAN2/3) [Smart Fan Mode]
Fan 3 Mode (SYSFAN4) [Smart Fan Mode]
Select Fan Control Option.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.22.1290 Copyright (C) 2024 AMI

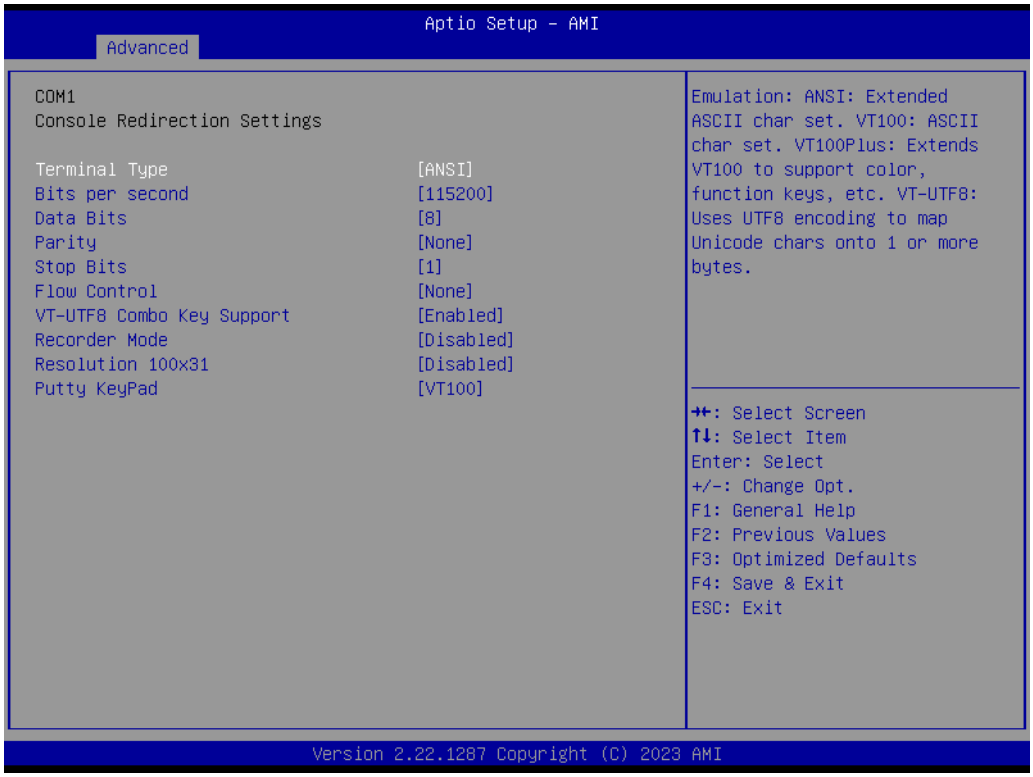
3.2.2.6 Serial Port Console Redirection



- **Serial Communication via IPMI COM**



COM1 Console Redirection Settings

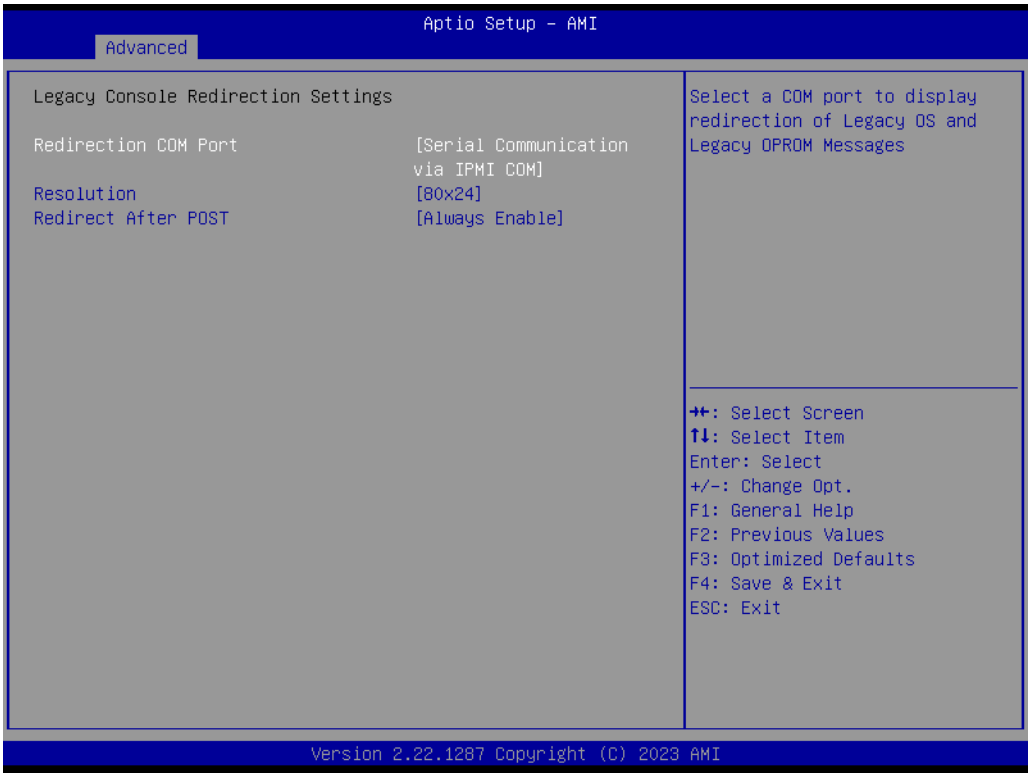


– **Terminal Type [ANSI]**
  Select a terminal type to be used for console redirection.
  Options available: VT100/VT100+/ANSI/VT-UTF8.
– **Bits Per Second [115200]**
  Select the baud rate for console redirection.
  Options available: 9600/19200/38400/57600/115200/230400/460800/921600.
– **Data Bits [8]**
  Option available: 7/8
– **Parity [None]**
  A parity bit can be sent with the data bits to detect some transmission errors.
  Even: parity bit is 0 if the number of 1's in the data bits is even.
  Odd: parity bit is 0 if number of 1's the data bits is odd.
  Mark: parity bit is always 1. Space: Parity bit is always 0.
  Mark and Space Parity do not allow for error detection.
  Options available: None/Even/Odd/Mark/Space.
– **Stop Bits [1]**
  Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
  Options available: 1/2.

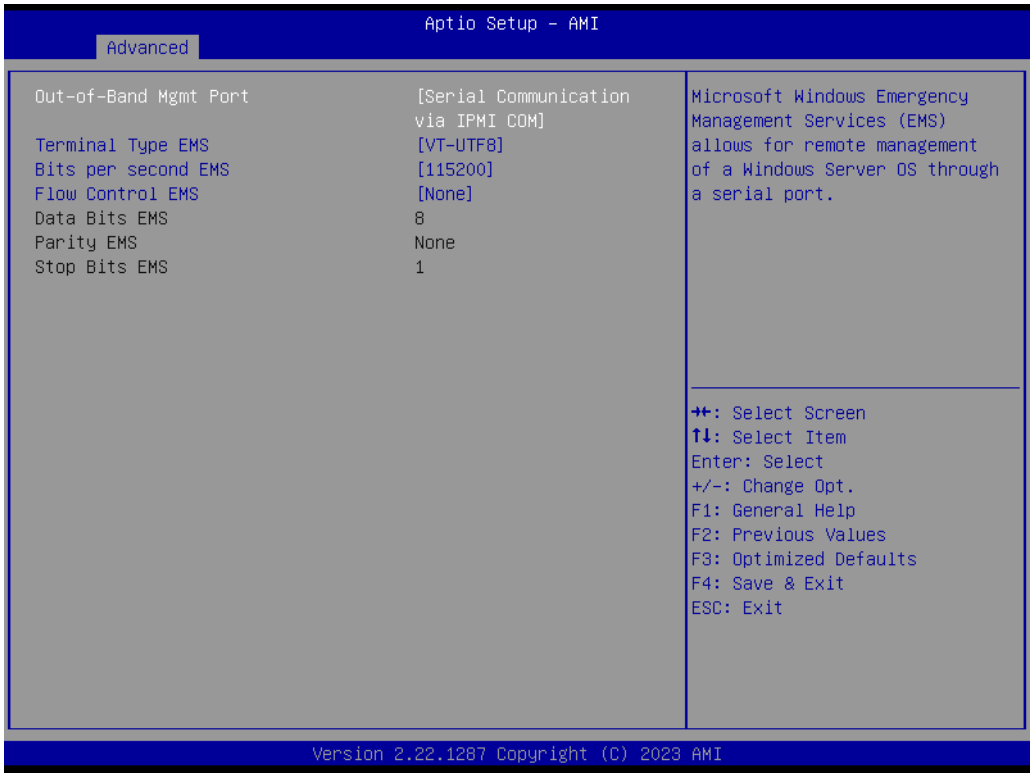
- **Flow Control [None]**
  Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
  Options available: None/Hardware RTS/CTS.
- **VT-UTF8 Combo Key Support [Enable]**
  Enable VT-UTF8 combination key support for ANSI/VT100 terminals
- **Recorder Mode [Disable]**
  When this mode enabled, only text will be sent; this is to capture terminal data.
  Options available: Enabled/Disabled.
- **Resolution 100x31 [Disable]**
  Enables or disables extended terminal resolution.
- **Putty Keypad [VT100]**
  Select function key and keypad on putty, default setting is "VT100".

■ **Legacy Console Redirection Settings**

Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.

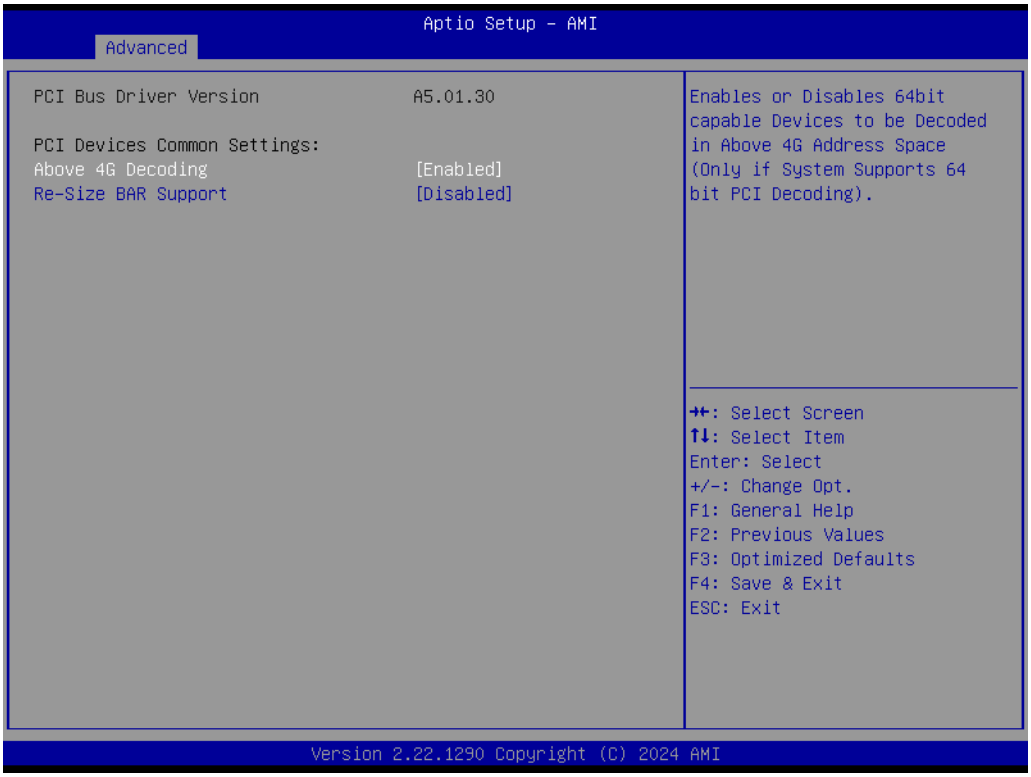


Console Redirection Settings



- **Out-of-Band Mgmt Port**
  To select the com port user would like to set for having console redirection feature.
- **Terminal Type [VT-UTF8]**
  Set as "VT100", "VT100+", "VT-UTF8", or "ANSI". "VT-UTF8" is the default setting.
- **Bits Per Second [115200]**
  To select serial port transmission. Speed must be matched on the other side. It can be set as "9600", "19200", "57600", "115200", "230400", "460800" or "921600". "115200" is the default setting.
- **Flow Control [None]**
  Flow control can prevent data loss from buffer overflow. It can be set as "None", "Hardware RTS/CTS", or "Software Xon/Xoff". "None" is the default setting.

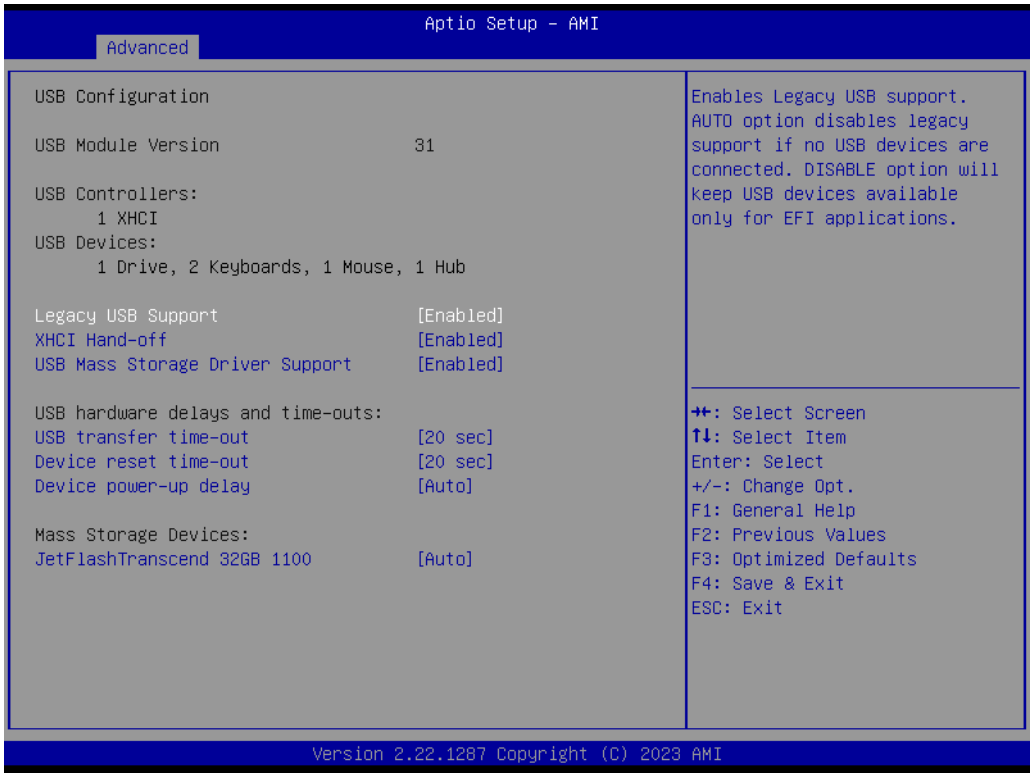
3.2.2.7 PCI Subsystem Settings



- **Above 4G Decoding [Enable]**
  Enable or Disable 64-bit capable devices to be decoded in above 4G address space (Only if system supports 64-bit PCI decoding)
- **Re-Size BAR Support [Disable]**
  If the PCIe add on card has the capability of Resizable BAR, this option is to Enable or Disable Re-Size BAR support from system.

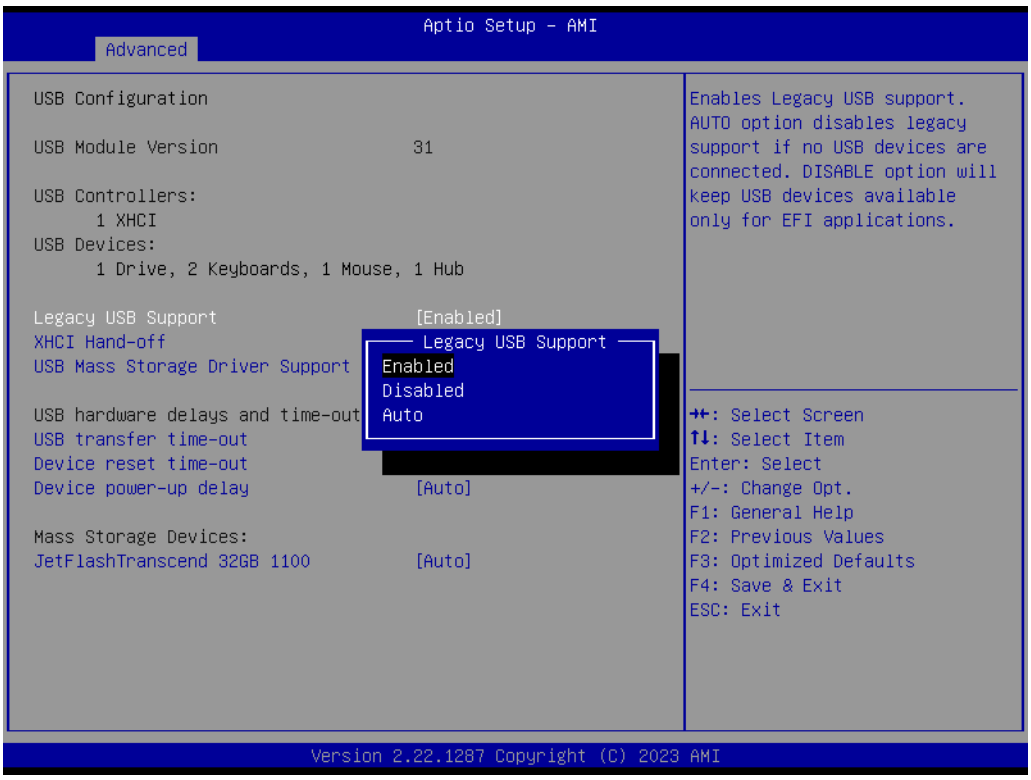
Note! *Some graphics or GPU cards need to enable 4G decoding.*

3.2.2.8 USB Configuration

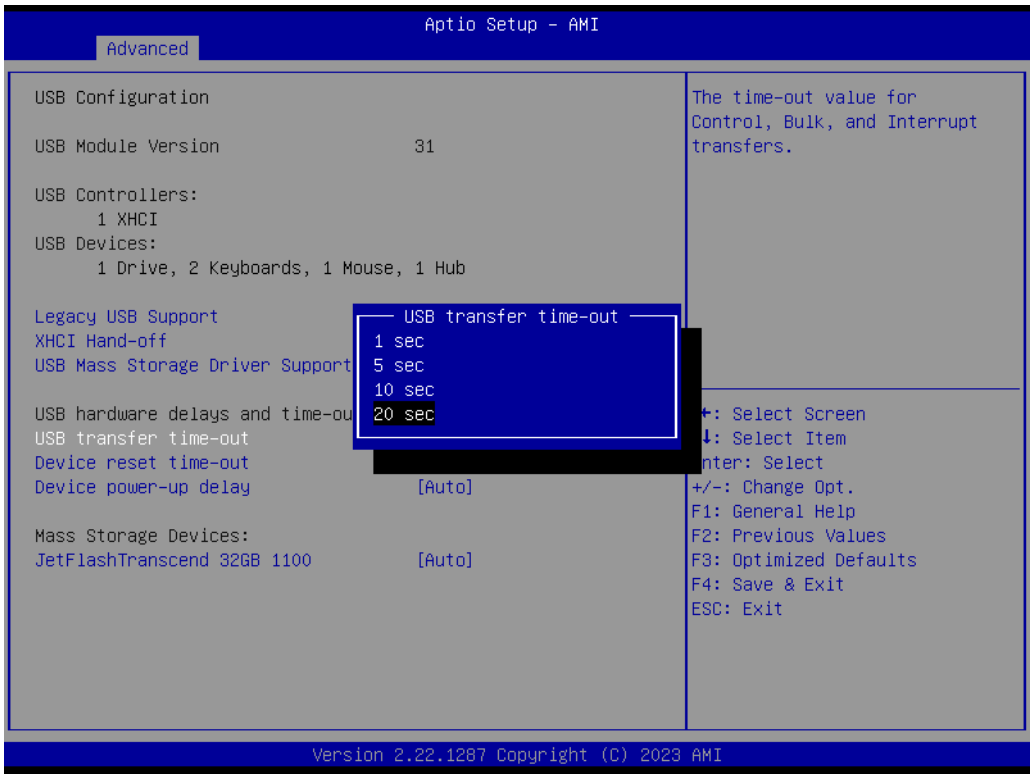


- **Legacy USB Support [Enable]**

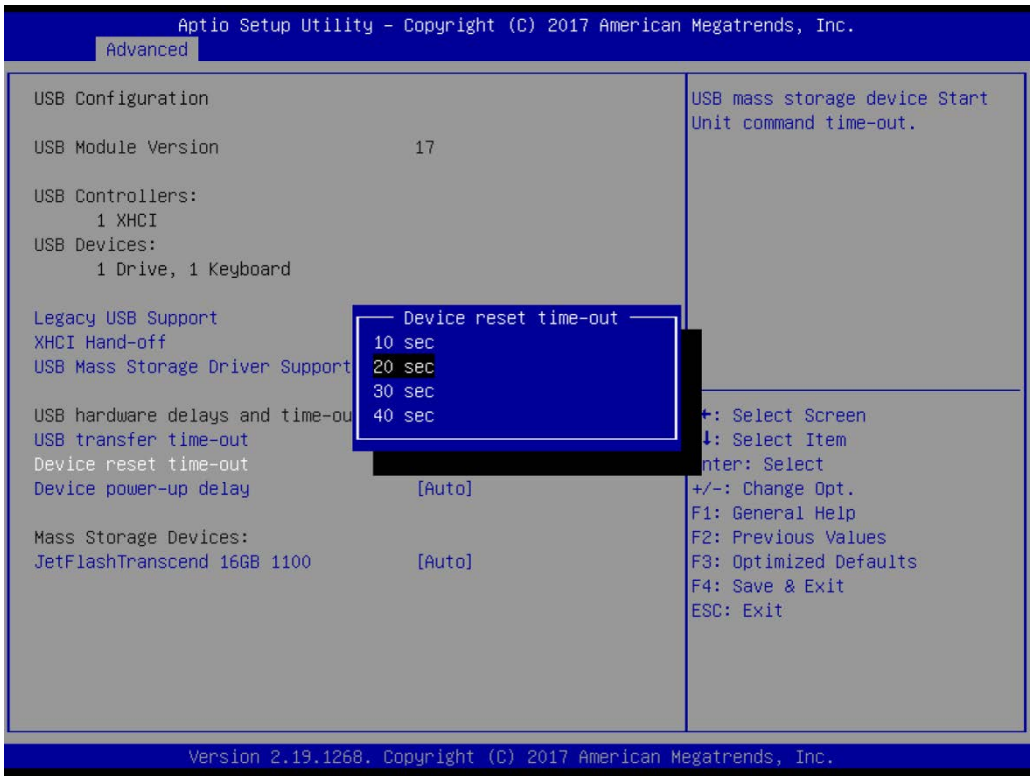
  This is for supporting USB device under a legacy OS such as DOS. When choosing "Auto", the system will automatically detect if any USB device is plugged into the computer and enable USB legacy mode when a USB device is plugged, or disable USB legacy mode when no USB device is attached. "Enabled" is the default setting.



- **XHCI Hand-off [Enable]**
  This is a workaround for OS without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver. "Enabled" is the default setting.
- **USB Mass Storage Driver Support [Enable]**
  Enable or disable USB mass storage driver support.
- **USB Transfer Time-out [20sec]**
  Selects the USB transfer time-out value. [1,5,10,20sec]

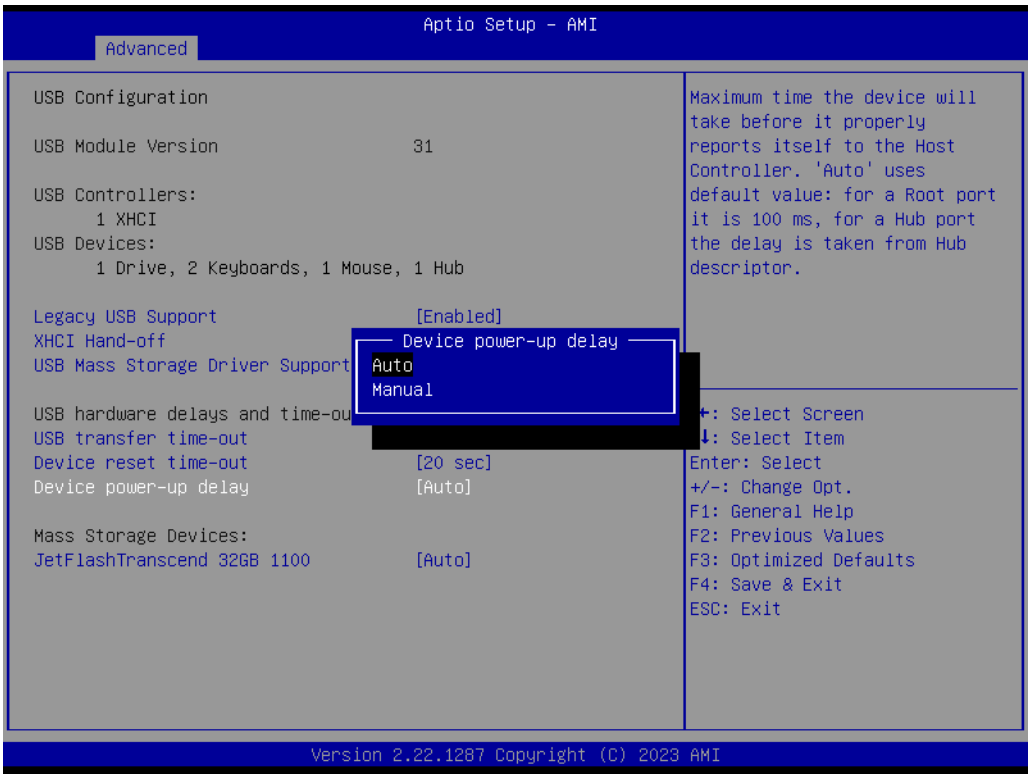


- **Device Reset Time-out [20sec]**
  Selects the USB device reset time-out value. [10,20,30,40 sec]



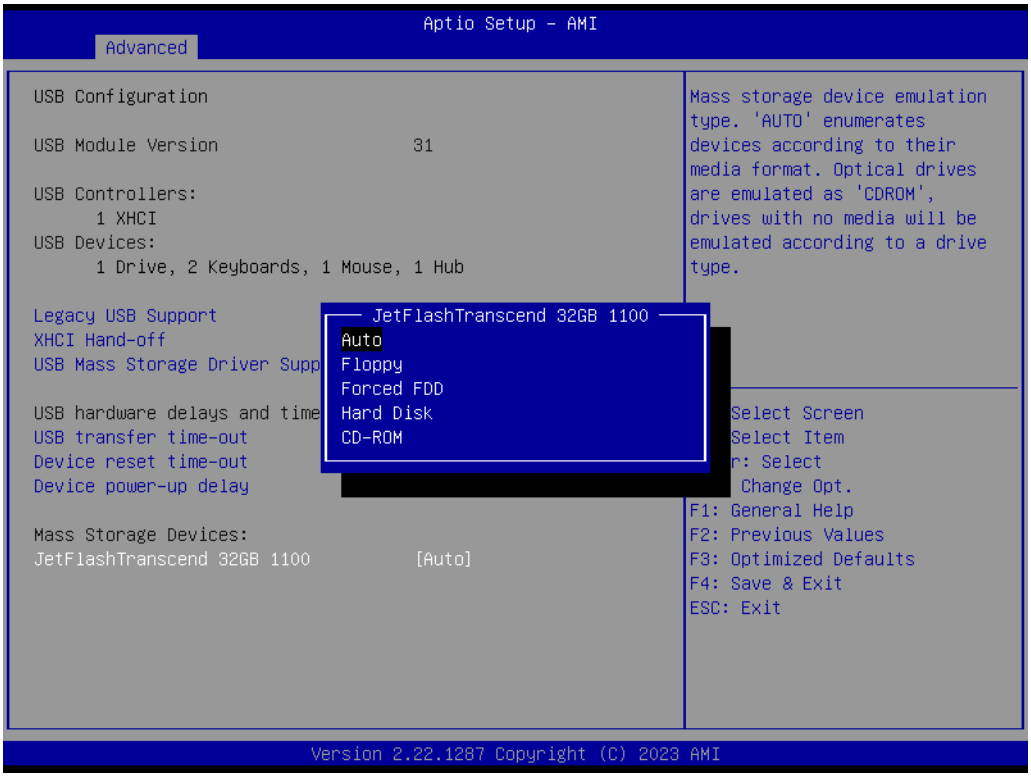
■ **Device Power-up Delay [Auto]**

This item appears only when Device power-up delay item is set to [manual]. "Auto" is the default setting.

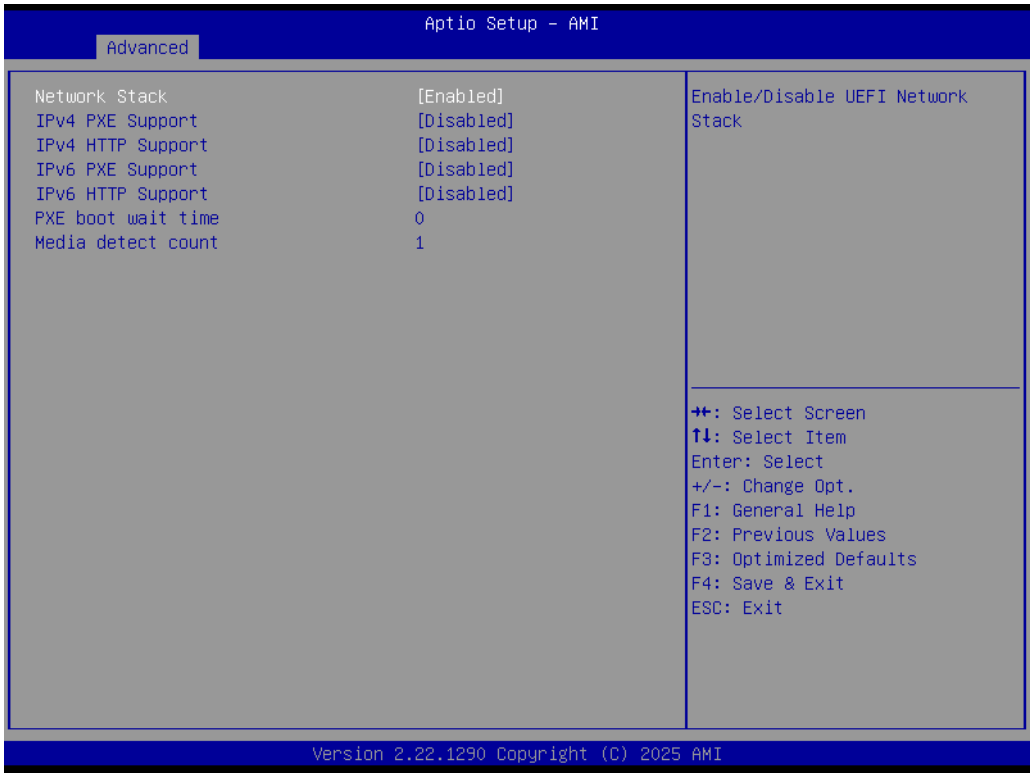


■ **Mass Storage Devices [Auto]**

Default is "Auto" to enumerate mass storage devices according to media format.

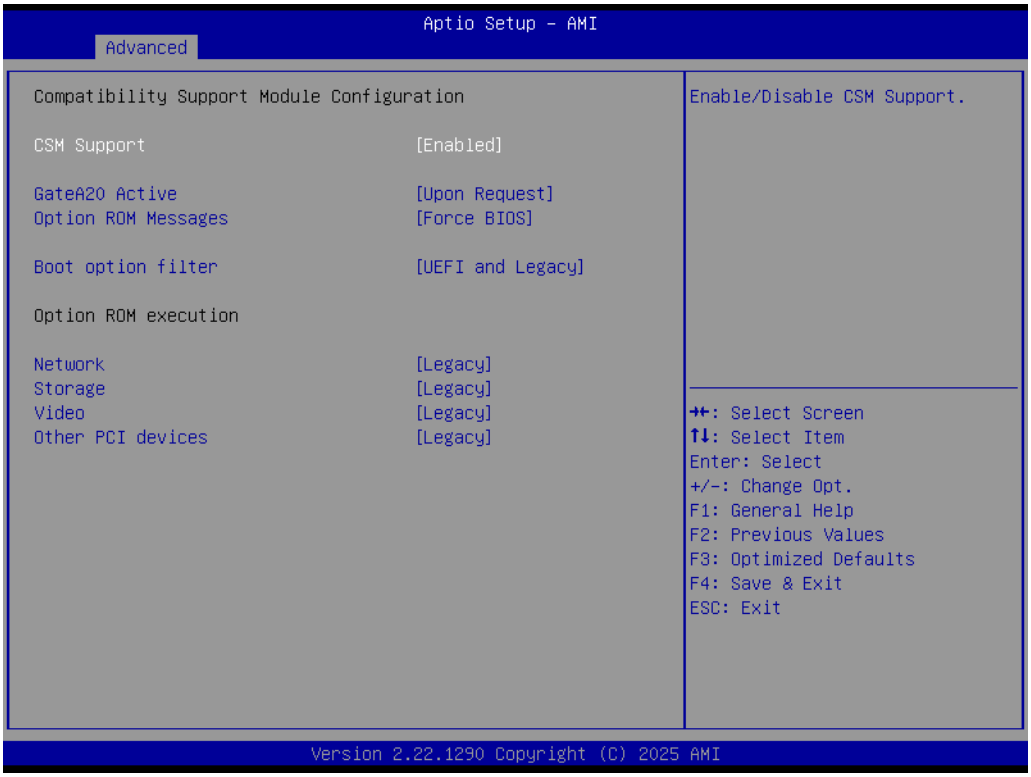


3.2.2.9 Network Stack Configuration

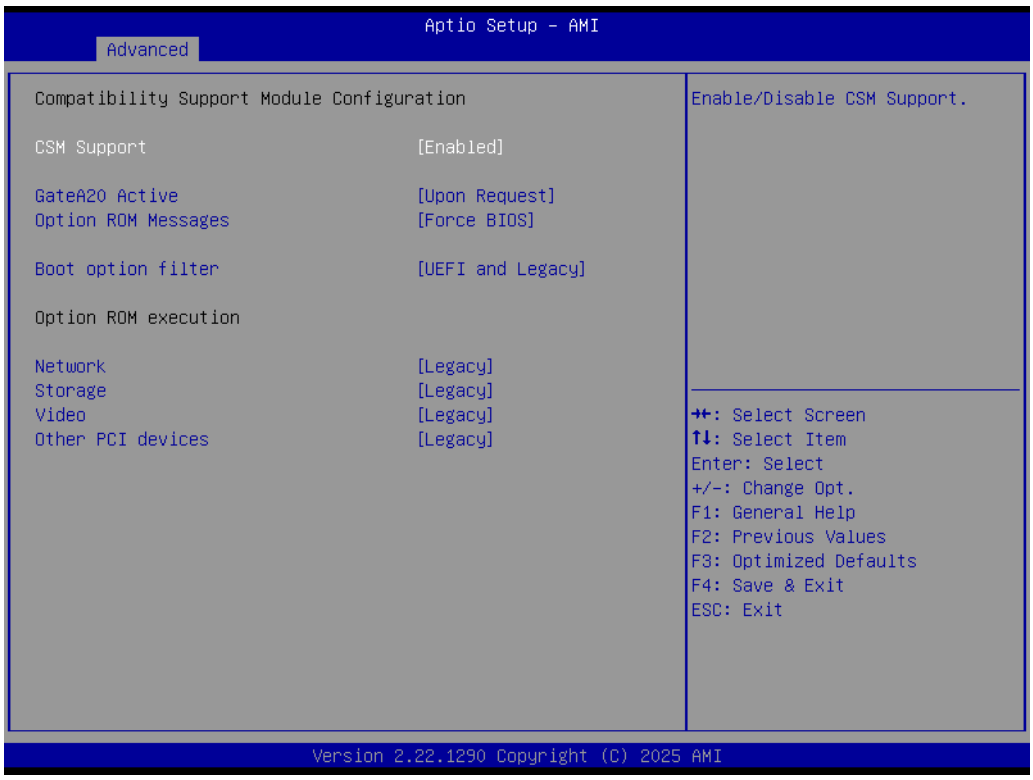


- **Network Stack [Enable]**
  Enable or disable UEFI network stack.

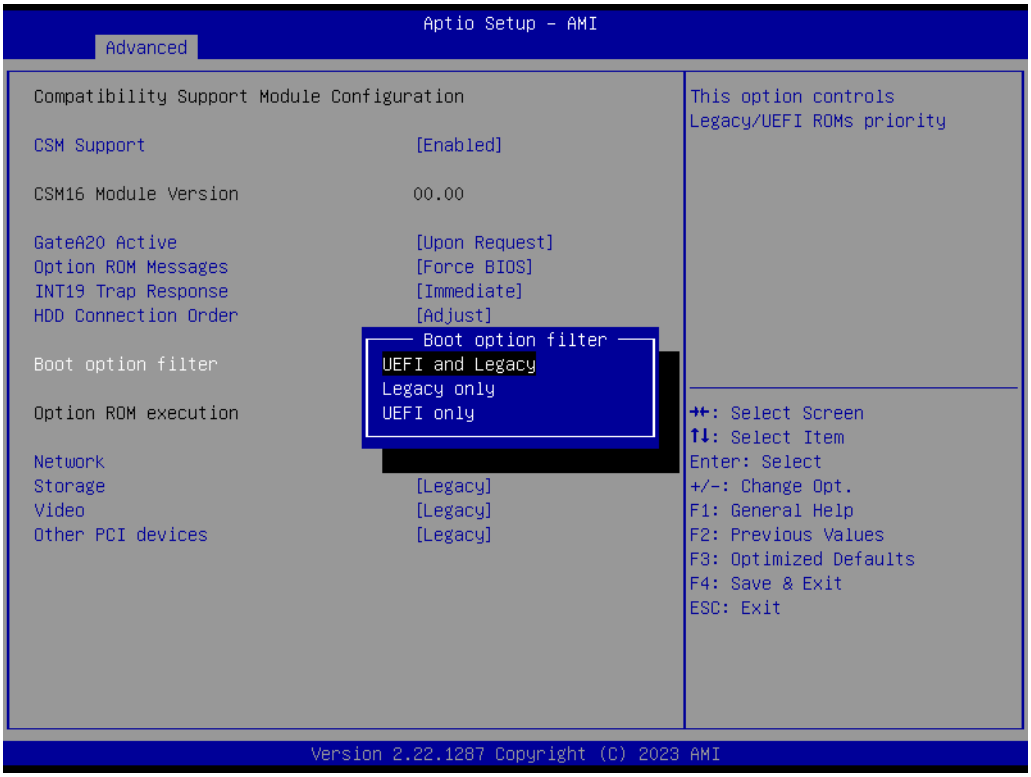
3.2.2.10 CSM Configuration



- **CSM Support [Disable]**
  Enable or disable UEFI CSM (Compatibility Support Module) to support a legacy PC boot process.
- **GateA20 Active [Upon Request]**
  This items is useful when RT code is executed above 1MB. When it's set as "Upon Request", GA20 can be disabled using BIOS services. When it's set as "Always", it does not allow disabling GA20.

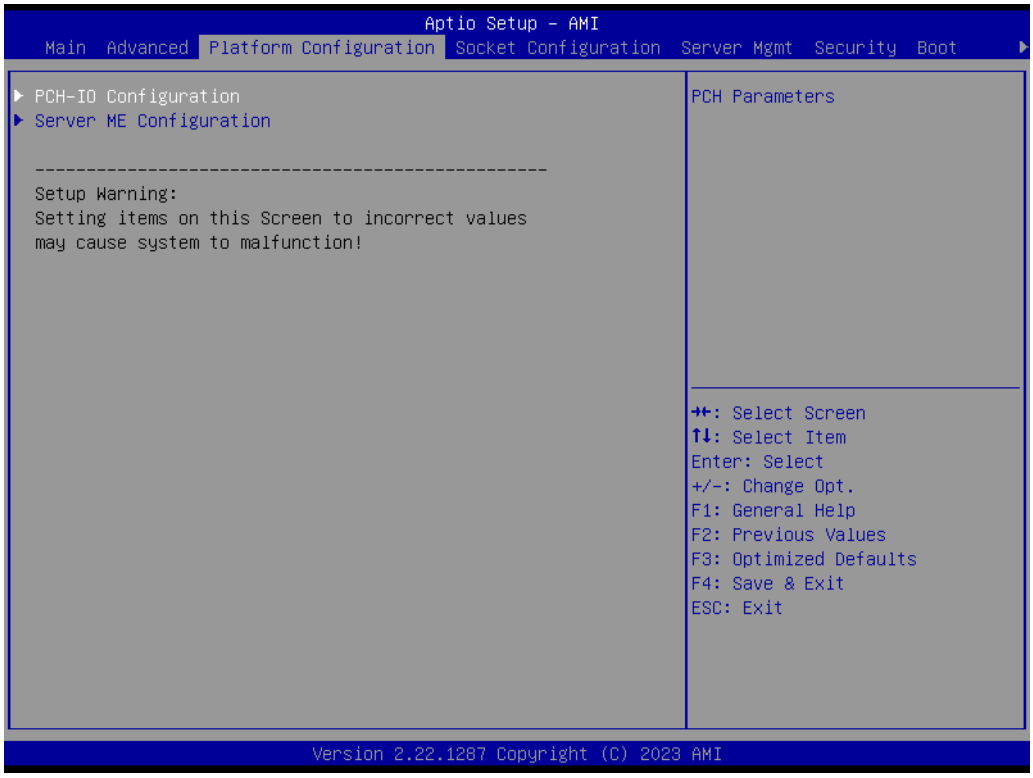


- **Option ROM Messages [Force BIOS]**
  To Force BIOS or Keep Current to set the display mode for Option ROM.
- **Boot option filter [UEFI and Legacy]**
  Change UEFI/legacy ROM priority for boot option. "UEFI and Legacy" is the default setting.

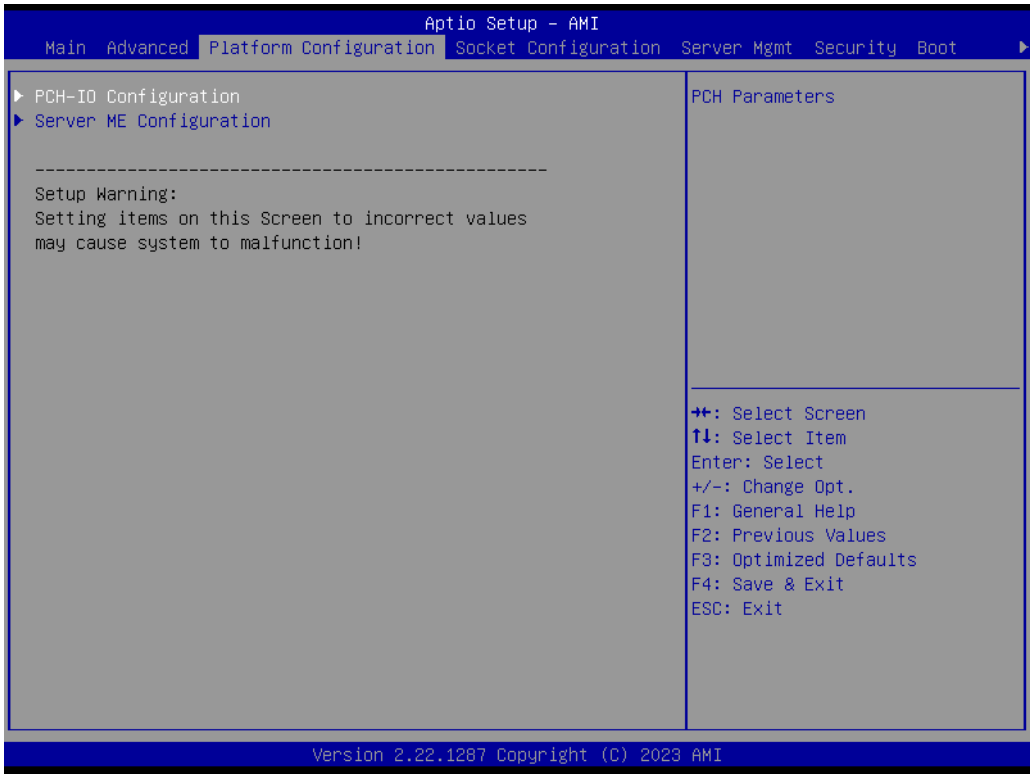


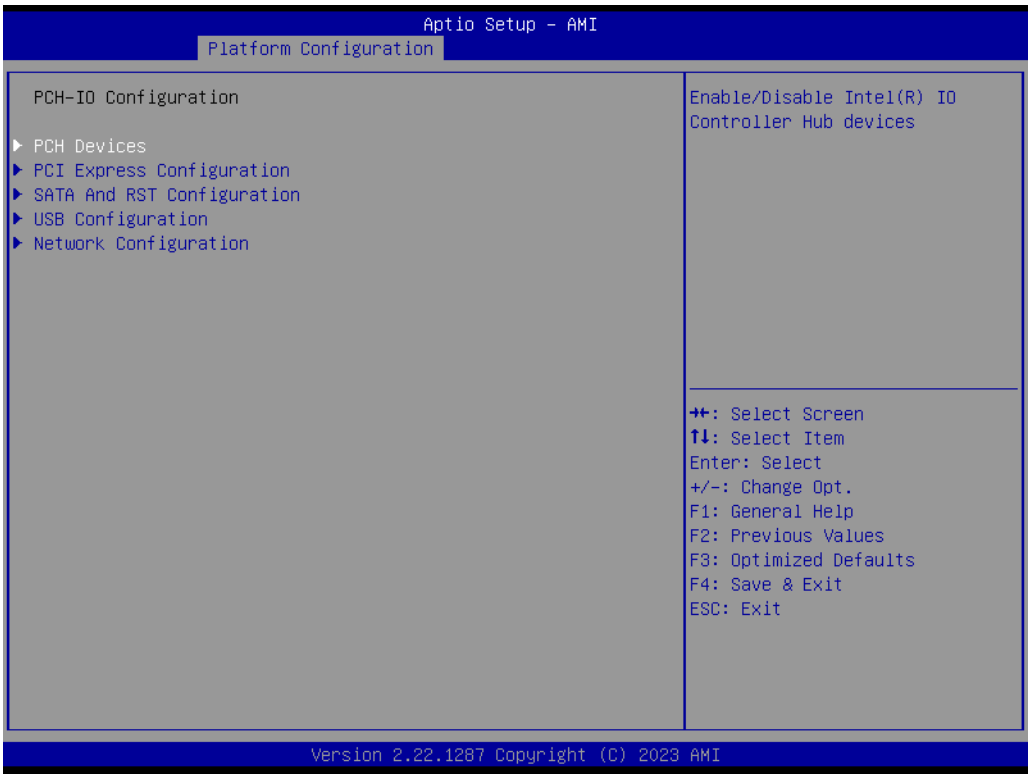
- **Network [Legacy]**
  Control the execution of UEFI and legacy PXE OpROM.
- **Storage [Legacy]**
  Control the execution of UEFI and legacy storage OpROM.
- **Video [Legacy]**
  Control the execution of UEFI and legacy video OpROM.
- **Other PCI devices [Legacy]**
  Control the execution of UEFI and legacy other PCI devices OpROM.

3.2.3 Platform Configuration



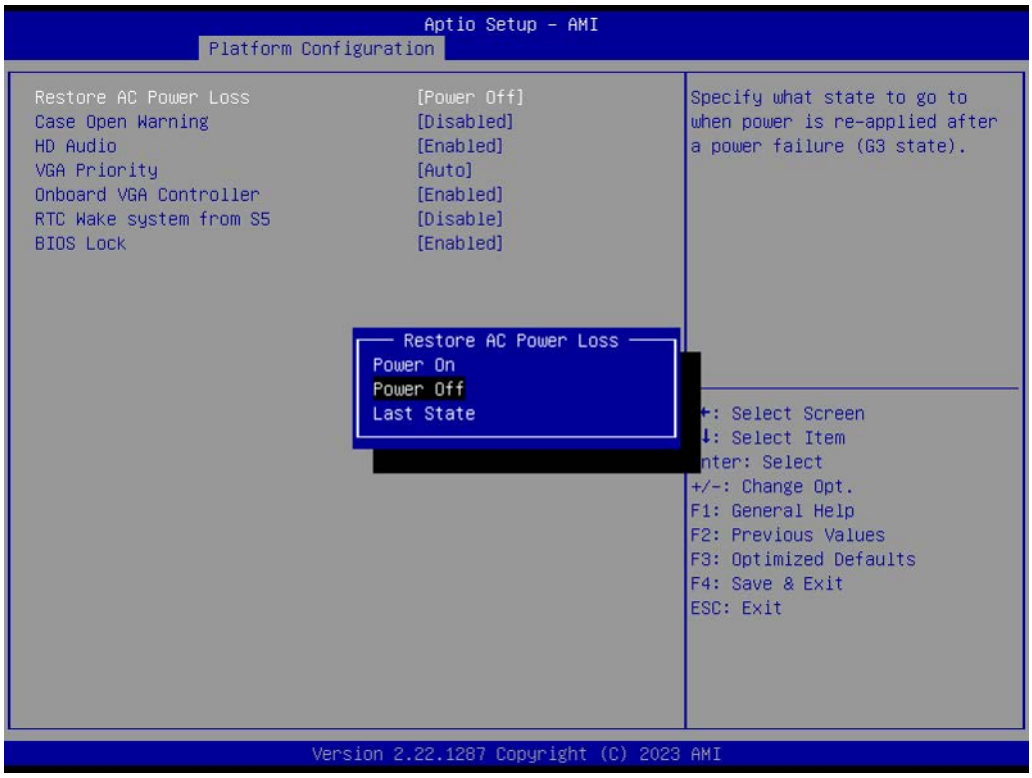
3.2.3.1 PCH-IO Configuration





- **PCH Devices**
  This item is to set up IO controller hub devices.

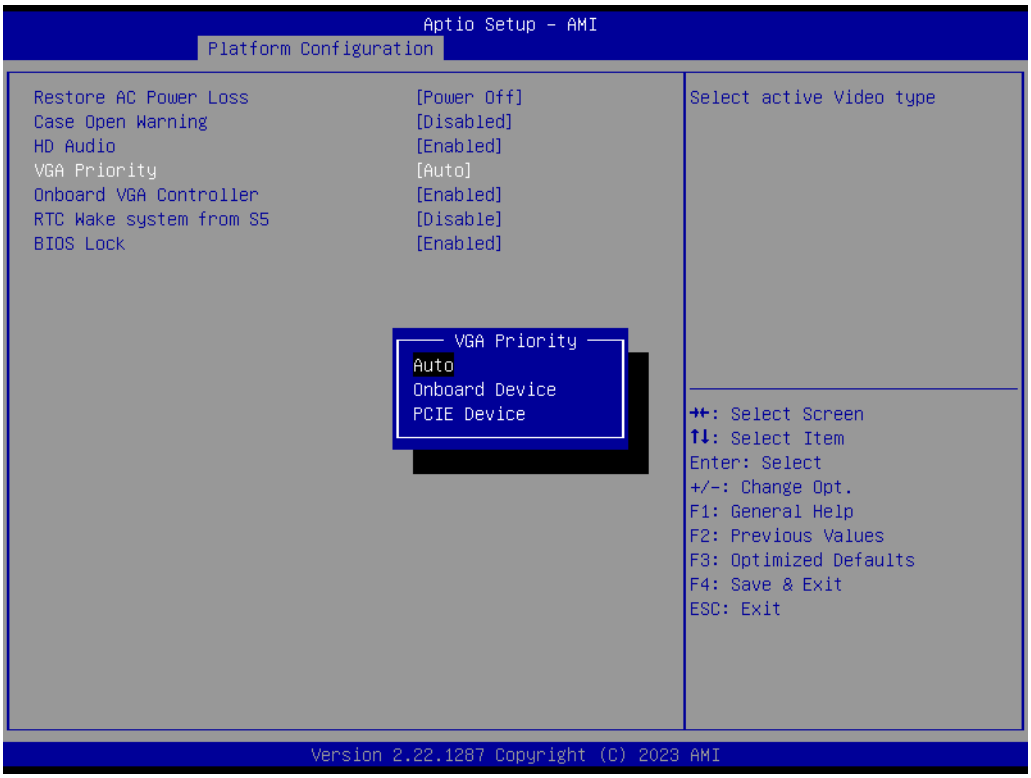
■ **Restore AC Power Loss [Power Off]**
Specify what state to go to when power is re-applied after a power failure (G3 state). It can be set to "Power on", "Power Off", and "Last State" states.



– **Case Open Warning [Disable]**
Enable or disable the chassis intrusion monitoring function. When enabled and the case is opened, the warning message will show in POST screen.

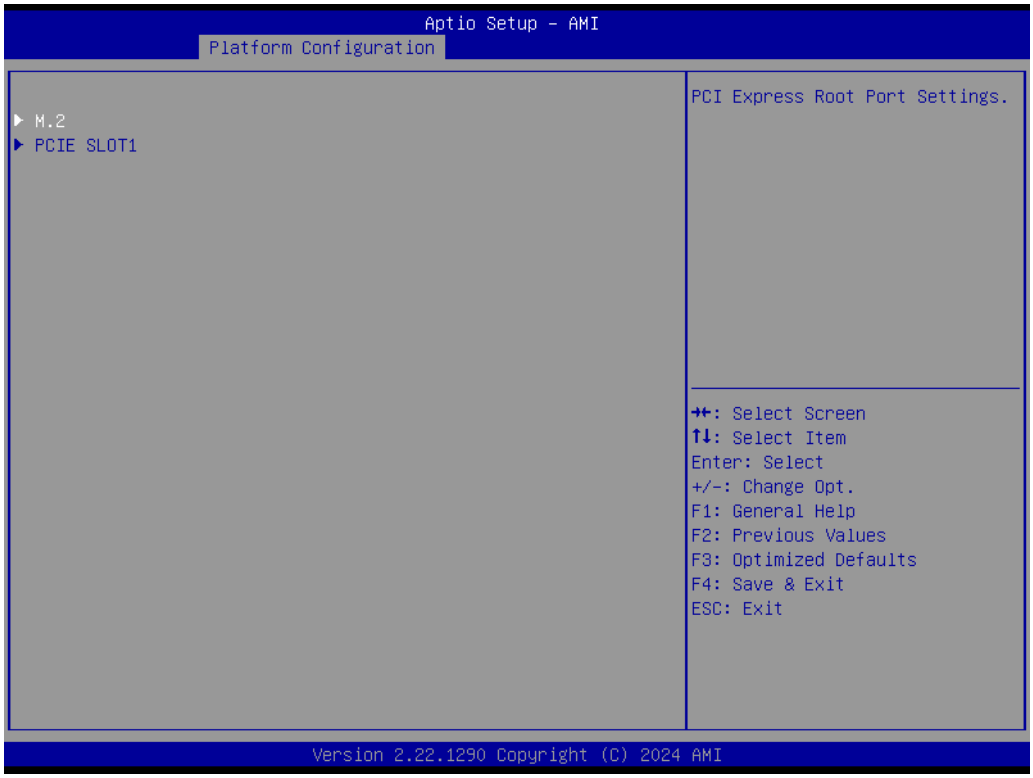
– **HD Audio [Enable]**
Enable or disable HD audio device.

– **VGA Priority [Auto]**
  Determines priority between onboard and 1st off-board video device found. "Auto" is the default setting.

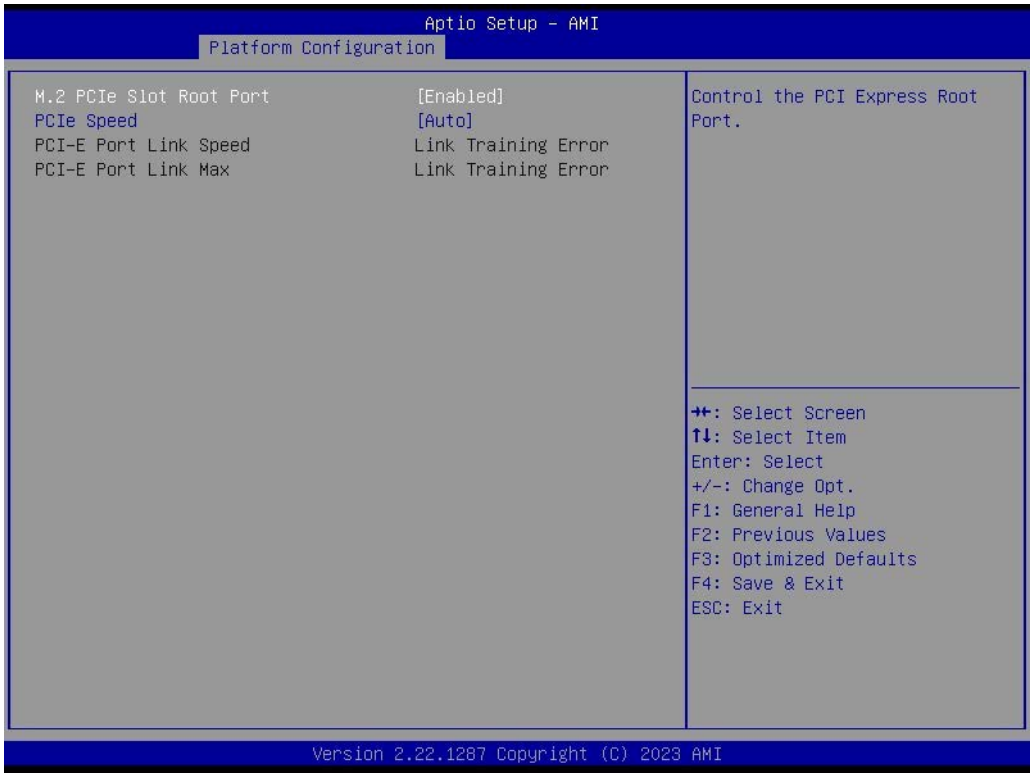


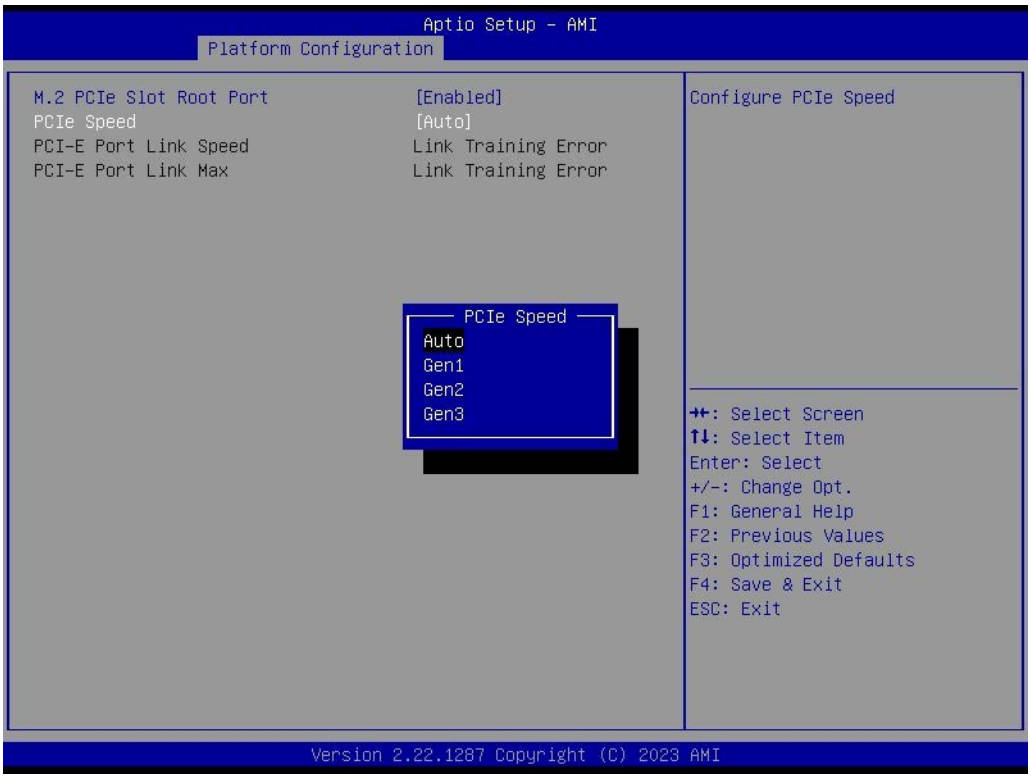
– **Onboard VGA Controller [Enable]**
  Enable or disable onboard VGA controller.
– **RTC Wake system from S5 [Disable]**
  Enable or disable system wake on alarm event.
– **BIOS lock [Enable]**
  Enable or disable BIOS lock protection

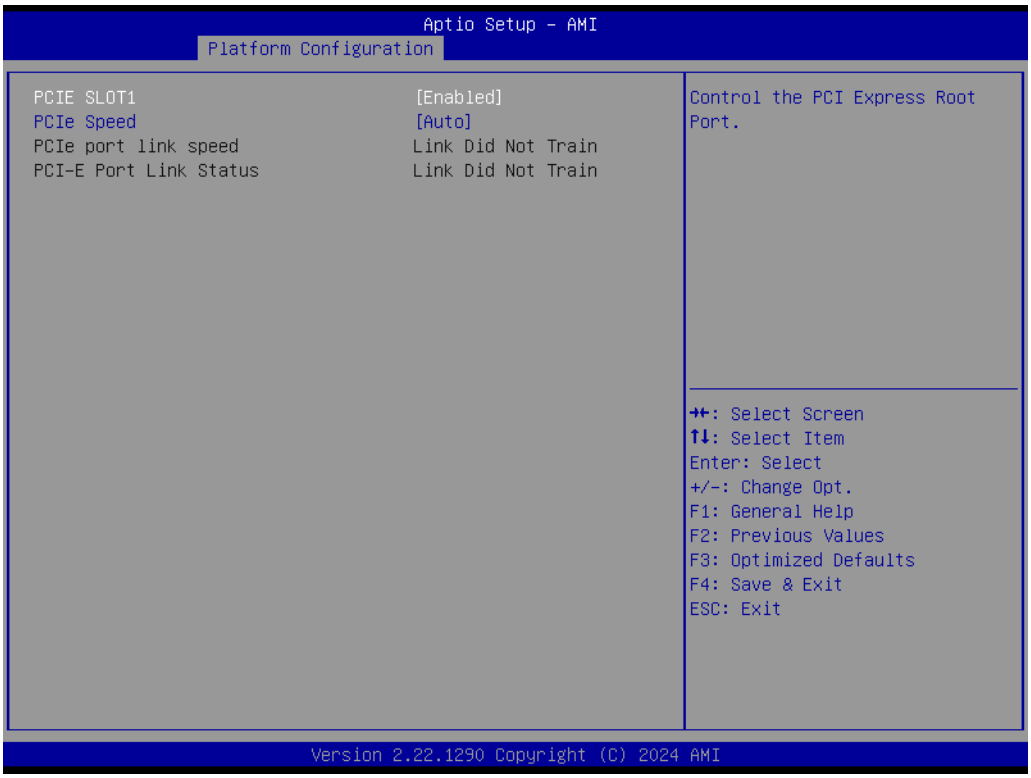
PCI Express Configuration



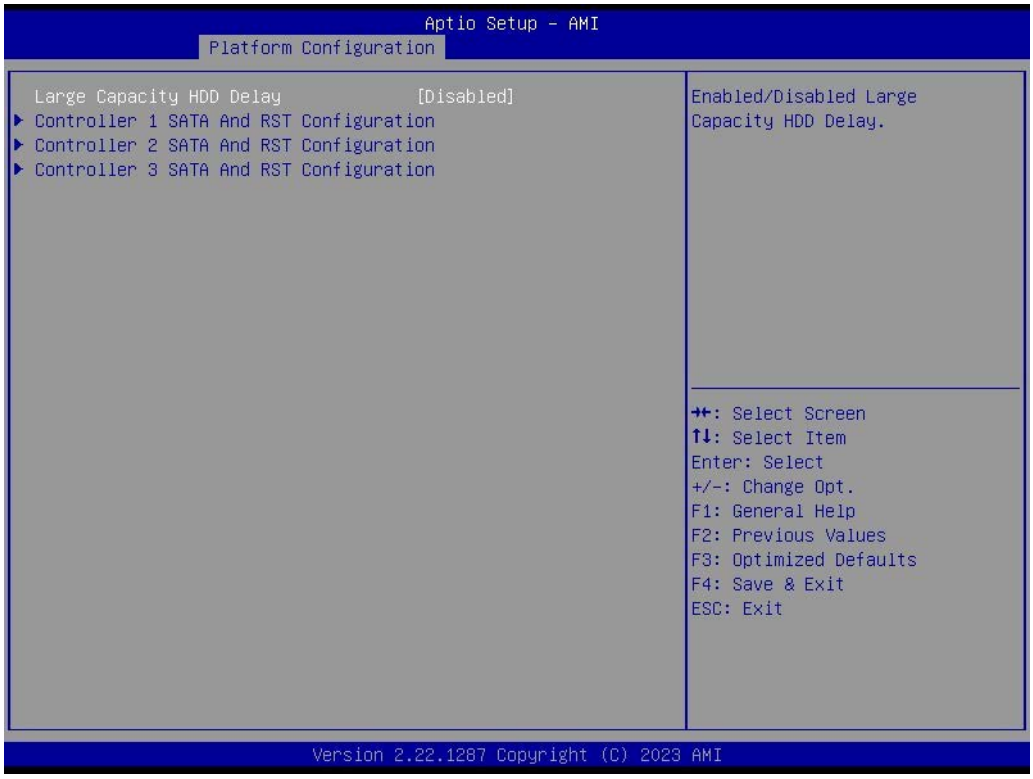
– **M.2 PCIe Slot Root Port**
  PCIe root port settings.
– **PCIE slot 1**
  Root port setting for PCIe slot 1

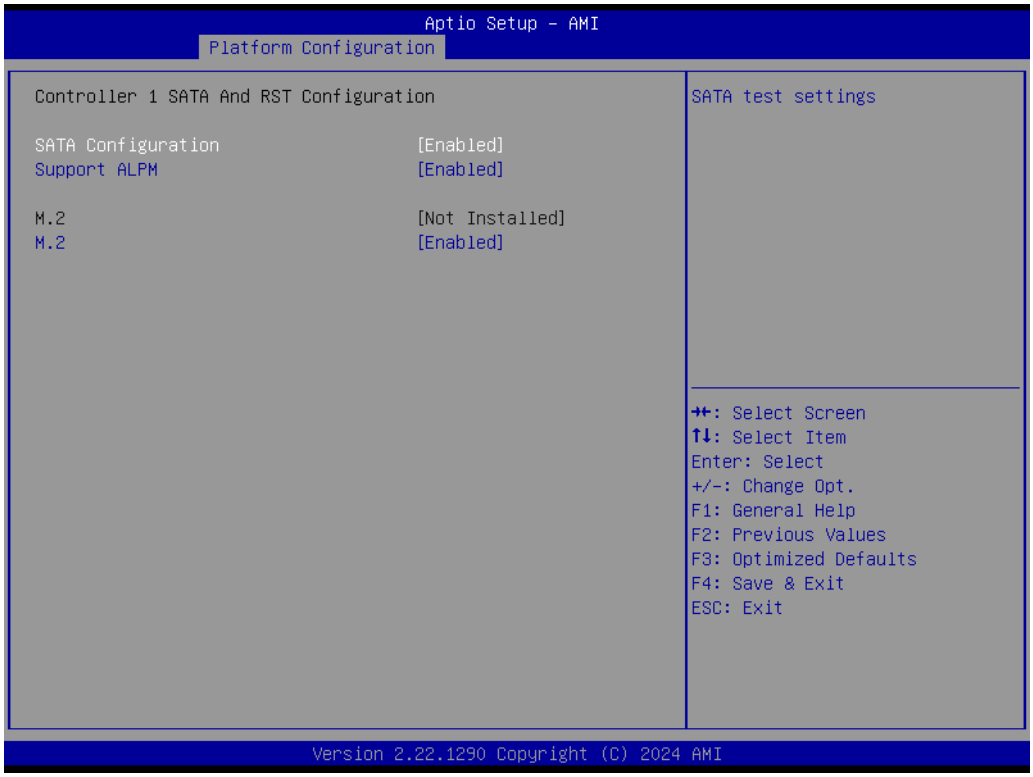


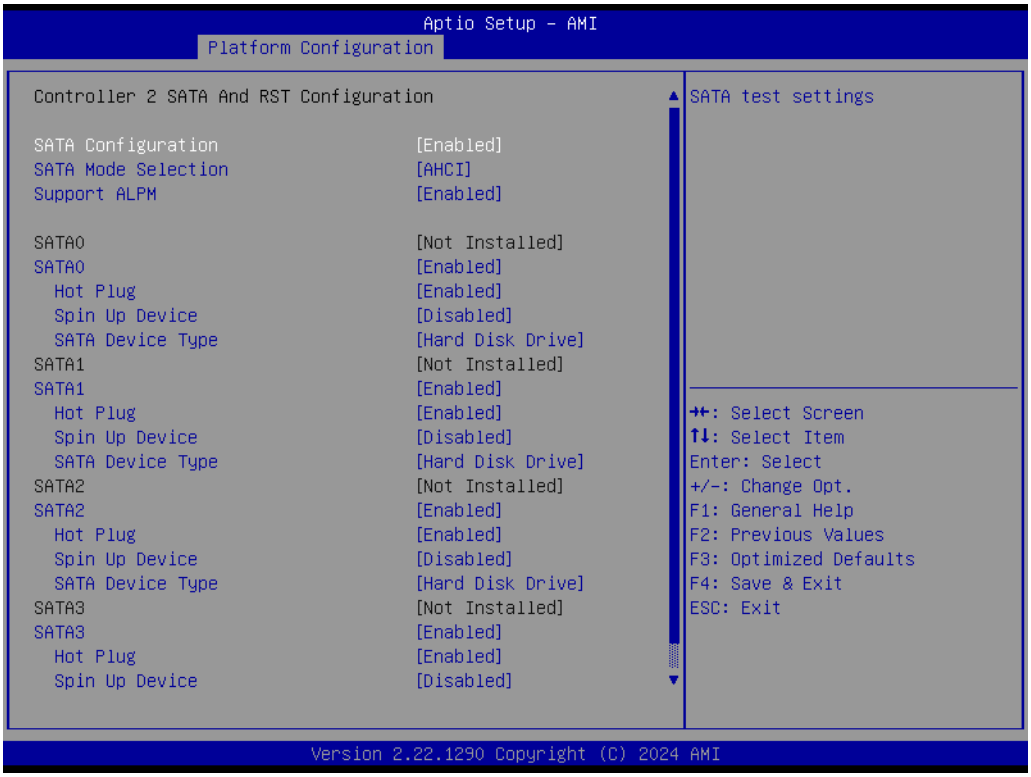

Aptio Setup – AMI
Platform Configuration
M.2 PCIe Slot Root Port [Enabled]
PCIe Speed [Auto]
PCI-E Port Link Speed Link Training Error
PCI-E Port Link Max Link Training Error
PCIe Speed
Auto
Gen1
Gen2
Gen3
Configure PCIe Speed
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.22.1287 Copyright (C) 2023 AMI

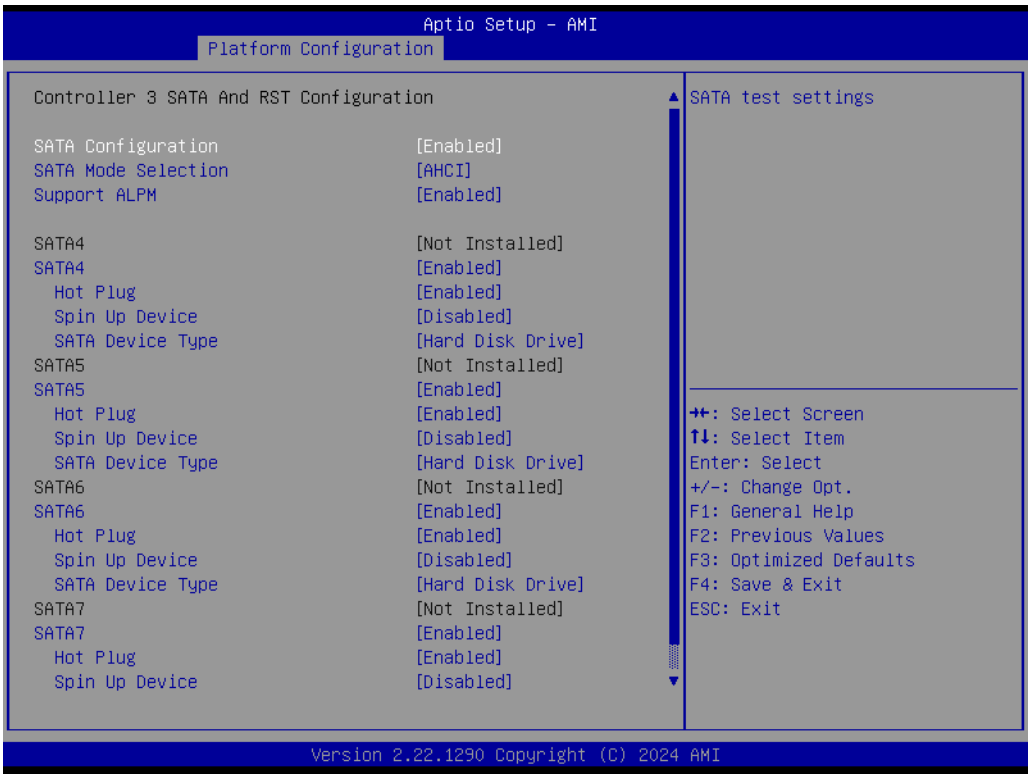

Aptio Setup – AMI
Platform Configuration
PCIE SLOT1 [Enabled]
PCIe Speed [Auto]
PCIe port link speed Link Did Not Train
PCI-E Port Link Status Link Did Not Train
Control the PCI Express Root Port.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.22.1290 Copyright (C) 2024 AMI

SATA RST Configuration







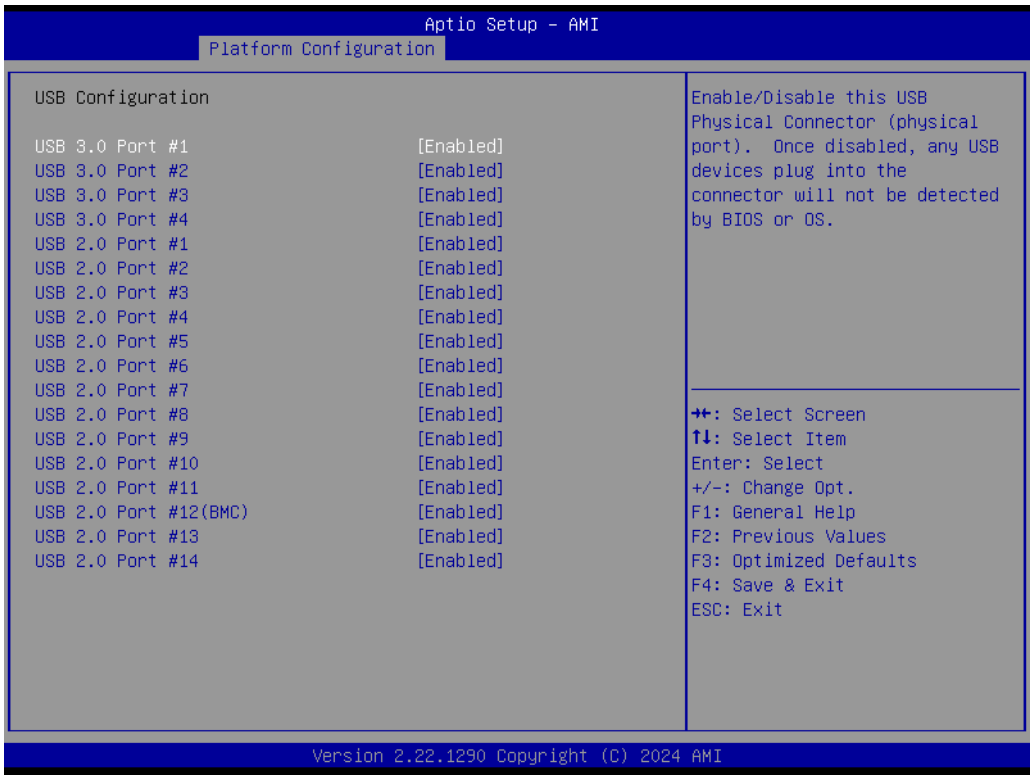


- **SATA Configuration [Enable]**
  Enable or disable SATA devices.
- **SATA Mode Selection [AHCI]**
  Set as AHCI or RAID when SATA controllers are enabled.

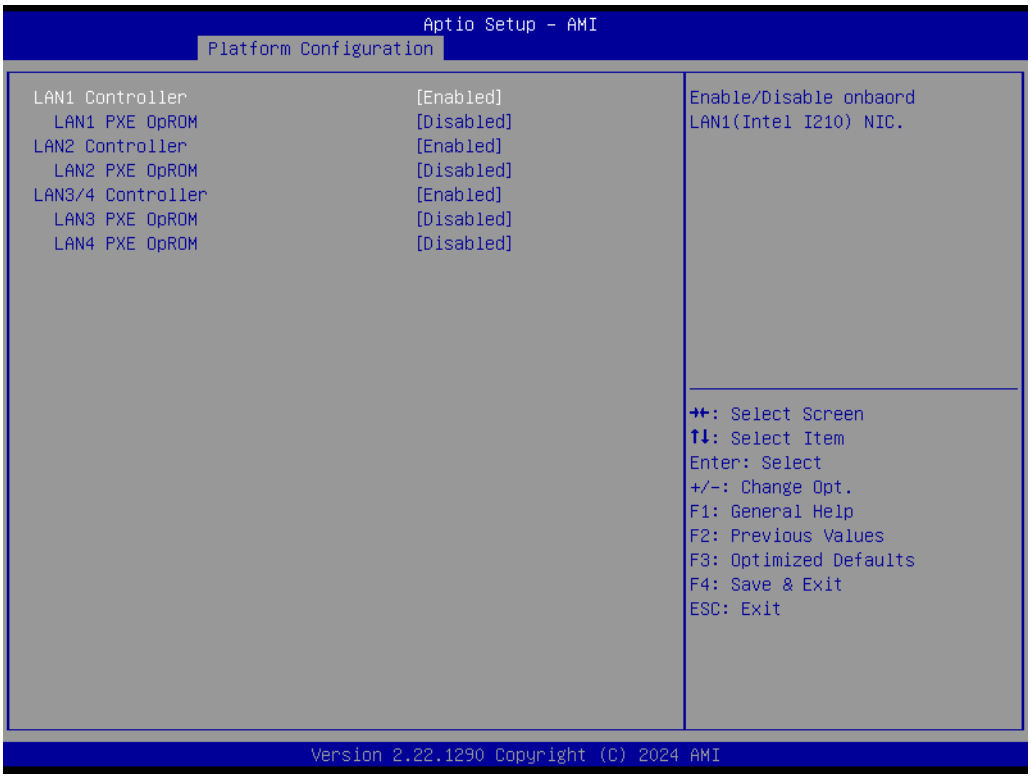
- **Support ALPM [Enable]**
  Enable or disable Aggressive Link Power Management (ALPM) protocol for Advanced Host controller Interface-compliant (AHCI) Serial ATA (SATA) devices.
- **SATA Port 0~7(M.2 for Controller 1, SATA port 0~3 are for Controller 2, SATA port 4~7 are for Controller 3) [Enable]**
  Enable or disable SATA port 0~7 and M.2.
- **Hot Plug Port 0~7 (SATA port 0~3 are for Controller 2, SATA port 4~7 are for Controller 3) [Disable]**
  Designates SATA port 0~7 as hot pluggable. "Disabled" is the default setting.
- **SATA Port 0~7 Spin Up Device (SATA port 0~3 are for Controller 2, SAT-Aport 4~7 are for Controller 3) [Disable]**
  On an edge detect from 0 to 1, the PCH starts a COM RESET initialization sequence to the device. "Disabled" is the default setting.
- **SATA Port 0~7 Device Type (SATA port 0~3 are for Controller 2, SATA port 4~7 are for Controller 3) [Hard Disk Drive]**
  To identify the SATA is connected to Solid State Drive or Hard Disk Drive.

USB Configuration

Enable/Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS.



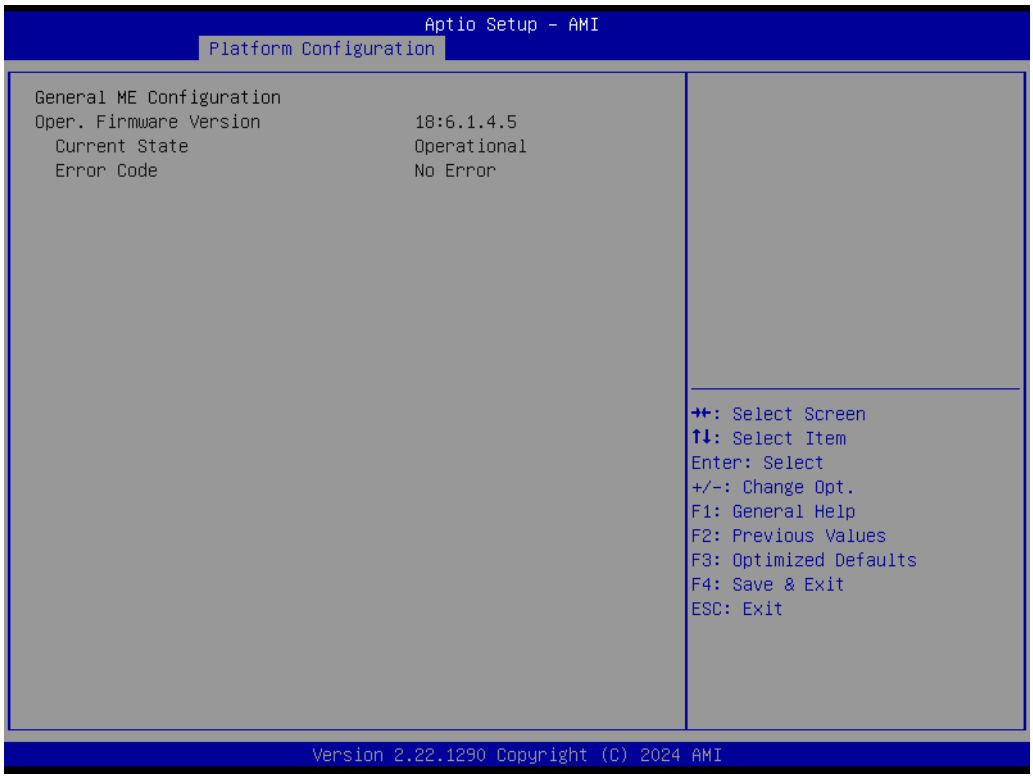
Networking



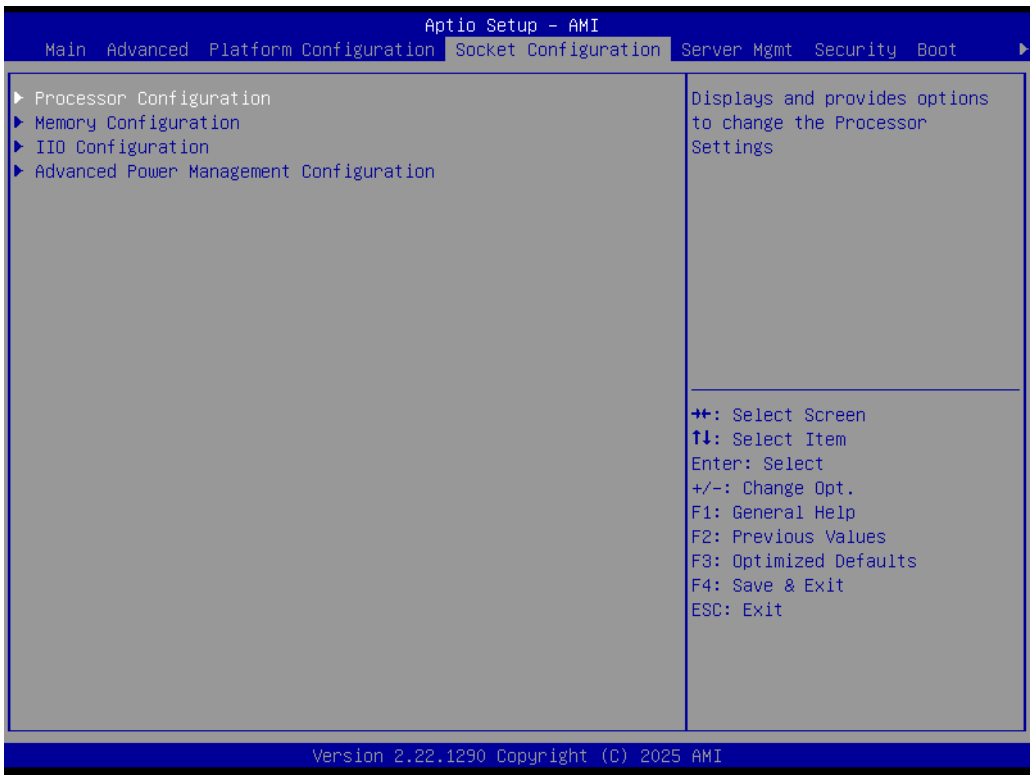
- **LAN1 Controller [Enable]**
  Enable or disable Intel I210 controller support.
- **LAN1 PXE OpROM [Disable]**
  Enable or disable Boot option for Intel I210 controller.
- **LAN2 Controller [Enable]**
  Enable or disable Intel I210 controller support.
- **LAN2 PXE OpROM [Disable]**
  Enable or disable Boot option for Intel I210 controller.
- **LAN3/4 Controller [Enable]**
  Enable or disable Intel X710 controller support
- **LAN3 PXE ROM**
- **LAN4 PXE ROM [Disable]**
  Enable or disable boot option for Intel X710 controller

3.2.3.2 Server ME Configuration

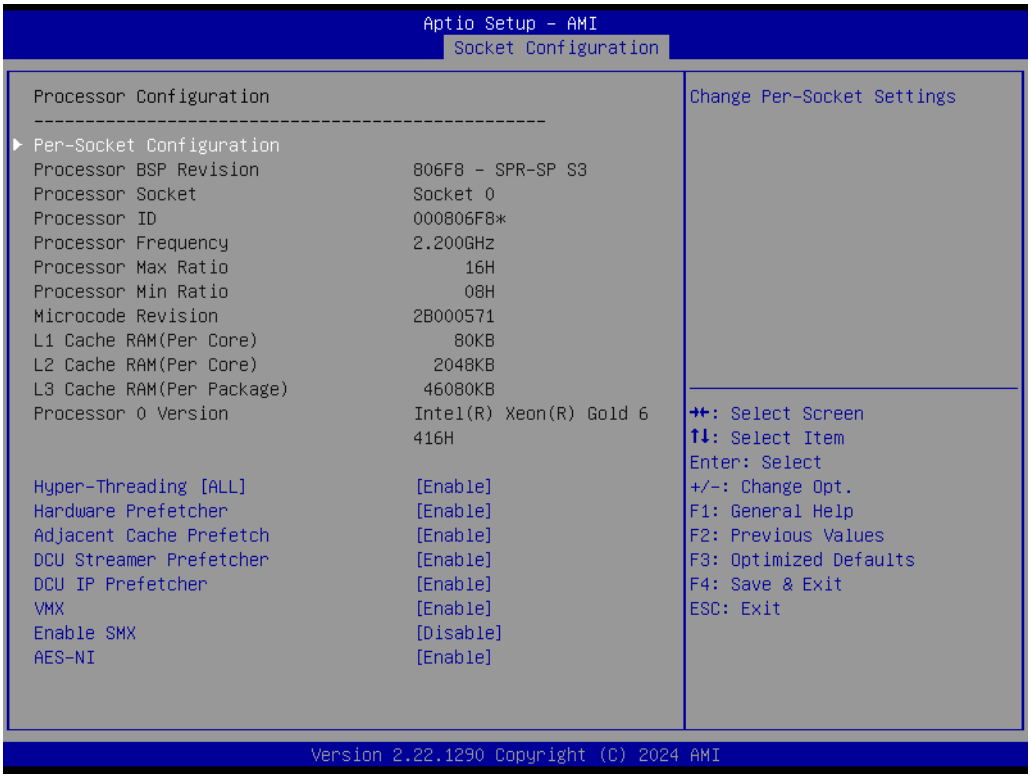
This page shows the Server ME configuration information.



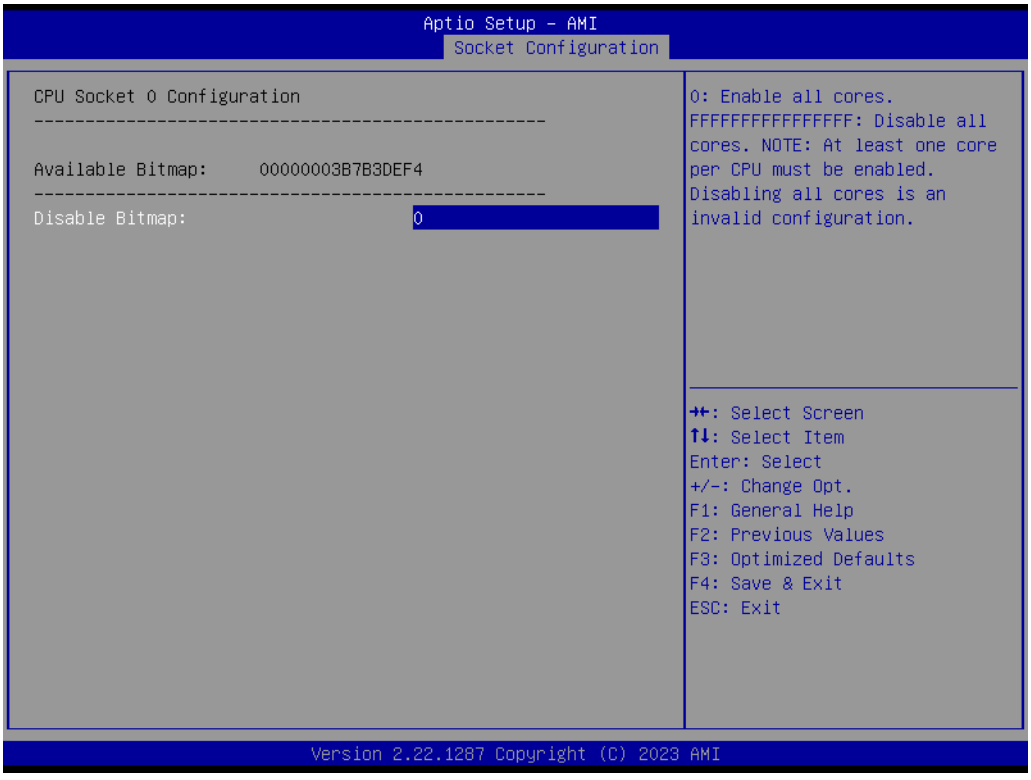
3.2.4 Socket Configuration



3.2.4.1 Processor Configuration



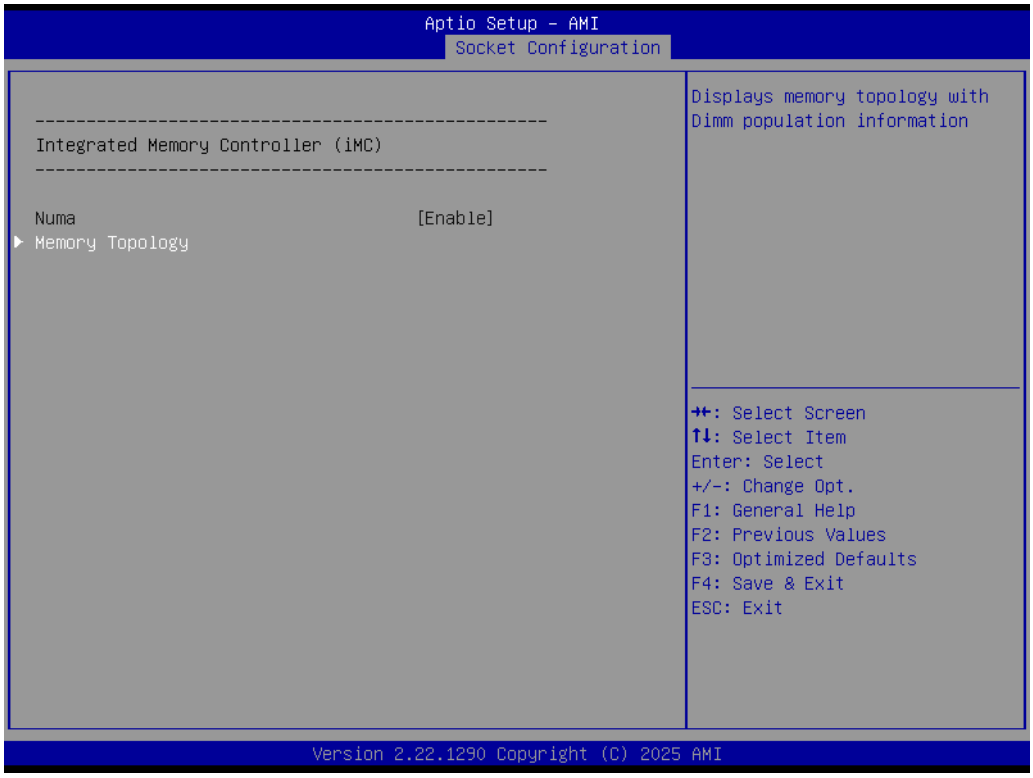
- **Per-Socket Configuration**
  Use this to select how many processor cores you want to activate when you are using a dual or quad core processor.



- **Hyper-threading [All]**
  Enable or disable Intel Hyper Threading technology.

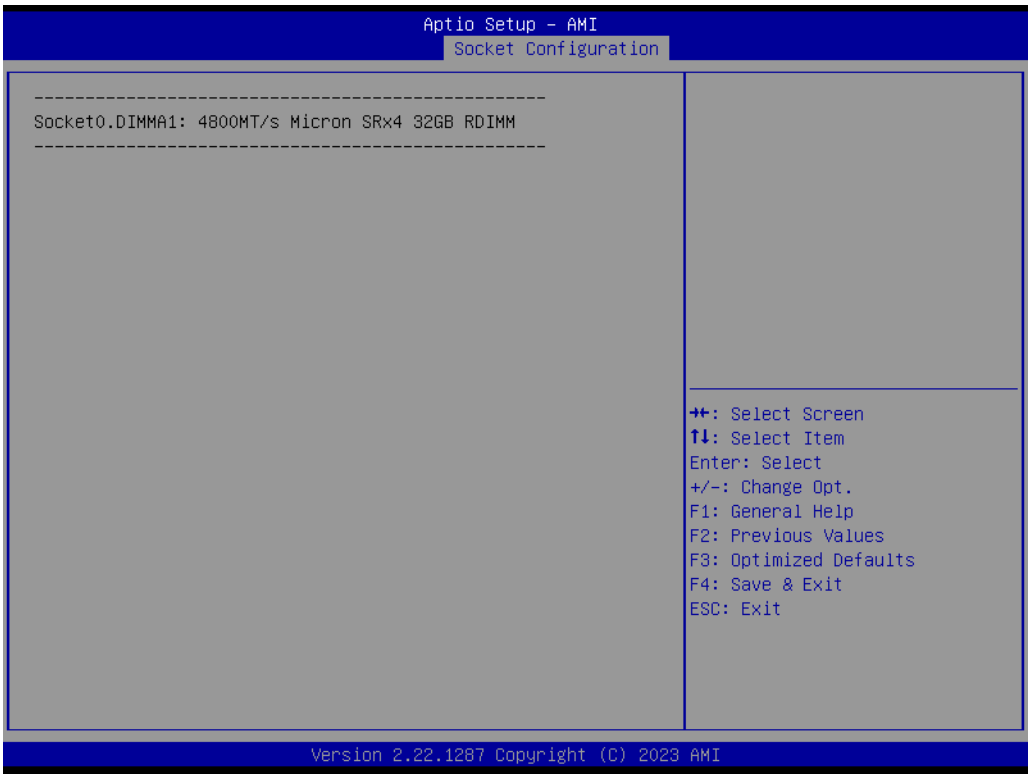
- **Hardware Prefetcher [Enable]**
  Hardware Prefetcher is a technique that fetches instructions and/or data from memory into the CPU cache memory well before the CPU needs it, so that it can improve the load-to-use latency. “Enable" is the default setting.
- **Adjacent Cache Prefetch [Enable]**
  The Adjacent Cache-Line Prefetch mechanism, like automatic hardware prefetch, operates without programmer intervention. When enabled through the BIOS, two 64-byte cache lines are fetched into a 128-byte sector, regardless of whether the additional cache line has been requested or not. “Enable" is the default setting.
- **DCU Streamer Prefetcher [Enable]**
  Enable prefetch of next L1 data line based upon multiple loads in same cache line.
- **DCU IP Prefetcher [Enable]**
  Enable prefetch of next L1 line based upon sequential load history.
- **VMX [Enable]**
  Enable or disable Intel Virtual Machine Extensions (VMX) for IA-32 processors that su+pport Intel® Vanderpool Technology.
- **Enable SMX [Disable]**
  Enable or disable Safer Mode Extensions. Safer Mode Extensions (SMX) provide a means for system software to launch an MLE and establish a measured environment within the platform to support trust decisions by end users.
- **AES-NI [Enable]**
  This item is to Enable or disable CPU advanced encryption standard instructions. “Enable" is the default setting.

3.2.4.2 Memory Configuration

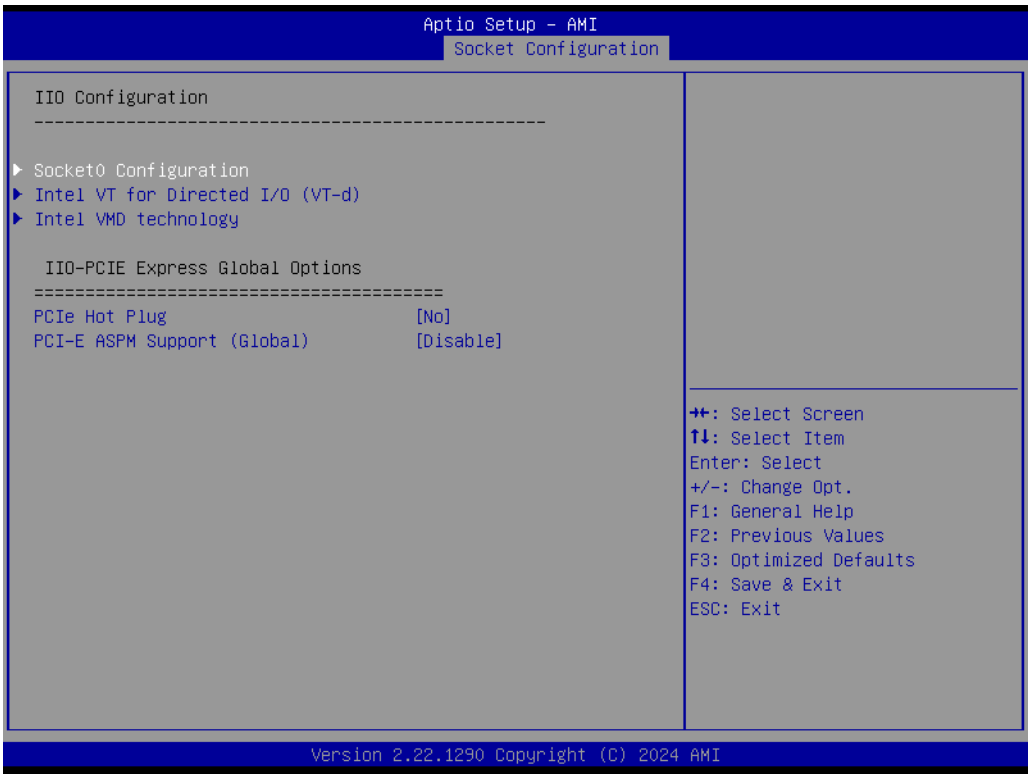


- **Numa [Enable]**
  Enable or disable non uniform memory access (NUMA). The Numa function is used by dual CPUs.

- **Memory Topology**
  Display memory topology with DIMM population information.



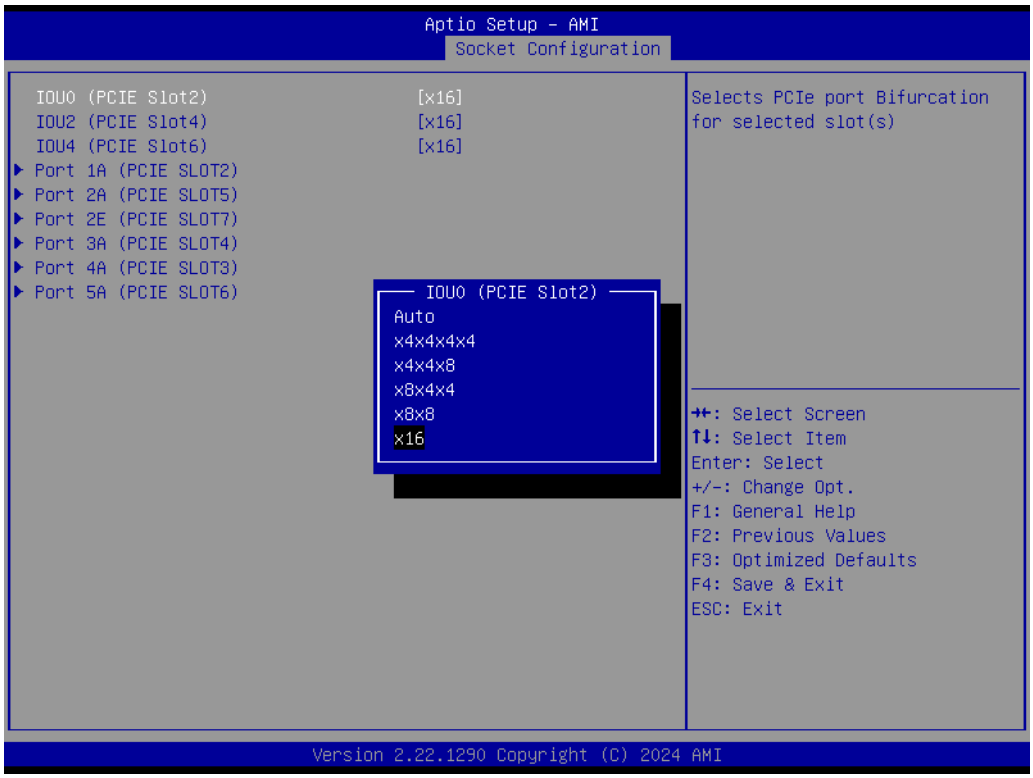
3.2.4.3 IIO Configuration

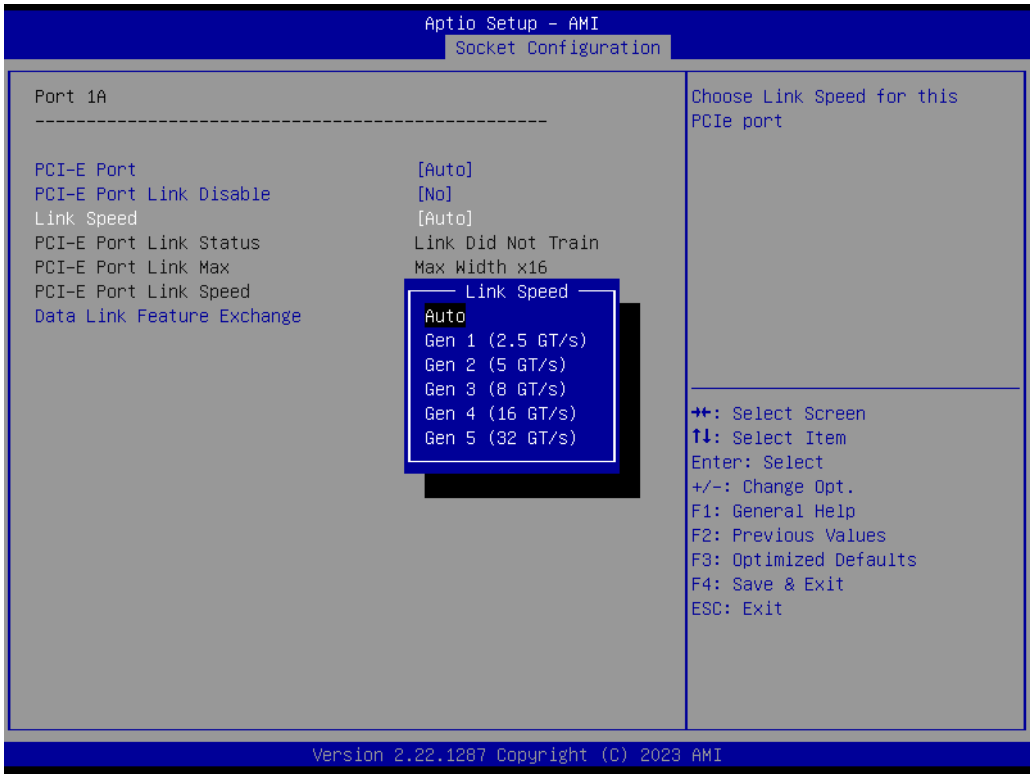


- **PCIE hot plug [Disable]**
  Enable or Disable hot plug support for all PCIe slots
- **PCI-E ASPM Support (Global) [Disable]**
  Enable or Disable ASPM support for all PCIe slots,

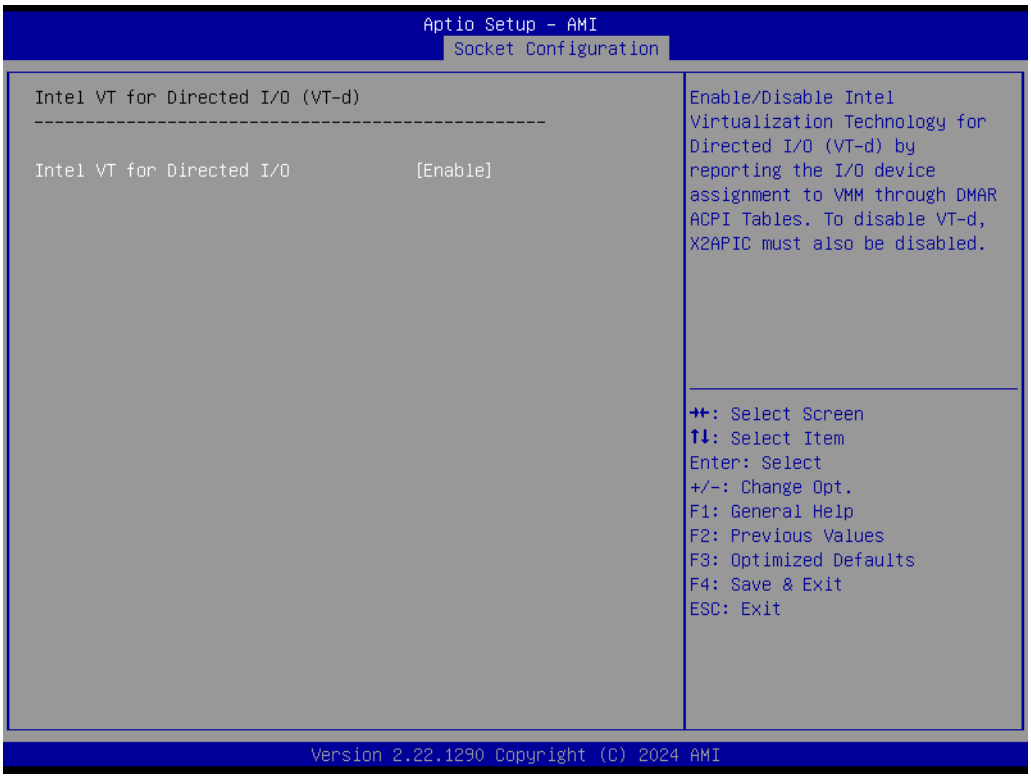
Socket0 PCIe Configuration [Auto]

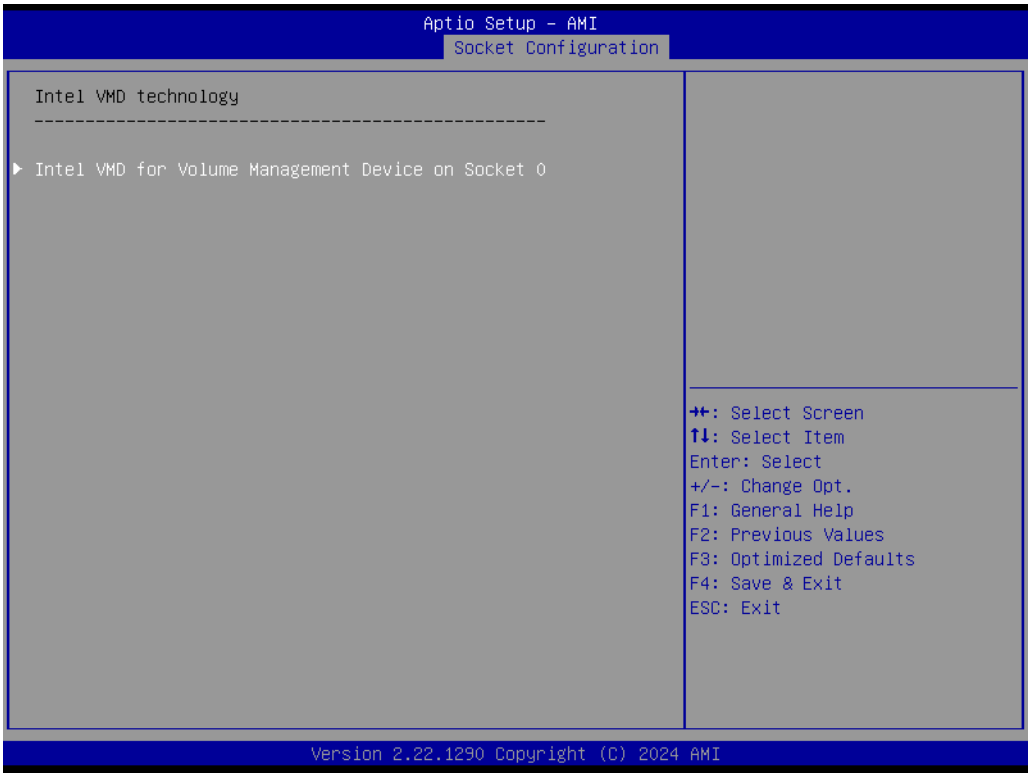
PCIe port bifurcation control and select target link speed as Gen1, Gen2, Gen3,Gen4, or Gen5. "Auto" is the default setting.

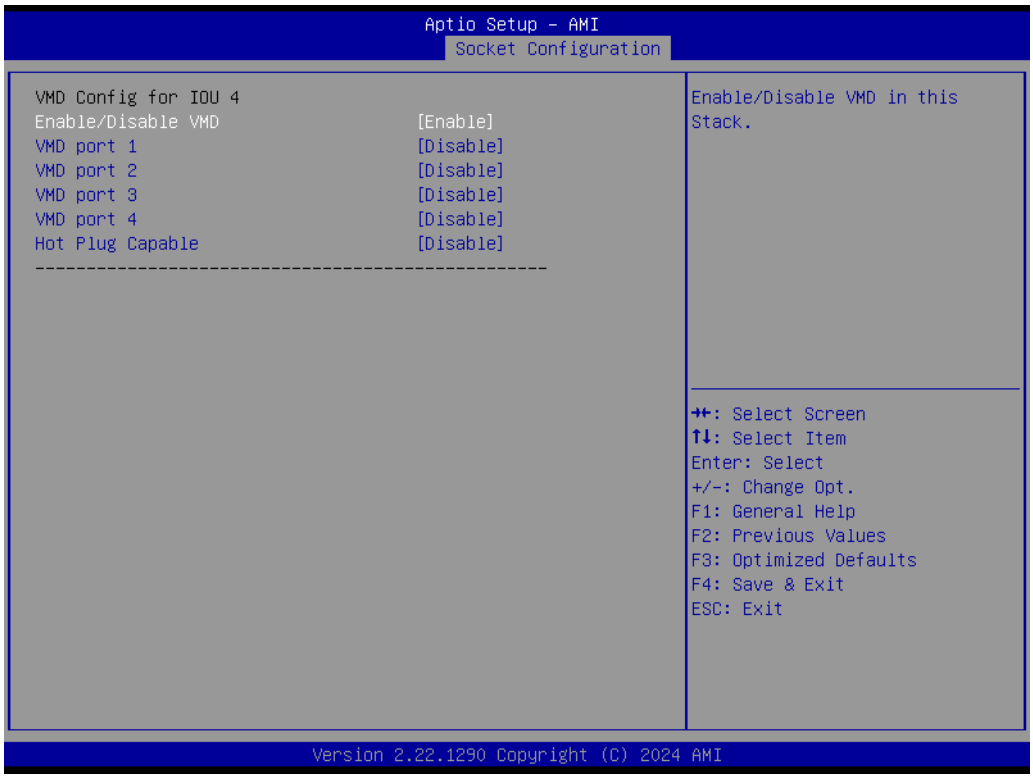




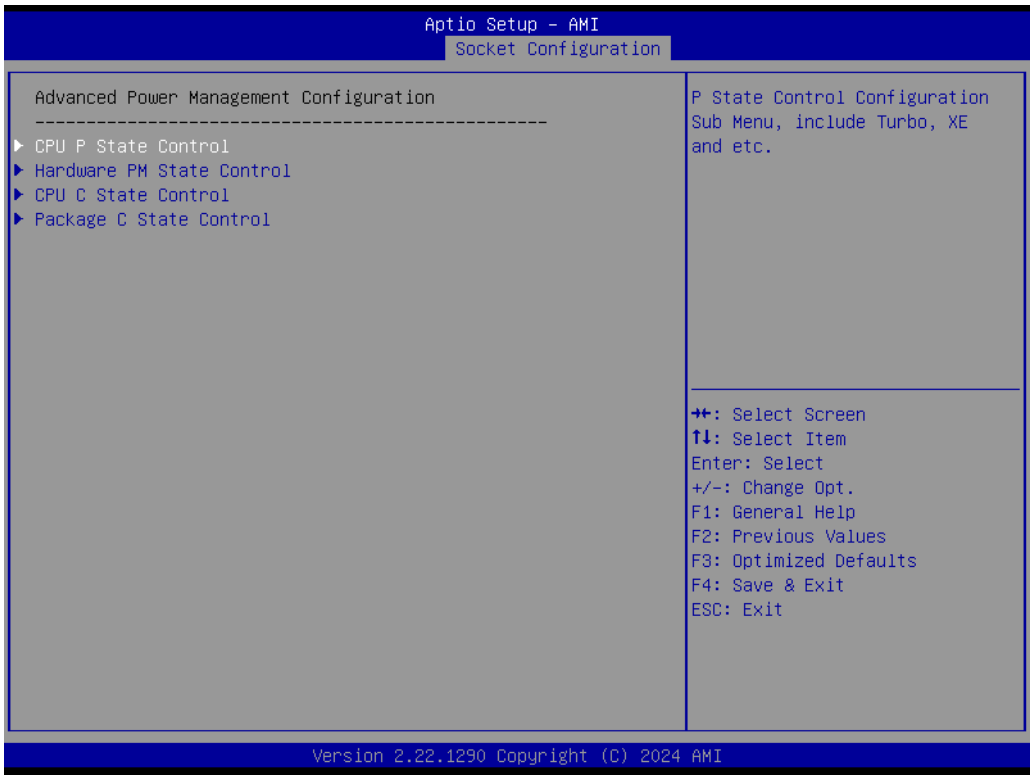
- **Intel VT for Directed I/O (VT-d) [Enable]**
  Enable or disable Intel Virtualization Technology for Directed I/O.



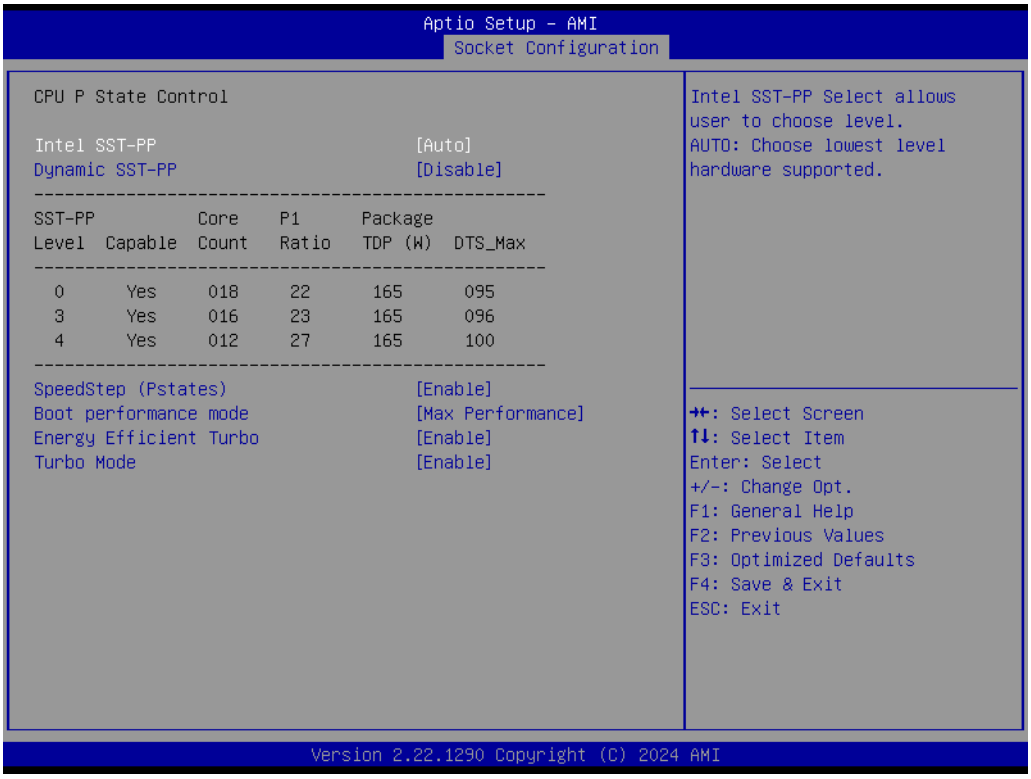




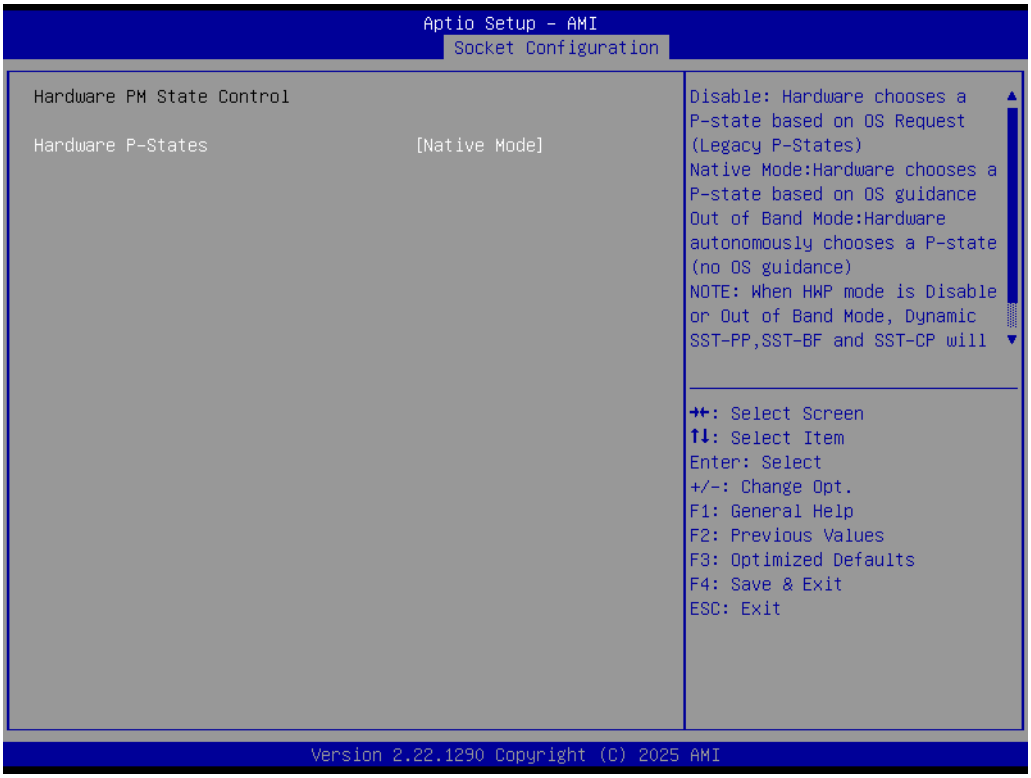
3.2.4.4 Advanced Power Management Configuration



CPU P State Control



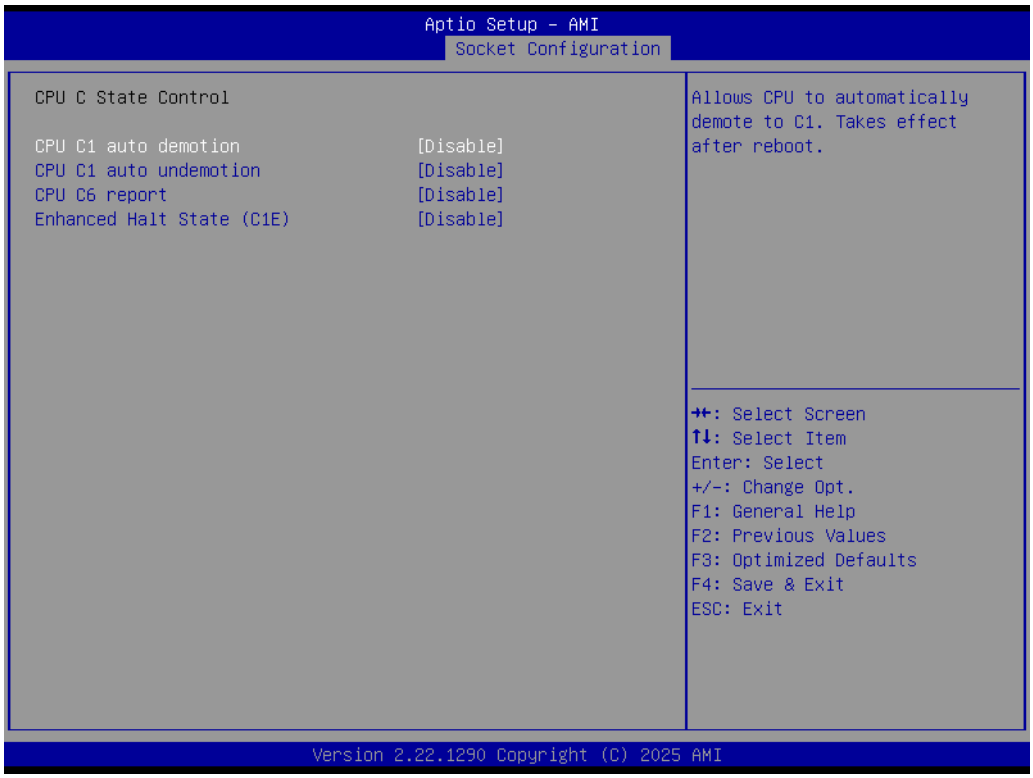
Hardware PM State Control



– **Hardware P-State [Native Mode]**
Option available: Disable, Native Mode, out of band mode or Native mode w/ o Legacy support
disable, hardware choose a P-state based on OS request.

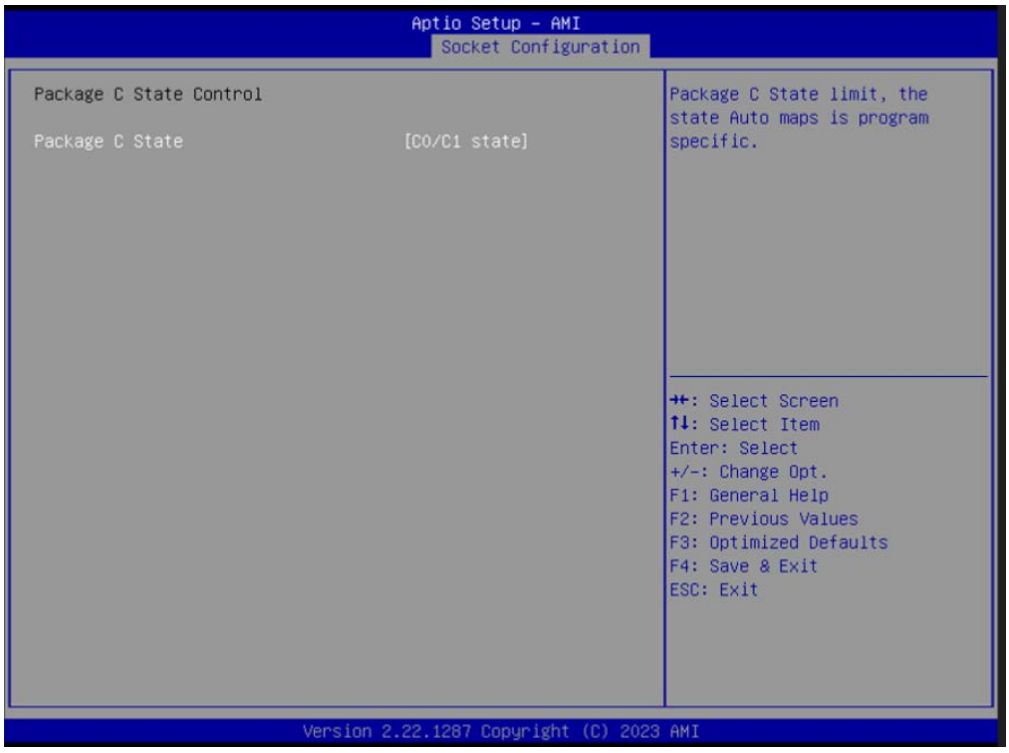
native mode, hardware choose a P-state based on OS guidance
out of band mode: hardware chose P-state w/o OS guidance

CPU C State Control

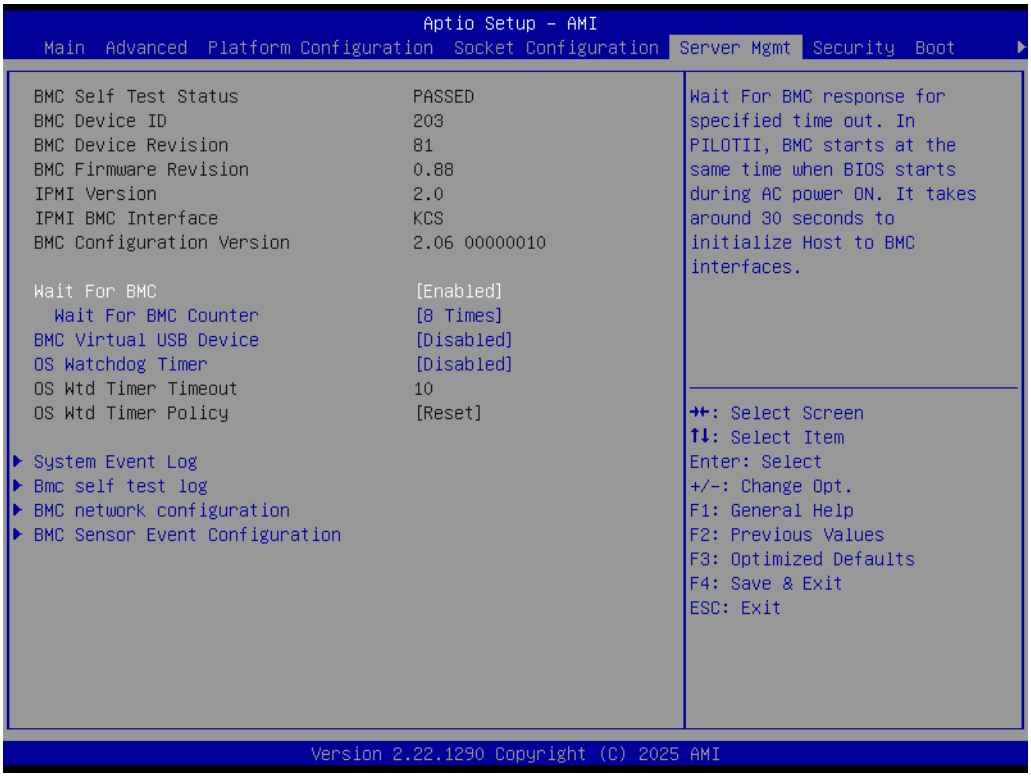


- **CPU C1 auto demotion [Disable]**
  Enable or Disable CPU to automatically demotion to C1
- **CPU C1 auto undemotion [Disable]**
  Enable or Disable CPU to automatically undemotion from C1
- **CPU C6 report [Disable]**
  Enable or Disable CPU C6 (ACPI C3) report to OS
- **Enhanced Halt State (C1E) [Disable]**
  Enable or Disable core C1E auto promotion control

- **Package C State Control**



3.2.5 Server Management



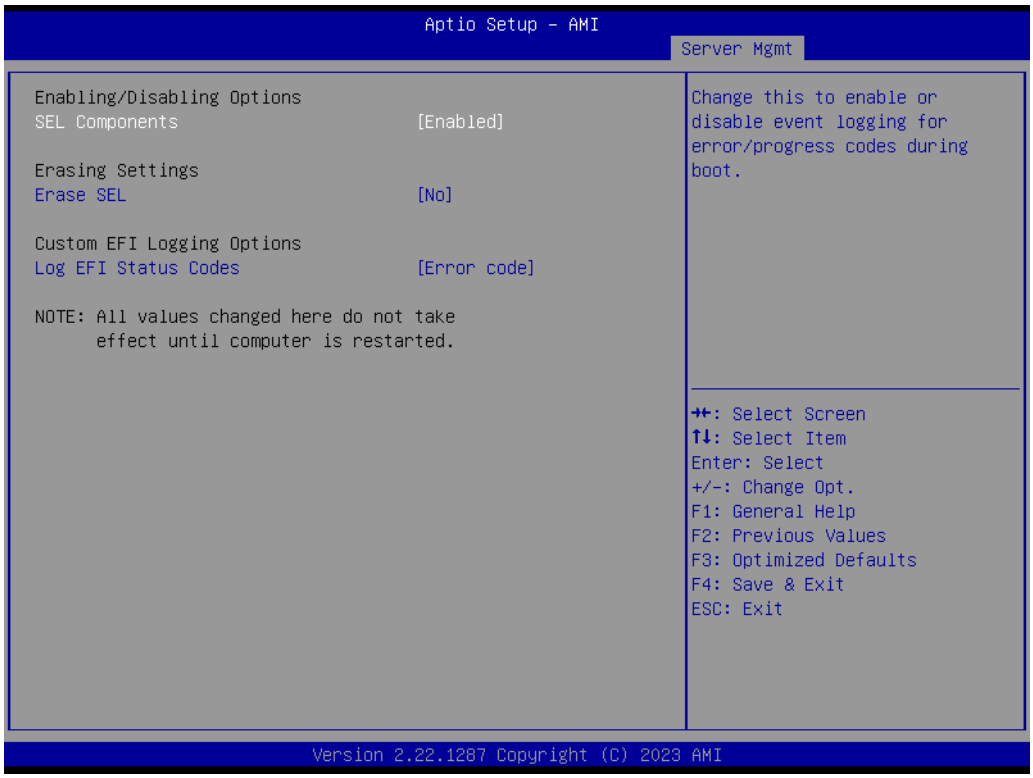
- **Wait for BMC [Disable]**

  If enabled, motherboard will wait 30 ~ 60 seconds until BMC module boots up completely. After that, the normal BIOS post screen will be displayed.

  If disabled, motherboard will not wait for BMC module's response.

- **Wait for BMC counter**
  Initialize host to BMC interfaces. The MB beeps per 5 seconds to check it.
- **OS Watchdog Timer [Disable]**
  If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. "Disabled" is the default setting.

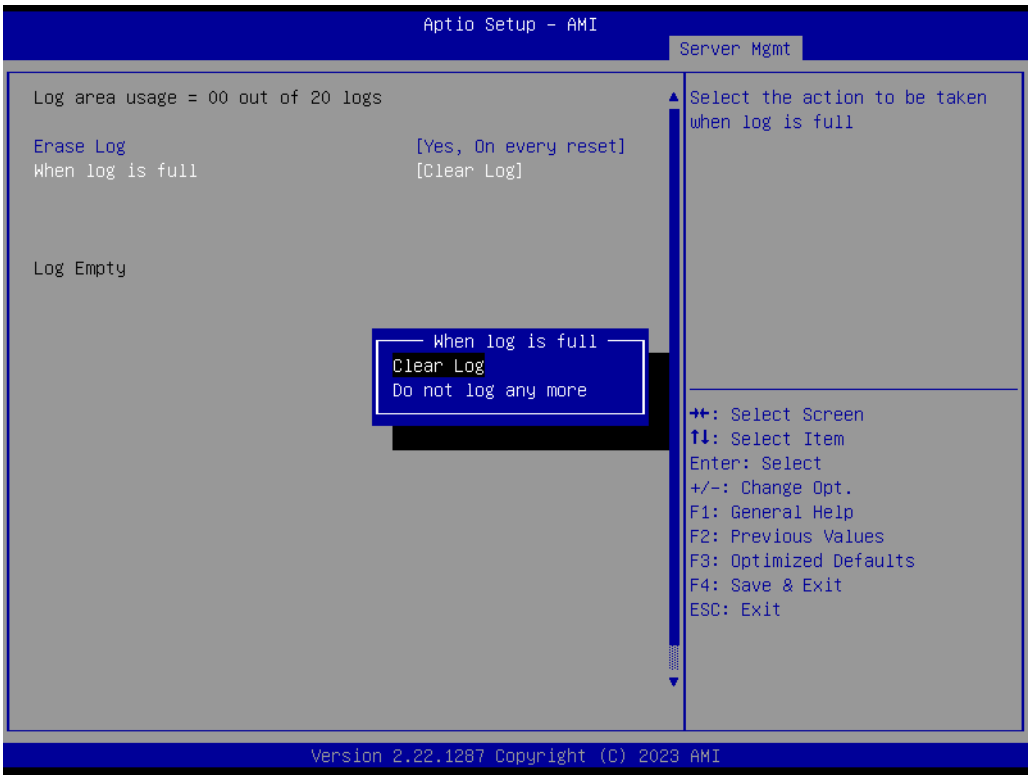
3.2.5.1 System Event Log



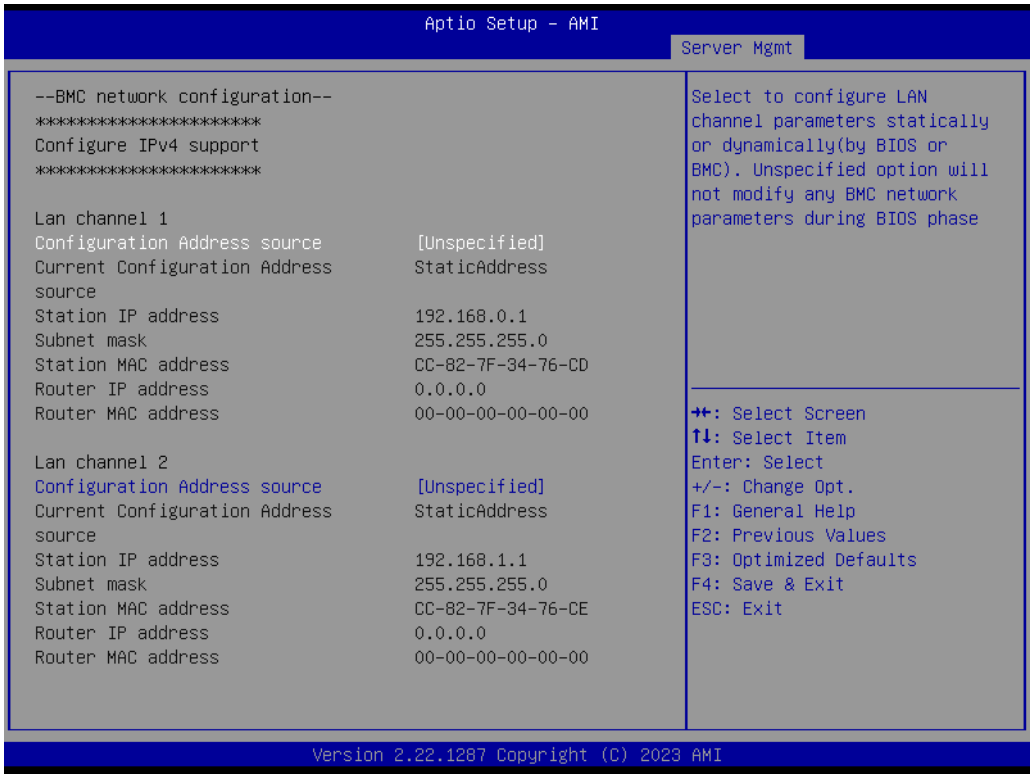
- **SEL Components [Enable]**
  Enable or disable all features of system event logging during boot.
- **Erase SEL [No]**
  Choose options for erasing SEL. "No" is the default setting.
- **Log EFI Status Codes [error code]**
  Disable the logging of EFI status codes or log only error code or only progress code or both.

3.2.5.2 BMC Self Test Log

- **Erase Log**
  Erase log options.
- **When Log is Full**
  Select the action to be taken when log is full.

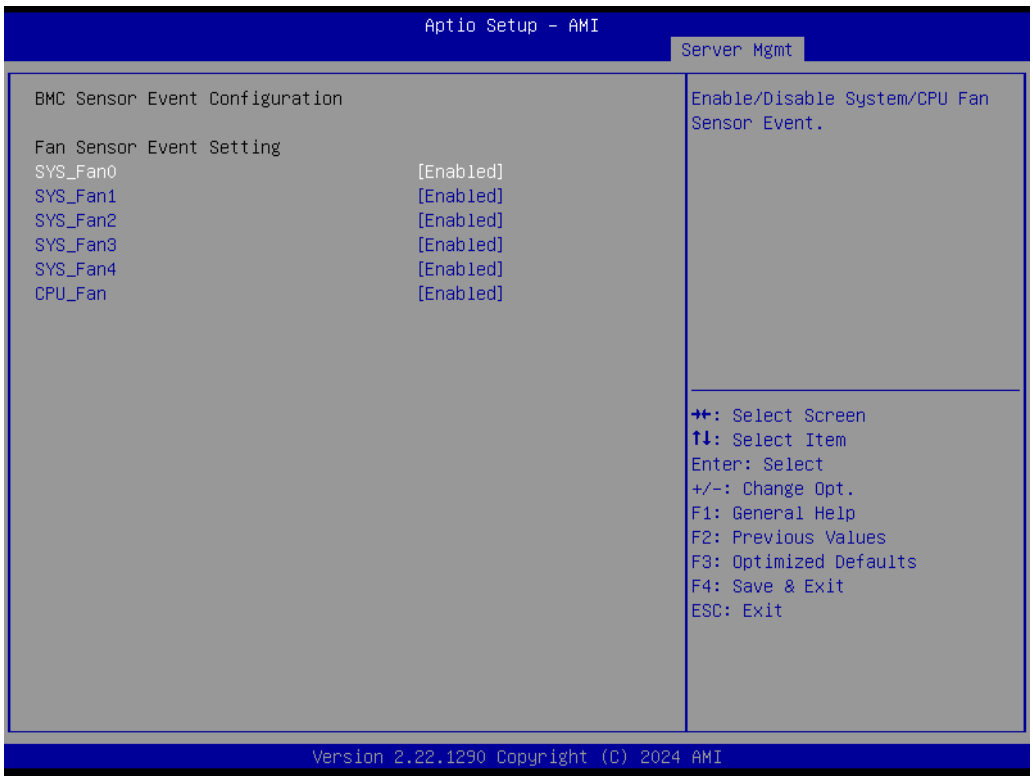


3.2.5.3 BMC Network Configuration

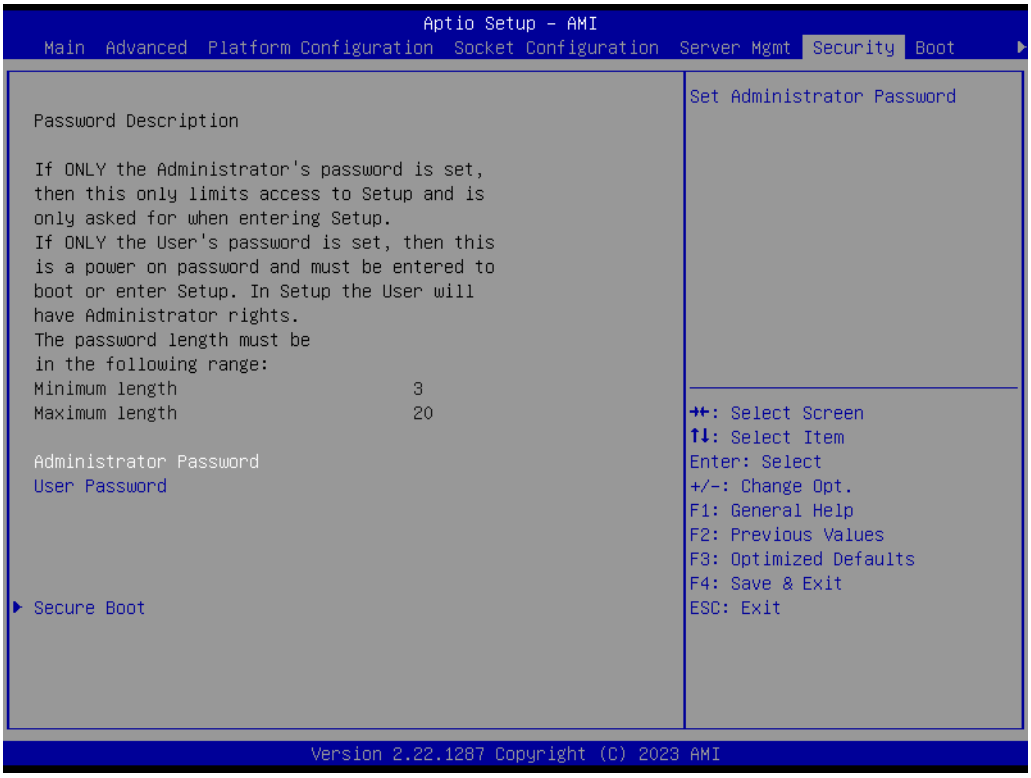


- **Configuration Address Source [Unspecified]**
  Select to configure LAN channel parameters statically or dynamically (by BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

3.2.5.4 BMC Sensor Event Configuration



3.2.6 Security



Note! ***With AC power & Battery. Short CMOS1 Jumper:***

Date/Time & Password: Keep

Setting: reset to default

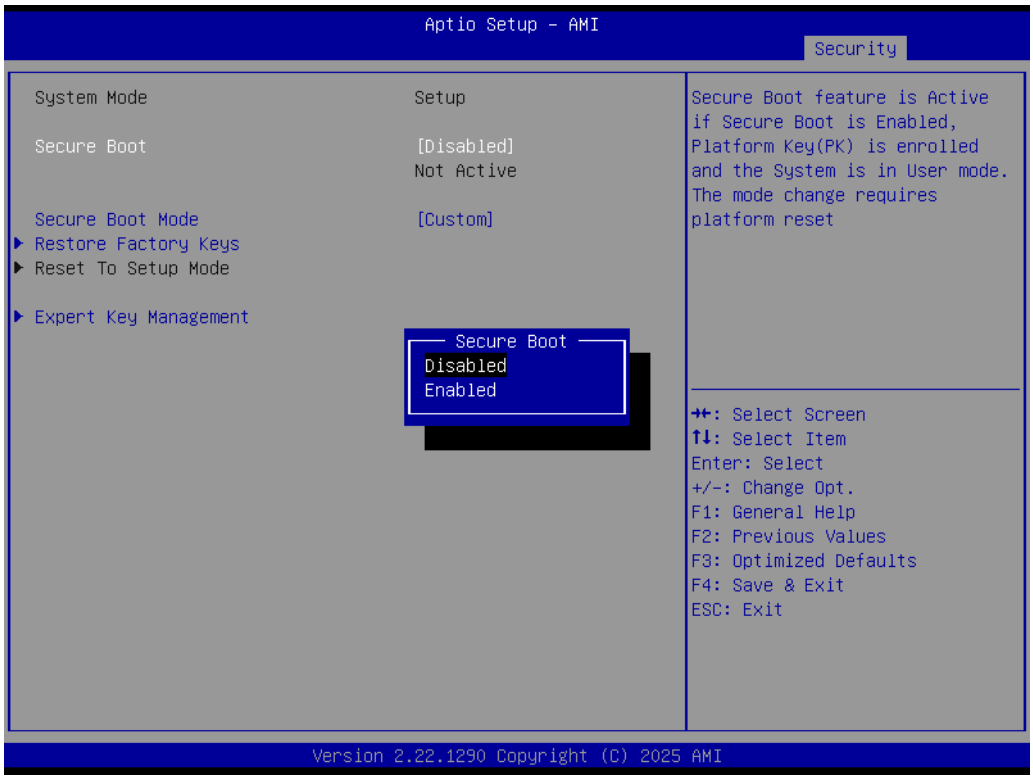
AC power and CMOS battery are removed. Short CMOS1 Jumper:

Date/Time: reset to default

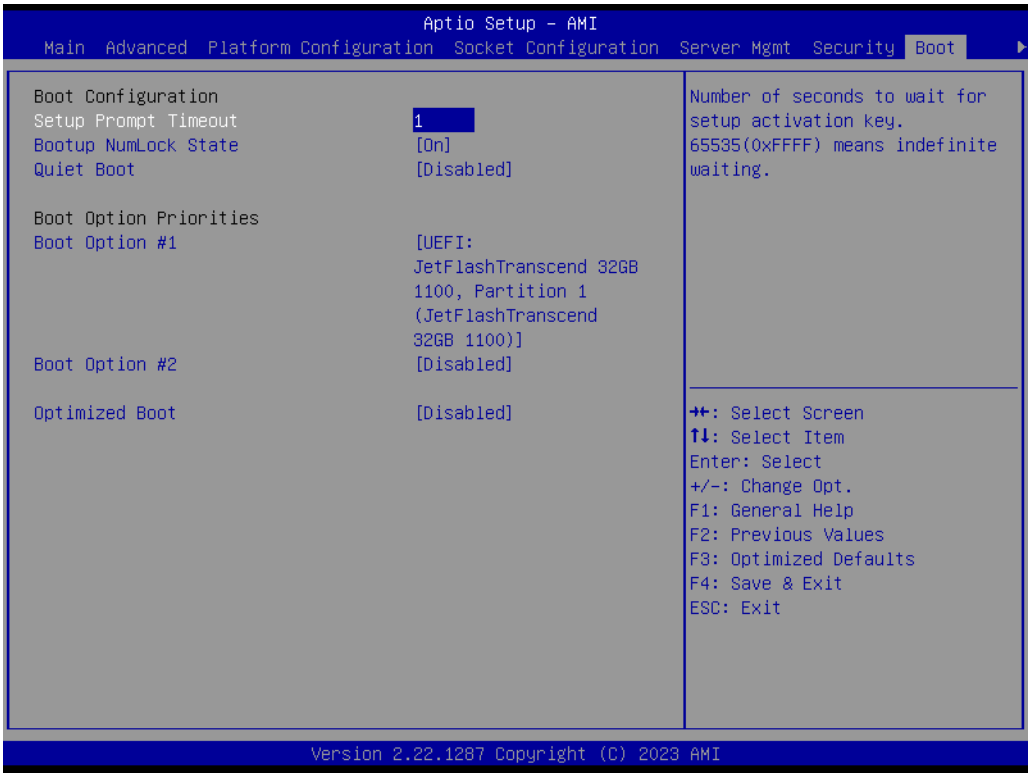
Password: Keep

Setting: reset to default

3.2.6.1 Secure Boot

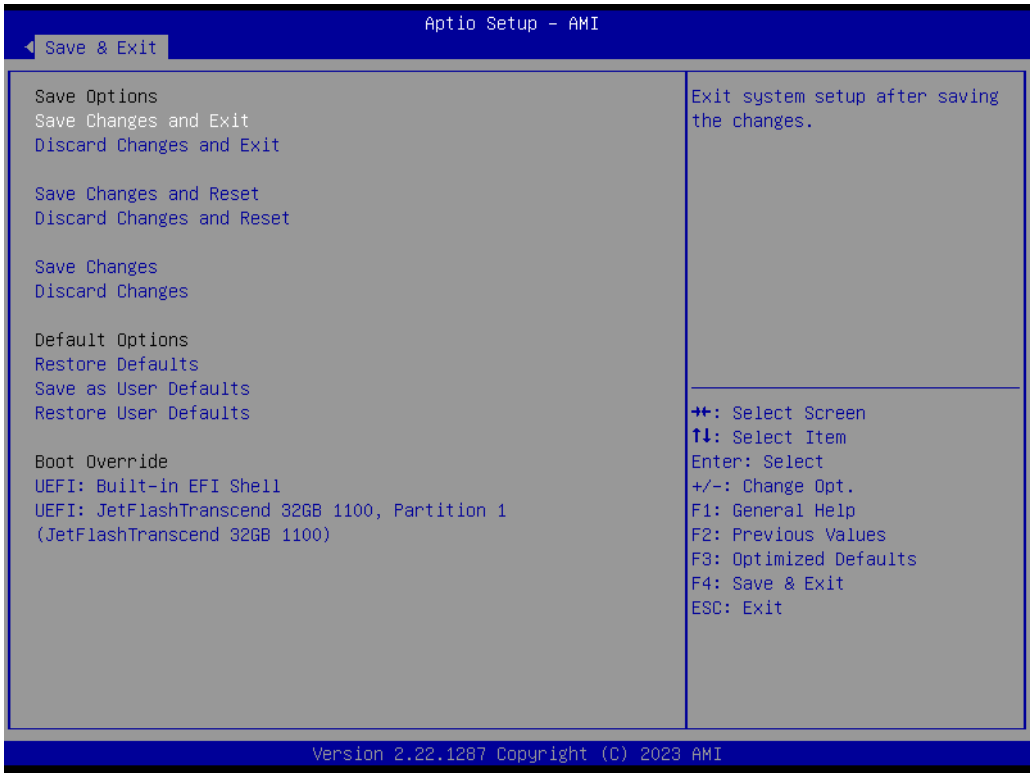


3.2.7 Boot



- **Setup Prompt Timeout [1]**
  Number of seconds to wait for setup activation key. "1" is the default setting.
- **Bootup NumLock State [On]**
  Select the keyboard NumLock state as "On" or "Off".
- **Quiet Boot [Disable]**
  Enable or disable quiet boot option.
- **Boot Option Priorities**
  Sets the system boot priorities.

3.2.8 Save & Exit



- **Save Changes and Exit**
  Exit system setup after saving the changes.
- **Discard Changes and Exit**
  Exit system setup without saving any changes.
- **Save Changes and Reset**
  Reset the system after saving changes.
- **Discard Changes and Reset**
  Reset system setup without saving any changes.
- **Save Changes**
  Save changes done so far to any of the setup options.
- **Discard Changes**
  Discard changes done so far to any of the setup options.
- **Restore Defaults**
  Restore/Load default values for all the setup options.
- **Save as User Defaults**
  Save the changes done so far as user defaults.
- **Restore User Defaults**
  Restore the user defaults to all the setup options.

Chapter 4

Chipset Software Installation Utility

4.1 Before Beginning

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the ASMB-817 are available online for download from the Advantech support website.

Before beginning, it is important to note that most display drivers need to have the relevant software application already installed on the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.

4.2 Introduction

The Intel Chipset Software Installation (CSI) utility installs the Windows INF files that outline the operating system on how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- USB 1.1/2.0/3.2 gen1 support
- Identification of Intel chipset components in the Device Manager

Note! *The chipset driver is used for the following versions of Windows, and it has to be installed before installing all the other drivers:*

Windows Server 2022 DataCenter x64	*x64*
Windows Server 2019 DataCenter x64	*x64*
Windows 11 Enterprise x64	*x64*
Windows 10 Enterprise x64	*x64*

Note! *It is necessary to update all the latest Microsoft hot fix files when using this OS.*

Chapter 5

Graphic Setup

5.1 Introduction

Install the ASPEED VGA driver to enable this function, which includes the following features:

- 32-bit 2D graphics engine on board for normal use.
- 64 MB RAM for this chip, the highest resolution is 1920x1200.

5.2 Windows Series Driver Setup

When the folder is displayed, navigate to the “Graphic” folder and click the executable file to complete the installation of the drivers for the OS that you need.

Note!

1. *If ASMB-817 carries an additional graphics card for VGA output, please set this additional graphic card as "major output" under the “Display properties” of OS.*
2. *The WDDM driver can support for the following OS versions:*
   - *Windows 10 x64 version*
   - *Windows 11 x64 version*
   - *Windows Server 2012R2 version (WHQL*
   - *Windows Server 2016 version (WHQL)*
   - *Windows Server 2019 version (WHQL)*
   - *Windows Server 2022 version (WHQL)*
3. *ASPEED Graphics WDDM Driver Limitation on Microsoft Windows OS.*
   - *It is a non-WHQL certified driver because ASPEED VGA is a 2D VGA, and it cannot meet the WHQL requirement of WDDM drivers which require 3D VGA functions.*
   - *Because it is a non-WHQL certified driver, it may have some compatibility issues with some specific applications*

Chapter 6

LAN Configuration

6.1 LAN Configuration

6.1.1 Introduction

The ASMB-817 has two ten Gigabit Ethernet LAN connections, LAN1 and LAN2 - Intel® X710-AT2. They eliminate bottlenecks of network data flow and incorporate Gigabit Ethernet at 10 Gbps.

6.1.2 Features

- 100/1000 & 10G Base-T Ethernet controller
- 100/1000 & 10G Base-T triple-speed MAC
- Full duplex at 100/1000 Mbps or 10 Gbps and half duplex at 10/100/1000 Mbps
- Wake-on-LAN (WOL) support

6.1.3 Installation

The integrated Intel gigabit Ethernet controller supports all major network operating systems. However, the installation procedure varies with different operating systems. In the following sections, refer to the one that provides the driver setup procedure for the operating system you are using.

6.1.4 Windows Series Driver Setup

Select folder “LAN” then click on the proper LAN driver for the OS.

\

Appendix A

Programming the Watchdog Timer

The ASMB-817's watchdog timer can be used to monitor system software operation and take corrective action if the software fails to function within the programmed period. This section describes the operation of the watchdog timer and how to program it.

A.1 Watchdog Timer Overview

The watchdog timer is built in to the EC controller IT5121E. It provides the following functions for user programming:

- Can be enabled and disabled by the user's program
- Timer can be set from 1 to 255 seconds
- Generates an interrupt or reset signal if the software fails to reset the timer before time-out

A.2 Programming the Watchdog Timer

The I/O port address of the watchdog timer is as below:

Table A.1: The I/O Port Address of the Watchdog Timer

Address	Description	
0x57	Event - Warm Reset: 0x04	
0x5E	Warm Reset Timer (High BYTE)	Based 100ms
0x5F	Warm Reset Timer (Low BYTE)	

Here is an example to step by step program the Watchdog Timer.

Table A.2: Programming the Watchdog Timer

Step	Action	Description
00	Read 0x299 port	Clear I/O port
	Wait IBF clear	0x29A, BIT1, = 0
01	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0
02	Write 0x5E to 0x299 port	
	Wait IBF clear	0x29A, BIT1, = 0
03	Write 0x00 to 0x299 port	Set 10 sec (high byte)
	Wait IBF clear	0x29A, BIT1, = 0
04	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0
05	Write 0x5F to 0x299 port	
	Wait IBF clear	0x29A, BIT1, = 0
06	Write 0x64 to 0x299 port	Set 10 sec (low byte)
	Wait IBF clear	0x29A, BIT1, = 0
07	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0
08	Write 0x57 to 0x299 port	Watchdog Event
	Wait IBF clear	0x29A, BIT1, = 0
09	Write 0x04 to 0x299 port	(Warm) Reset event
	Wait IBF clear	0x29A, BIT1, = 0
10	Write 0x28 to 0x29A	Start watchdog
	Wait 1 ~ 9 sec	
	Wait IBF clear	0x29A, BIT1, = 0
11	Write 0x29 to 0x29A	Stop watchdog
	Wait IBF clear	0x29A, BIT1, = 0
12	Go to Step 07	

Appendix B

I/O Pin Assignments

B.1 USB 3.2 Gen1 Header (USB3H1)

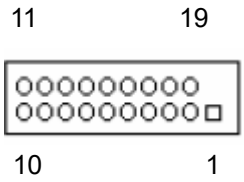


Table B.1: USB Header (USB3H1)

Pin	Signal	Pin	Signal
1	+5 V	2	STDA_SSRX-
3	STDA_SSRX+	4	GND
5	STDA_SSRX-TX-	6	STDA_SSRX+TX+
7	GND	8	D-
9	D+	10	NC (reserved for OC pin)
11	D+	12	D-
13	GND	14	STDA_SSRX+TX+
15	STDA_SSRX-TX-	16	GND
17	STDA_SSRX+	18	STDA_SSRX-
19	+5 V	20	

B.2 LAN Ports (LAN1, LAN2)

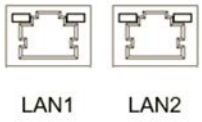


Table B.2: LAN RJ-45 Port (LAN1, LAN2)

Pin	Signal	Pin	Signal
1	MID0+	4	MID2+
2	MID0-	5	MID2-
3	MID1+	7	MID3+
6	MID1-	8	MID3-

B.3 VGA Connector (VGA1)

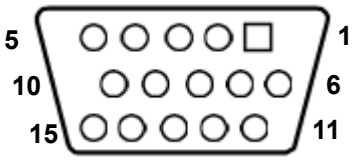


Table B.3: VGA Connector (VGA1)			
Pin	**Signal**	**Pin**	**Signal**
1	RED	9	VCC
2	GREEN	10	GND
3	BLUE	11	N/C
4	N/C	12	SDT
5	GND	13	H-SYNC
6	GND	14	V-SYNC
7	GND	15	SCK
8	GND		

B.4 RS-232 Interface (COM1)

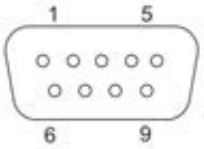


Table B.4: RS-232 Connector (COM1)	
Pin	**Signal**
1	DCD
2	DSR
3	RXD
4	RTS
5	TXD
6	CTS
7	DTR
8	RI
9	GND

B.5 System & CPU Fan Power Connector (SYSFAN0~4, CPUFAN0)

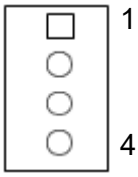


Table B.5: CPU FAN Connector (CPUFAN0)

	CPUFAN0
1	GND
2	+12V
3	CPU_FAN0_TACH
4	CPU0_PWM

Table B.6: SYS FAN Connector (SYSFAN0~4)

	SYSFAN0	SYSFAN1	SYSFAN2	SYSFAN3	SYSFAN4
1	GND	GND	GND	GND	GND
2	+12V	+12V	+12V	+12V	+12V
3	FAN0_TACH	FAN1_TACH	FAN2_TACH	FAN3_TACH	FAN4_TACH
4	FAN0_PWM	FAN1_PWM	FAN2_PWM	FAN3_PWM	FAN4_PWM

B.6 Power LED (JFP3)

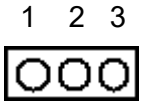


Table B.7: Power LED Connector (JFP3)

Pin	Function
1	LED power (3.3 V)
2	NC
3	Ground

B.7 External Speaker Connector (JFP1+JFP2)

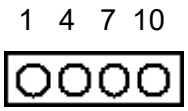


Table B.8: External Speaker Connector (JFP1+JFP2)	
Pin	**Function**
1	SPK+
4	NC
7	BZ-
10	SPK-

B.8 Reset Connector (JFP1+JFP2)

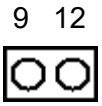


Table B.9: Reset Connector (JFP1+JFP2)	
Pin	**Signal**
9	RESET
12	GND

B.9 HDD LED Connector (JFP1+JFP2)

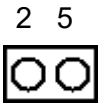


Table B.10: HDD LED Connector (JFP1+JFP2)	
Pin	**Signal**
2	HDD_LED+
5	HDD_LED-

B.10 ATX Soft Power Switch (JFP1+JFP2)

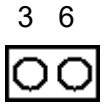


Table B.11: ATX Soft Power Switch (JFP1+JFP2)	
Pin	**Signal**
3	PWR-BTN
6	GND

B.11 ATX Soft Power Switch (JFP1)

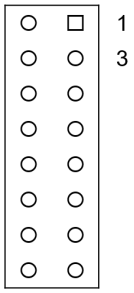


Table B.12: ATX Soft Power Switch (JFP1)	
Pin	**Signal**
1	PWR BTN
3	PWR GND

B.12 Reset Connector (JFP1)

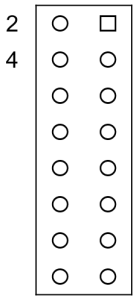


Table B.13: Reset Connector (JFP1)	
Pin	**Signal**
2	RST BTN
4	RST GND

B.13 Front Panel LAN LED Connector (JFP1)

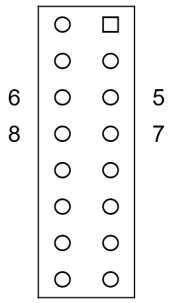


Table B.14: Front Panel LAN LED Connector (JFP1)

Pin	Signal
5	LAN2_LED+
6	LAN1_LED+
7	LAN2_LED-
8	LAN1_LED-

B.14 HDD LED Connector (JFP1)

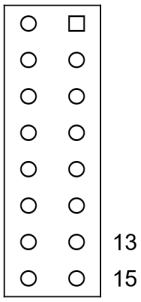


Table B.15: SNMP SMBus Connector (JFP2)

Pin	Signal
13	HDD_LED+
15	HDD_LED-

B.15 Power LED (JFP1)

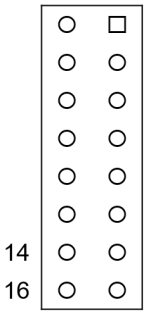


Table B.16: Power LED (JFP1)	
Pin	**Signal**
14	PWR LED+
16	PWR LED-

B.16 SMBus Connector (SMBUS1)

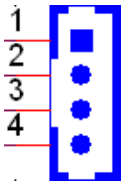


Table B.17: Front Panel SMBus Connector (SMBUS1)	
Pin	**Signal**
1	+5V
2	RESUME_SMB_CLK
3	RESUME_SMB_DATA
4	GND

B.17 USB & LAN Ports (LAN1_USB2C1 / LAN2_USB2C2 / BMCLAN_USB3C1)

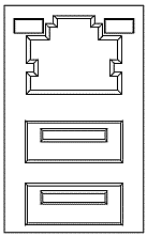


Table B.18: USB Port (BMC_LAN_USB3C1)

Pin	Signal
1	VBUS
2	D-
3	D+
4	GND
5	StdA_SSRX-
6	StdA_SSRX+
7	GND_DRAIN
8	StdA_SSTX-
9	StdA_SSTX+

Table B.19: USB Port (LAN1_USB2C1 / LAN2_USB2C2)

Pin	Signal
1	VCC
2	DT1-
3	DR1+
4	GND

Table B.20: LAN RJ-45 Port (LAN1_USB2C1 / LAN2_USB2C2 / BMCLAN_USB3C1)

Pin	Signal	Pin	Signal
1	MID0+	4	MID2+
2	MID0-	5	MID2-
3	MID1+	7	MID3+
6	MID1-	8	MID3-

B.18 Case Open Connector (JCASE1)

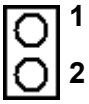


Table B.21: Case Open Connector (JCASE1)

Pin	Signal
1	CASEOP
2	GND

B.19 Front Panel LAN LED Connector (LANLED1)

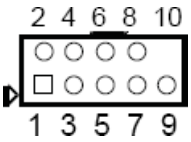


Table B.22: LAN LED Connector (LANLED1)

Pin	Signal	Pin	Signal
1	LAN1_LED1_ACT#	2	LAN2_LED1_ACT#
3	+V3.3_AUX	4	+V3.3_AUX
5	LAN3_ACT_LVC3#	6	LAN4_ACT_LVC3#
7	+V3.3_AUX	8	+V3.3_AUX
9	NC	10	NC

B.20 Clear CMOS Connector (JCMOS1)

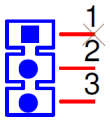


Table B.23: Clear CMOS Connector (JCMOS1)

Pin	Signal
	JCMOS1
1	NC
2	RTC_RST_PCH
3	GND

B.21 PMBUS Connector (PMBUS1)

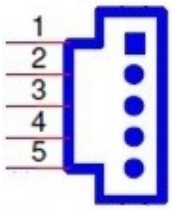


Table B.24: PMBUS Connector (PMBUS1)	
Pin	**Signal**
1	PMBUS_SMB_CLK
2	PMBUS_SMB_DATA
3	PMBUS_SW_ALERT#
4	GND
5	+V3.3_AUX

B.22 GPIO Connector (GPIO1)

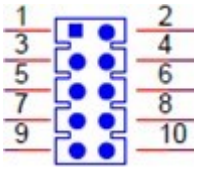


Table B.25: GPIO Connector (GPIO1)			
Pin	**Signal**	**Pin**	**Signal**
1	GPIO0	2	GPIO4
3	GPIO1	4	GPIO5
5	GPIO2	6	GPIO6
7	GPIO3	8	GPIO7
9	VCC_GPIO	10	GND

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All product specifications are subject to change without notice.