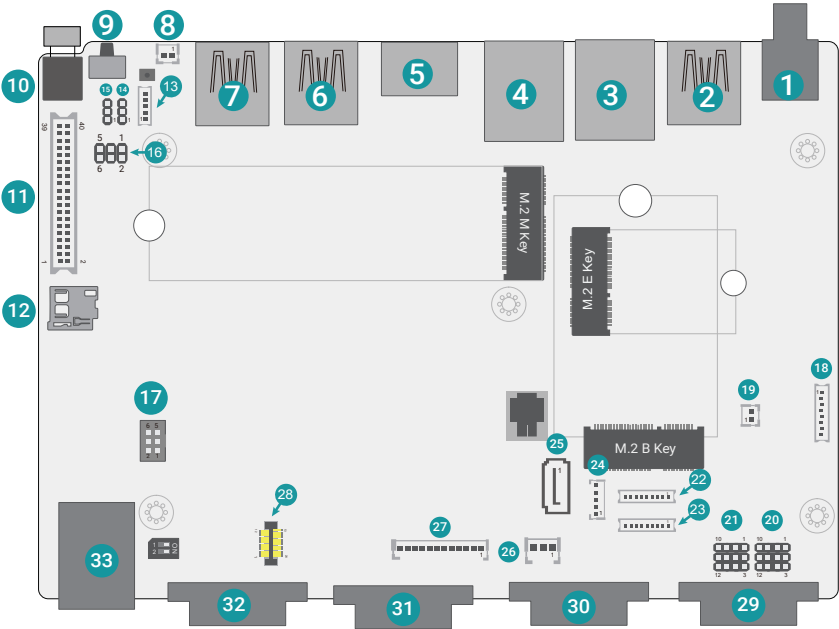
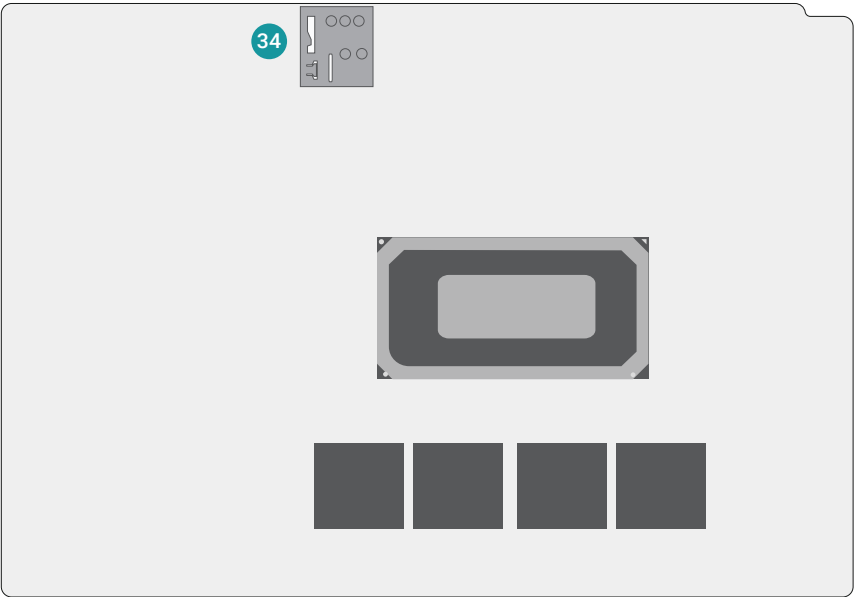


Board Layout

Top View



Bottom View



1	DC-IN	21	COM4 Port / DIO Selection
2	USB3.2	22	COM5
3	2.5G LAN	23	COM6
4	2.5G LAN	24	SATA Power
5	HDMI	25	SATA0
6	USB3.2	26	System Fan
7	▲USB2.0 ▼USB Type C	27	eSPI Header (Debug)
8	RTC Battery	28	eMMC
9	Reset Button	29	COM 4 (RS232/422/485) or 8-bit DIO
10	Power Button	30	COM 3 (RS232)
11	LVDS	31	COM 2 (RS232)
12	uSD Slot	32	COM 1 (RS232/422/485)
13	I2C	33	LAN
14	Inverter Panel Power Selection	34	Nano SIM Slot
15	Panel Backlight ON Level Selection		
16	VCC Panel Power Selection		
17	Front Panel		
18	USB2.0		
19	Case Open		
20	COM4 Port / DIO Selection		

20 COM4 Port / DIO Selection	TSJP1
COM4 (Default)	1-2 short/4-5 short 7-8 short/10-11 short
DIO	2-3 short/5-6 short 8-9 short/11-12 short

21 COM4 Port / DIO Selection	TSJP2
COM4 (Default)	1-2 short/4-5 short 7-8 short/10-11 short
DIO	2-3 short/5-6 short 8-9 short/11-12 short

15 Panel Backlight ON Level Selection	DPJP1
3V3 (Default)	1-2 Short
5V	2-3 Short

14 Inverter Panel Power Selection	DPJP2
12V (Default)	1-2 Short
5V	2-3 Short

16 VCC Panel Power Selection	DPJP3
12V	1-2 Short
5V	3-4 Short
3V3 (Default)	5-6 Short

PIN Assignment

8 ► RTC Battery (CN7)

Pin	Assignment
1	V_BAT
2	GND

26 ► System Fan (CN12)

Pin	Assignment
1	TACT
2	+12V
3	GND

13 ► I2C (CN6)

Pin	Assignment
1	3V3SB
2	GND
3	I2C1_SCL_SOC
4	I2C1_SDA_SOC
5	I2C1_INT#

17 ► Front Panel (J2)

Pin	Assignment	Pin	Assignment
1	PWR_BTN0	2	3.3V
3	GND	4	SUS_LED#
5	SYS_RST0	6	HD_LED#

19 ► Case Open (CN11)

Pin	Assignment
1	CASEOPEN-
2	GND

24 ► SATA Power (CN1)

Pin	Assignment
1	5V
2	5V
3	NC
4	GND
5	GND

25 ► SATA0 (J1)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

22 ► COM5 (TSCN5)

Pin	Assignment
1	MDCD-
2	MSIN
3	MSOUT
4	MDTR-
5	GND
6	MDSR-
7	MRTS-
8	MCTS-
9	MRI-

23 ► COM6 (TSCN6)

Pin	Assignment
1	MDCD-
2	MSIN
3	MSOUT
4	MDTR-
5	GND
6	MDSR-
7	MRTS-
8	MCTS-
9	MRI-

18 ► USB2.0 (UBCN4)

Pin	Assignment
1	5V
2	USB2_7_C_N
3	USB2_7_C_P
4	GND
5	5V
6	USB2_8_C_N (optional)
7	USB2_8_C_P (optional)
8	GND

27 ► eSPI (CN8)

Pin	Assignment
1	3V3SB
2	ESPI_RESET#
3	ESPI_ALERT#
4	ESPI_IO_0
5	ESPI_IO_1
6	ESPI_IO_2
7	ESPI_IO_3
8	ESPI_CLK
9	ESPI_CS0#
10	DEBUG_UART_RX
11	DEBUG_UART_TX
12	GND

11 ► LVDS (DPCN1)

Pin	Assignment	Pin	Assignment
1	LVDSA_LANE3_P	2	LVDSB_LANE3_P
3	LVDSA_LANE3_N	4	LVDSB_LANE3_N
5	GND	6	GND
7	LVDSA_LANE2_P	8	LVDSB_LANE2_P
9	LVDSA_LANE2_N	10	LVDSB_LANE2_N
11	GND	12	GND
13	LVDSA_LANE1_P	14	LVDSB_LANE1_P
15	LVDSA_LANE1_N	16	LVDSB_LANE1_N
17	GND	18	GND
19	LVDSA_LANE0_P	20	LVDSB_LANE0_P
21	LVDSA_LANE0_N	22	LVDSB_LANE0_N
23	GND	24	GND
25	LVDSA_CLK_P	26	LVDSB_CLK_P
27	LVDSA_CLK_N	28	LVDSB_CLK_N
29	GND	30	GND
31	LVDS_DDC_CLK	32	eDP_HPD_CON
33	LVDS_DDC_DATA	34	BLONOFF
35	GND	36	LVDS_3V3
37	INV_PWR	38	DIMMING
39	INV_PWR	40	VCC_PANEL_PWR

11 ► eDP SKU (DPCN1) -Optional

Pin	Assignment	Pin	Assignment
1	NC	2	NC
3	NC	4	NC
5	GND	6	GND
7	NC	8	NC
9	NC	10	NC
11	GND	12	GND
13	NC	14	eDP_LANE1_P
15	NC	16	eDP_LANE1_N
17	GND	18	GND
19	NC	20	eDP_LANE0_P
21	NC	22	eDP_LANE0_N
23	GND	24	GND
25	NC	26	eDP_AUX_C_P
27	NC	28	eDP_AUX_C_N
29	GND	30	GND
31	NC	32	eDP_HPD_CON
33	NC	34	BLONOFF
35	GND	36	LVDS_3V3
37	INV_PWR	38	DIMMING
39	INV_PWR	40	VCC_PANEL_PWR



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

