

AMI241

Fanless and Ventless System

User's Manual

Version 1.0
(September 2025)



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Compliance

CE

This product has passed CE tests for environmental specifications and limits. This product is in accordance with the directives of the European (EU). If users modify and/or install other devices in this equipment, the CE declaration of conformity may no longer apply.

FCC

This product has been tested and found to comply with the limits for a Class B device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation when properly installed and used. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with manufacturer's instructions, may cause harmful interference to radio communications.

WEEE



This product must not be disposed of as normal household waste, in accordance with the EU directive for waste electrical and electronic equipment (WEEE-2012/19/EU). Instead, it should be disposed of by returning it to a municipal recycling collection point. For proper disposal, please follow local regulations regarding electronic waste.

Green IBASE



This product complies with RoHS 2 restrictions, which prohibit the use of certain hazardous substances in electrical and electronic equipment. The following substances must not exceed the specified concentrations:

- Hexavalent chromium: 1,000 ppm
- Poly-brominated biphenyls (PBBs): 1,000 ppm
- Poly-brominated diphenyl ethers (PBDEs): 1,000 ppm
- Cadmium: 100 ppm
- Mercury: 1,000 ppm
- Lead: 1,000 ppm
- Bis(2-ethylhexyl) phthalate (DEHP): 1,000 ppm
- Butyl benzyl phthalate (BBP): 1,000 ppm
- Dibutyl phthalate (DBP): 1,000 ppm
- Diisobutyl phthalate (DIBP): 1,000 ppm

Important Safety Information

Carefully read the precautions before using the device.

Environmental conditions:

- Place the device horizontally on a stable and solid surface in case the device may fall, causing serious damage.
- Leave plenty of space around the device and do not block the openings for ventilation. NEVER DROP OR INSERT ANY OBJECTS OF ANY KIND INTO THE VENTILATION OPENINGS.

Care for Your IBASE products:

- Before cleaning the device, turn it off and unplug all cables such as power in case a small amount of electrical current may still flow.
- Use neutral cleaning agents or diluted alcohol to clean the device chassis with a cloth. Then wipe the chassis with a dry cloth.
- Vacuum the dust with a computer vacuum cleaner to prevent the air vent or slots from being clogged.



WARNING

Attention during use:

- Do not place heavy objects on the top of the device.
- Operate this device from the type of power indicated on the marking label. If you are not sure of the type of power available, consult your distributor or local power company.
- Do not walk on the power cord or allow anything to rest on it.
- If you use an extension cord, make sure that the total ampere rating of the product plugged into the extension cord does not exceed its limits.

Avoid Disassembly

Do not disassemble, repair or make any modification to the device. Disassembly, modification, or any attempt at repair could generate hazards and cause damage to the device, even bodily injury or property damage, and will void any warranty.



CAUTION

Danger of explosion if internal lithium-ion battery is replaced by an incorrect type. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Warranty Policy

- **IBASE standard products:**

IBASE offers a 24-month (2-year) warranty from the date of shipment. If the date of shipment cannot be ascertained, the product serial numbers can be used to determine the approximate shipping date.

- **3rd-party parts:**

12-month (1-year) warranty from delivery for the 3rd-party parts that are not manufactured by IBASE, such as CPU, memory, HDD, power adapter, panel and touchscreen.

* PRODUCTS, HOWEVER, THAT FAIL DUE TO MISUSE, ACCIDENT, IMPROPER INSTALLATION OR UNAUTHORIZED REPAIR SHALL BE TREATED AS OUT OF WARRANTY AND CUSTOMERS SHALL BE BILLED FOR REPAIR AND SHIPPING CHARGES.

Technical Support & Services

1. Visit the IBASE website at www.ibase.com.tw to find the latest product information.
2. If you need any further assistance from your distributor or sales representative, prepare the following information about your product and provide details about the problem.
 - Product model name
 - Product serial number
 - Detailed description of the problem
 - The error messages in text or in screenshots if there is any
 - The arrangement of the peripherals
 - Software in use (such as OS and application software, including the version numbers)
3. If repair service is required, log in to the RMA system of the website or and contact your distributor or sales representative for assistance.

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Chapter 1

General Information

The information provided in this chapter includes:

- Features
- Packing List
- Optional Accessories
- Specifications
- Product View
- Dimensions

1.1 Introduction

AMI241 is a fanless embedded system powered by the iBASE MBE240 board, supporting 14th/13th Gen Intel® Core™ i9/i7/i5/i3 desktop processors (35W TDP). It features DDR5-5600 memory up to 64GB, four LAN ports (2x 2.5GbE + 2x PoE+), three M.2 sockets, and a PCI-E x16 slot. Designed for industrial use, it includes dual SIM slots for WWAN redundancy, Intel® AMT 16.1, TPM 2.0, RAID 0/1 support, and full voltage protection.



AMI241

1.2 Features

- Fanless system with iBASE MBE240 proprietary board
- 14th/13th Intel® Core™ i9/i7/i5/i3 desktop processors (35W TDP)
- Supports DDR5-5600 memory, up to Max.64GB Non-ECC
- 2x Intel® 2.5G LAN, 2x Intel® GbE (Supports 802.3 at PoE+)
- 3x M.2 sockets (M-Key/B-Key/E-Key)
- Supports 1x PCI-E(16x) expansion slot
- Supports Over/Under/Reverse voltage protection
- Dual SIM slots support WWAN redundancy, iAMT (16.1), dTPM(2.0)
- 2x 2.5" Storage (Support RAID 0/1)

1.3 Packing List

Your product package should include the items listed below. If any of the items below is missing, contact the distributor or the dealer from whom you purchased the product.

- AMI241 x 1
- Terminal Block for DC-In Power Adaptor (5 pins) x 1
- Terminal Block for Remote Power Button (2 pins) x 1
- Wall Mount Kit x 1
- Flat Head Screw (for Wall Mount Kit) x 6



- Flat Head Screw for 2nd SSD (if not pre-installed) x 4



1.4 Optional Accessories

- 330W (24V@13.75A) DC-In Power Adaptor kit
- Antenna Kit
- M.2 Thermal Kit (Storage for M.2) (PN: SC2AMI241--0A1100R)
- Expansion Kit (with 2 cables for COM5 & COM6 ports): IP214, IP215
- 4G Module (PN: A024MDWIFI0040700P);
M.2 LTE/GPS (Global) [Sierra EM7565]
- Wireless; M.2-2230 A E Key B2 (PN: A008WIRELESS02200P);
802.11A/B/G/N/AC+BT [AW-CB260NF] RoHS2
- Sierra EM9 series 5G module [A024MD5GEM9 series010P]
- Power cord
- Riser card kits:
 - IP214 Riser Card Kit with 2x COM cables
 - IP215 Riser Card Kit with 2x COM cables

1.5 Specifications

Product Name	AMI241AFM (AMI) Aluminum Chassis with MBE240AF (Q670E PCH), Fanless Design, w/o CPU /memory/Storage, with riser card IP214. Supporting 6x COM, iAMT16.1, mounting brackets, (RoHS2)
System	
Motherboard	MBE240AF
CPU	14th/13th Gen Intel® Core™ i9/i7/i5/i3 Desktop Processors
Chipset	• Intel® Q670E
System Speed	Up to 5.0GHz
Memory	2x DDR5 5600 SO-DIMM, Max. 64GB <i>Non-ECC</i>
LAN	• Intel® I226LM 2.5GbE, Intel® I226V 2.5GbE • Intel® I210IT GbE, Intel® I210IT GbE
SATA	• 2x SATA III port for 2.5" SATA HDD or SSD
M.2	• M.2(E-Key @2230), for CNVi, w/ 1x PCIe(1x)+USB2.0 • M.2(M-Key @2280), for NVMe, w/ 1x PCIe(4x)(G4)+SATA • M.2(B-Key @3052), for 5G, w/ 1x PCIe(1x)+USB 3.2
Front Panel I/O	• 2x HDMI (2.0b) • 4x USB 3.2 ports • 2x Antenna holes • 2x RJ45 2.5GbE ports + Dual USB 3.2 stack ports • 1x Red LED for 2.5" storage device status • 1x Power button with Green LED indicator • 1x 2-pin terminal block for external power button • 1x SIM card cover for 2x SIM slots
Rear Panel I/O	• 2x RS232/422/485 ports for COM#1~COM#2 • 2x RS232 ports for COM#3~COM#4 • 2x RS232 ports for COM#5/#6 [Reserved via IP214] • 2x DisplayPort (1.2) (supports DP++) • 2x RJ45 Gigabit Ethernet ports (supports 802.3at,PoE+) • 1x 5-pin DC-in terminal block for 24V DC input • 2x Antenna holes

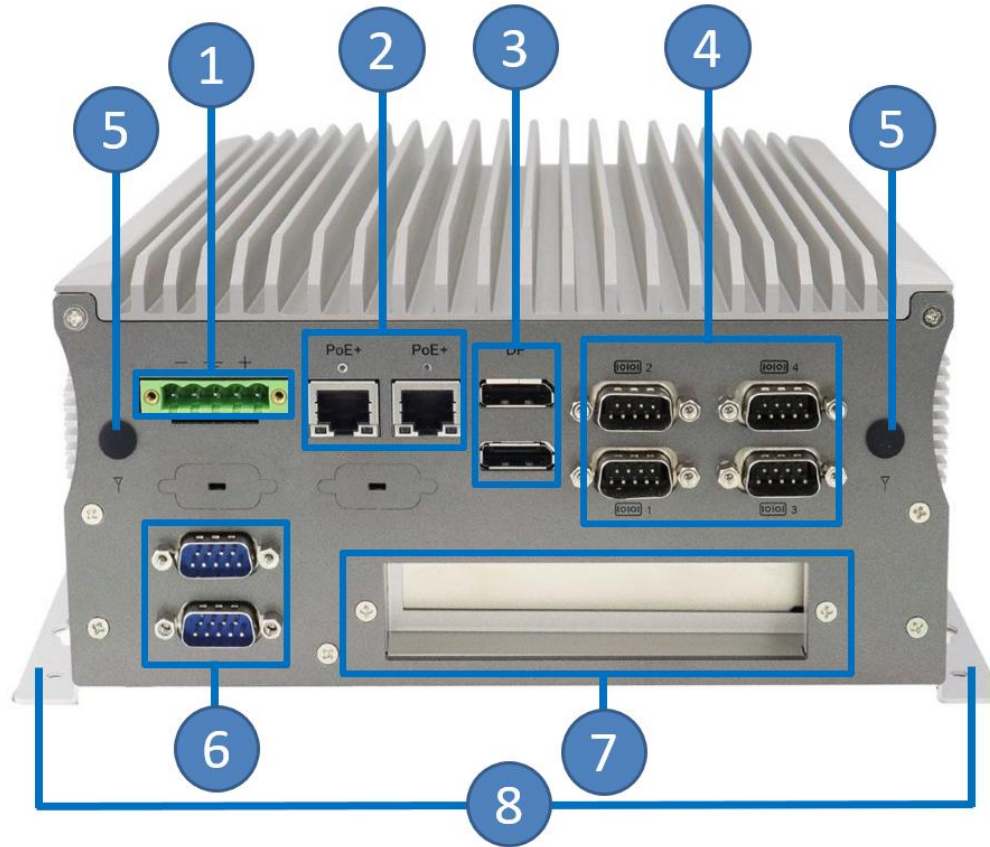
Expansion Slots	• 1x PCI-E (x16) expansion slot [Reserved via IP214 riser card]
M.2	• M.2-key@2230) x1, support CNVi , w/ 1x PCIe(1x) + USB2.0 • M.2(M-key@2280) x1, support NVMe, w/1x PCIe(4x)(G4) + SATA • M.2(B-key@3052) x1, support 5G/4G/LTE, w/1x PCIe(1x) + USB3.2

Mechanical and Environmental	
Dimensions	210mm(W) x 285mm(D) x 109mm(H)
Construction	Aluminum + Steel
Chassis color	Silver + Gray
Mounting type	Wall mount
Operating Temperature	-20°C to 70°C (-4°F~158°F) (for 35W CPU)
Storage Temperature	-20°C~80°C (-4°F~176°F)
Humidity	5%~90% @ 45°C (non-condensing)
Vibration	Operating: 3Grms / 5~500Hz
Shock	Operating: 20G / 11ms Non-operating: 40G / 11ms
Certification	CE *follow EN55032* FCC Class-A / LVD

All specifications are subject to change without prior notice.

1.6 Product View

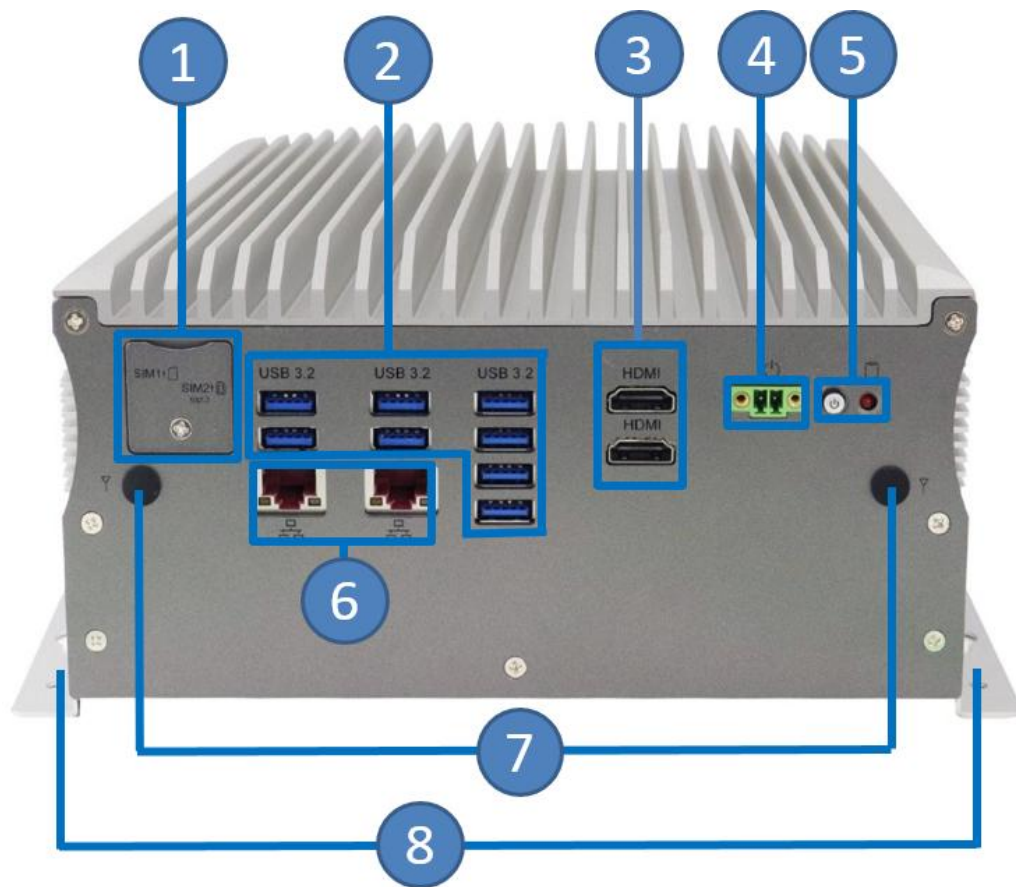
Front View



No.	Name	No.	Name
1	DC-In Power Connector for 24V input	5	Antenna holes
2	2x RJ45 port (supports 802.3at, PoE+)	6	COM5/COM6 (reserved)
3	DisplayPort	7	Expansion Slot (reserved)
4	4x COM Ports	8	Mounting Brackets



Rear View

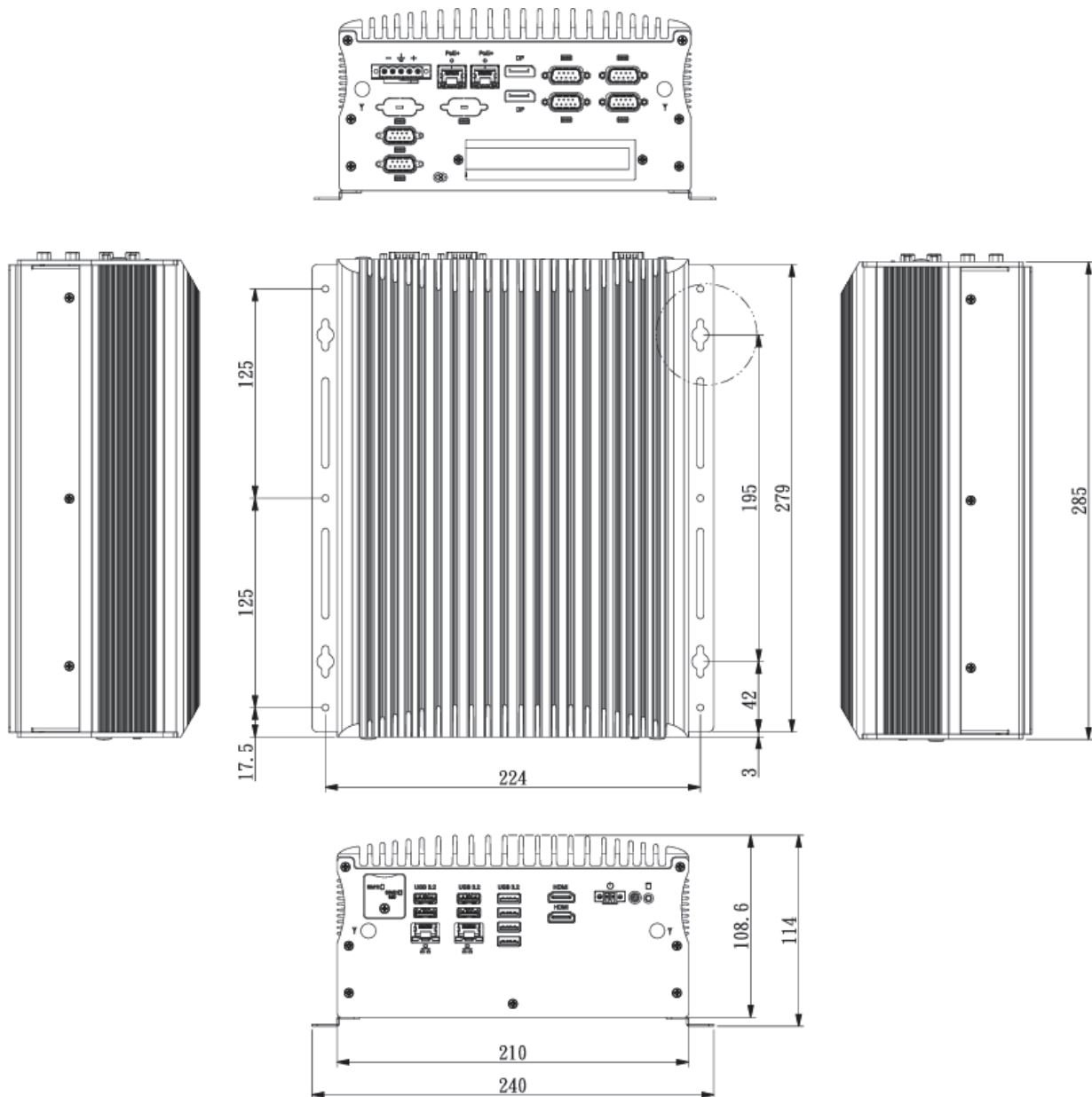


No.	Name	No.	Name
1	SIM Card Slots	5	Power Button & LED Indicator for HDD
2	USB Ports	6	RJ45
3	HDMI	7	Antenna holes
4	Terminal Block for external Power Button	8	Mounting Brackets



1.7 Dimensions

Unit: mm



Chapter 2

Hardware Configuration

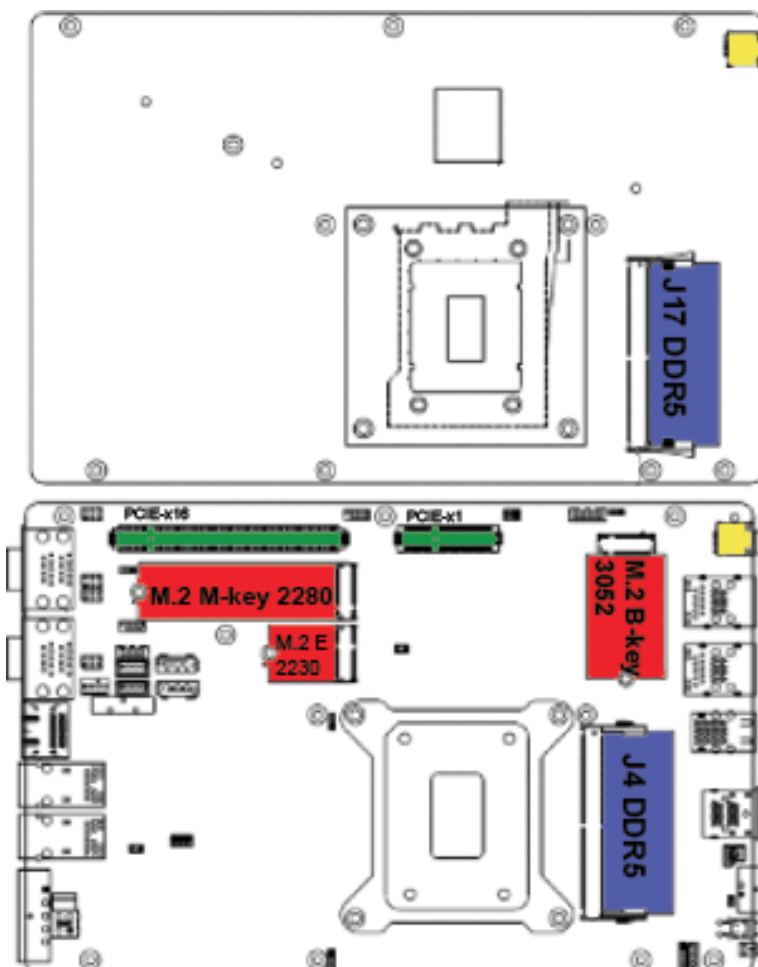
The information provided in this chapter includes:

- Essential installations
- Information and locations of connectors

2.1 Essential Installations

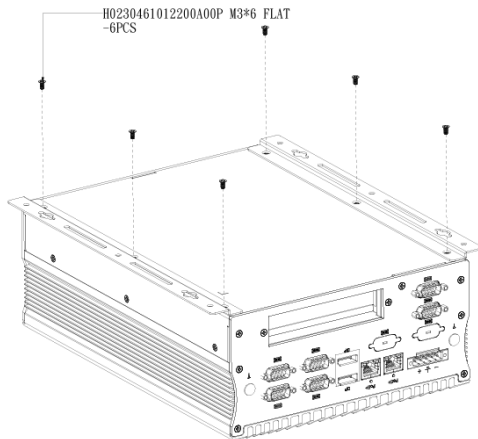
This section provides a step-by-step disassembly procedure for accessing specific parts, starting from the mounting brackets. Each step shows which screws need to be removed to expose the relevant components. Typically, the parts accessed on the main board include:

Function	Connector	Page
M.2 B-Key 3052 Slot	J3	27
M.2 M-Key M2280 Slot	J7	29
M.2 E-Key E2230 Slot	J9	30
DDR5 Memory Sockets	J4, J17	31
Nano SIM card 2, Nano SIM card 1 Slot	CN1, CN14	35
PCI-E x1, USB 2.0 Sys_Fan, SATA, COM TX/RX Signal	PCIE1	41
PCI-E x16 Connector	PCIE2	41



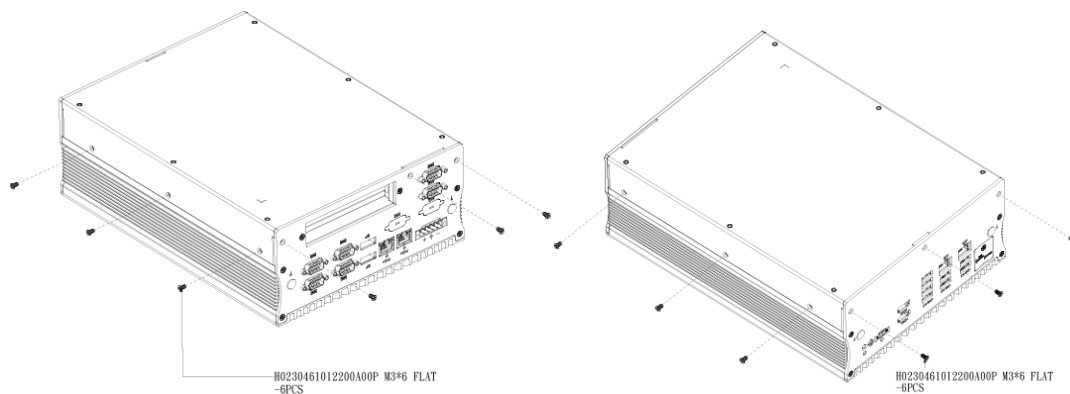
2.1.1 Mounting Brackets

To begin disassembly, remove the six screws securing the two mounting brackets to the system—three screws per bracket, as shown below. Lift the brackets away and set them aside.



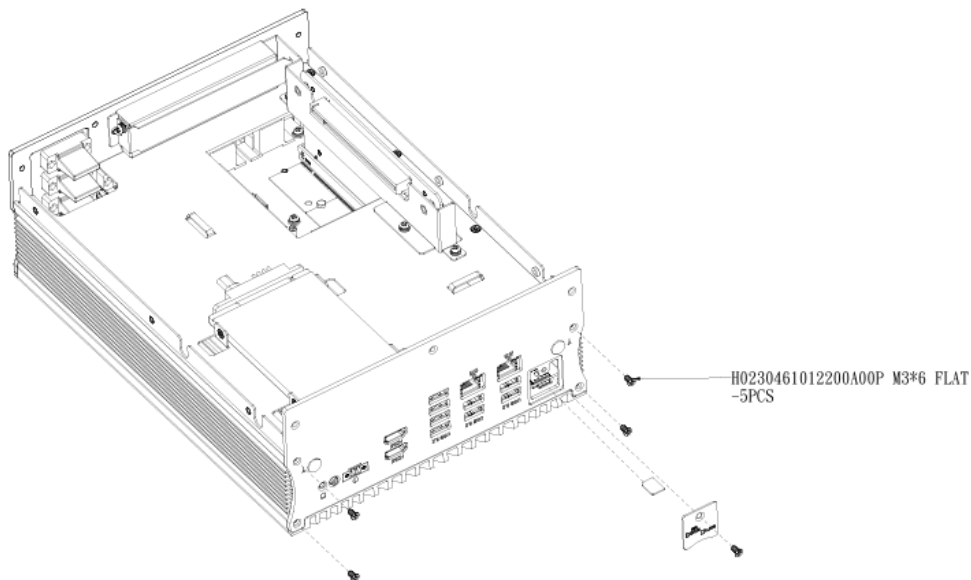
2.1.2 Removing the Cover

After removing the mounting brackets, take out the twelve screws securing the system cover—three screws on each side, as shown below. Lift off the cover and set it aside.

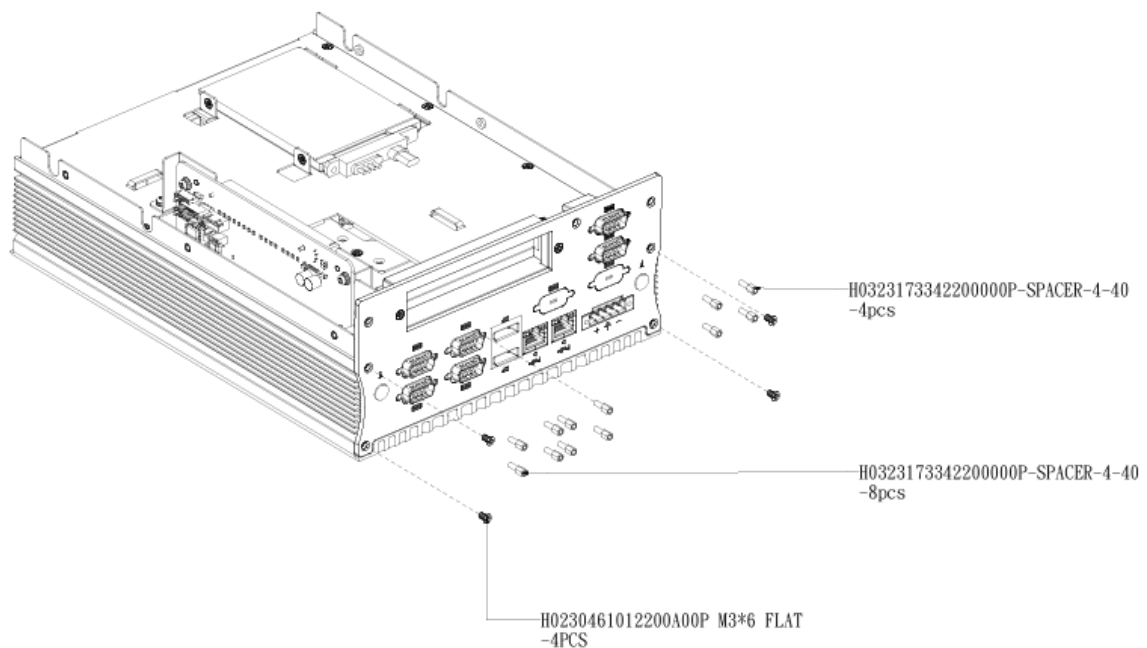


2.1.3 Removing the Front and Rear Plates

After removing the cover, take out the screws (including the SIM card cover screw) and spacers securing the front and rear plates, as shown below. Once all screws and spacers are removed, lift off the plates and set them aside.

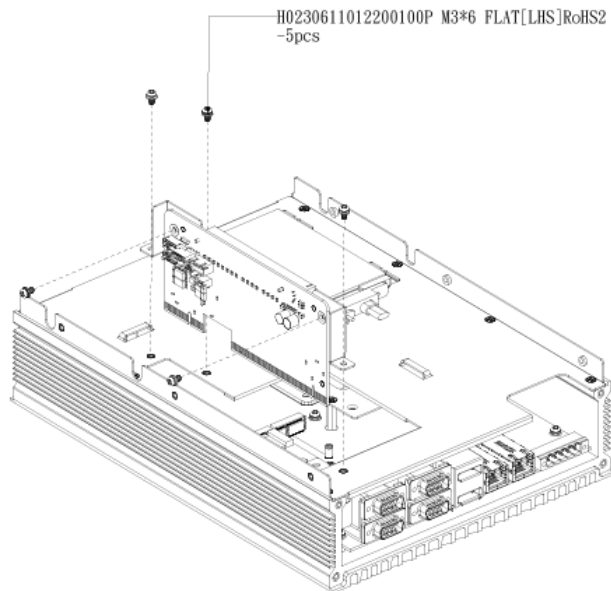


Remarks: Installation of the SIM card does not require removal of any screw other than the single screw for the SIM card slot door. Insert the SIM into the slot and press again to release it. Replace the screw to secure the SIM card slot door in place



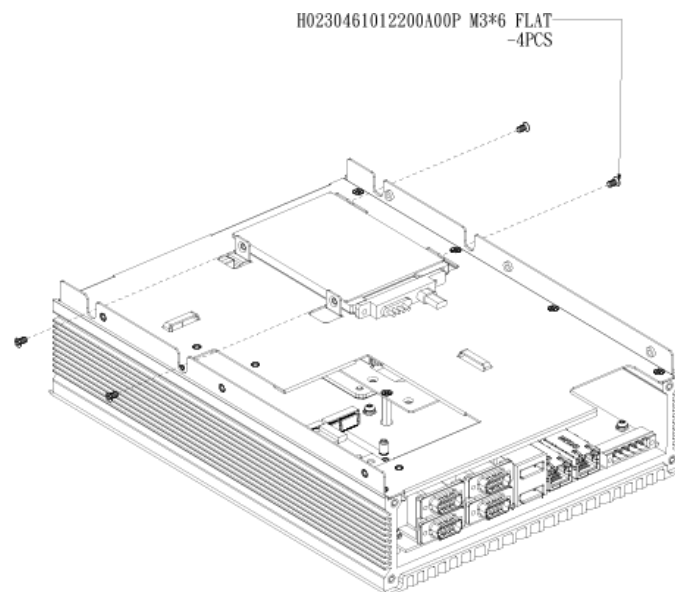
2.1.4 PCI-E Card

Remove the five screws securing the PCI-E card, as shown below. Carefully lift the card out of its slot and set it aside.



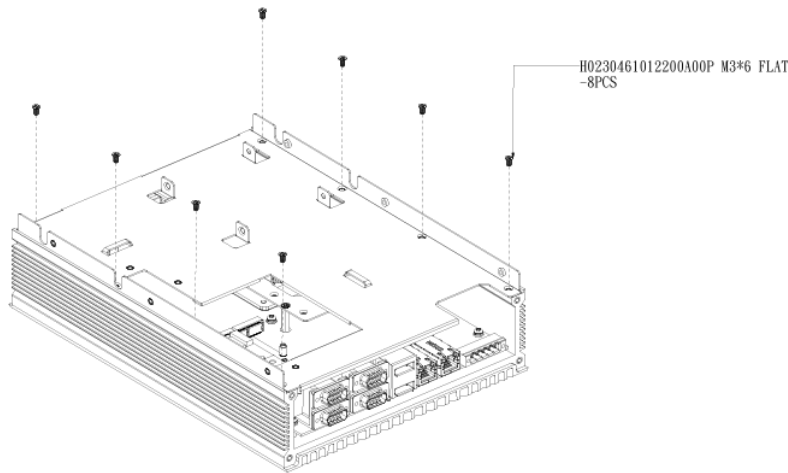
2.1.5 SSD1 Storage

Remove the four screws securing the SSD card, as shown below. Carefully lift the card out of its slot and set it aside.



2.1.6 Interlayer Plate Removal

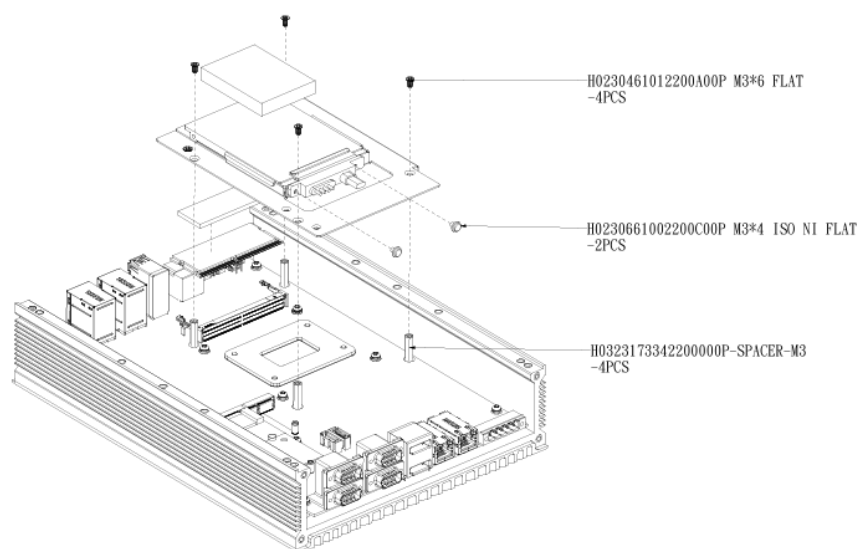
Remove the eight screws securing the interlayer plate covering the main board, as shown below. Carefully lift the plate out of its position and set it aside.



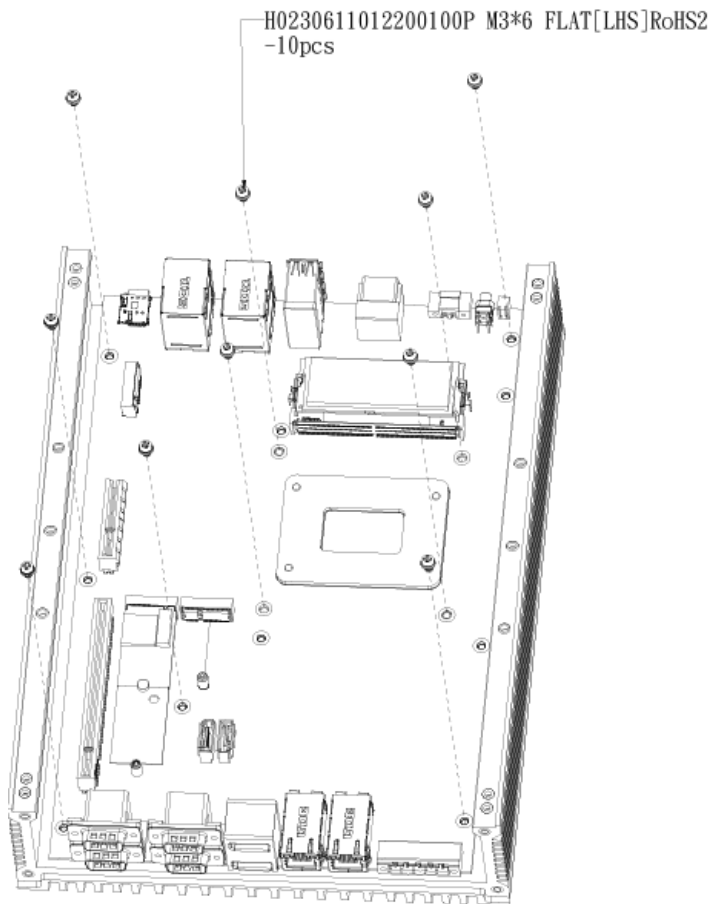
2.1.7 SSD2 Storage

A second SSD storage is available beneath the interlayer plate. Remove the screws securing the SSD card as necessary. At this stage, all three M.2 card slots and one DDR5 socket are accessible; the other DDR5 socket is on the reverse side of the board. For optimal cooling and heat transfer, use the required thermal pads for SSD, M.2 cards and memory modules.

Function	Connector	Page
M.2 B-Key 3052 Slot	J3	27
M.2 M-Key M2280 Slot	J7	29
M.2 E-Key E2230 Slot	J9	30
DDR5 Memory Sockets	J4, J17	31

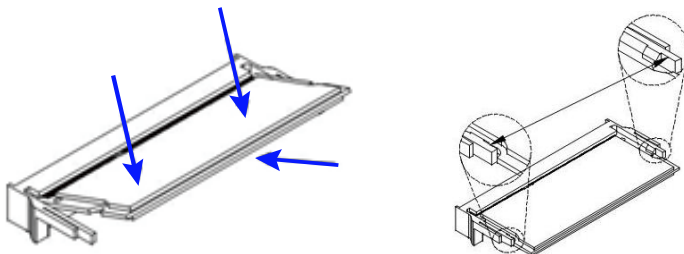


This final picture shows the top side of the main board as well as the screws to be removed to be able to access the reverse side of the main board.



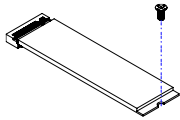
2.1.8 Memory Installation

To install a memory module, align the key of the module with the slot. Insert the module at an angle, then gently push it straight down until it is fully seated and the slot's clips snap into place. To remove a module, press the clips outward with both hands.



2.1.9 M.2 Card Installation

1. Align the key of the M.2 card with the socket and insert the card at a slight angle.
2. Push the card down, secure it to the standoff with a screw.



2.1.10 WiFi / 5G Antenna Installation

Carefully thread the WiFi / 5G antenna extension cable through one of the antenna holes. Apply adhesive to the edge of the hex nut behind the front I/O cover to prevent the extension cable from loosening.

1. Thread and fasten the hex nut and the washer, then install the antenna.

2. Apply adhesive around the nut to secure it.

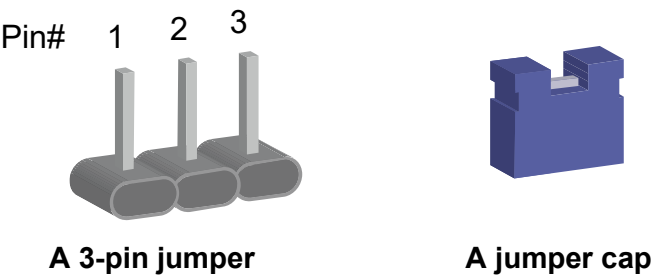


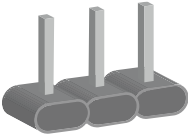
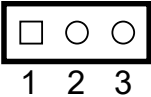
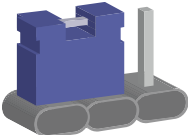
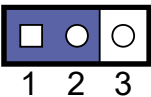
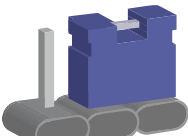
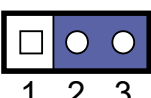
Info: The diameter of the nut is around 6.35 mm (0.25"-36UNC).

2.2 Setting the Jumpers

Whether you need to adjust jumper settings depends on whether the default configuration meets your requirements, the specific functions you need, or if you are performing upgrades or troubleshooting hardware issues. If you are unsure about the optimal configuration, contact your supplier for guidance.

Jumpers are short electrical connectors consisting of metal pins mounted on a non-conductive base on the circuit board. A removable jumper cap is placed over the pins to enable or disable specific functions or features. For a 3-pin jumper, you can short either PIN1–PIN2 or PIN2–PIN3 to achieve the desired connection.

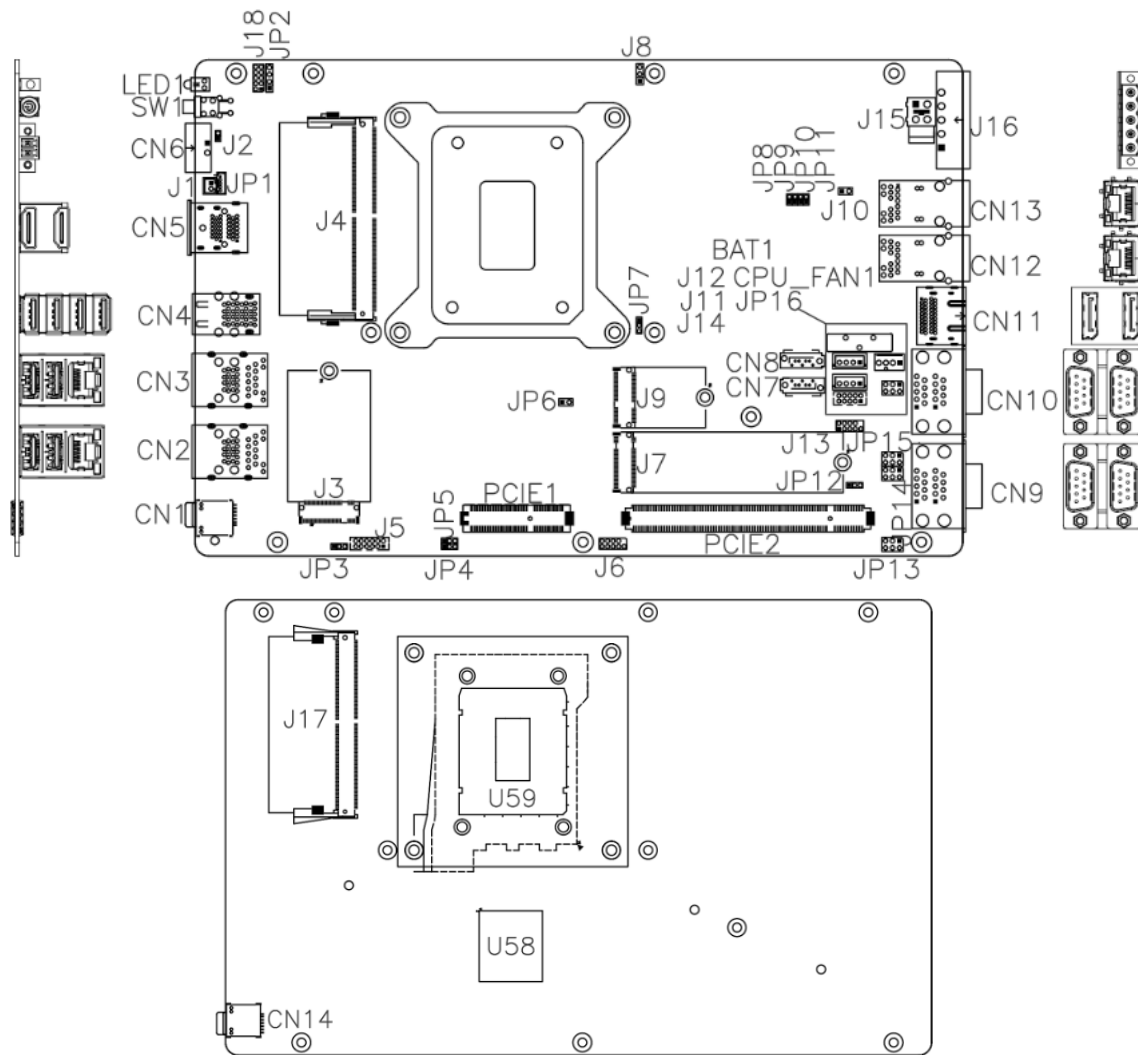


Pin closed	Oblique view	Illustration
Open		
1-2		
2-3		

When a jumper cap covers two pins, the jumper is **closed** (ON).

When the jumper cap is removed, the jumper is **open** (OFF).

2.3 Jumper & Connector Locations on Motherboard

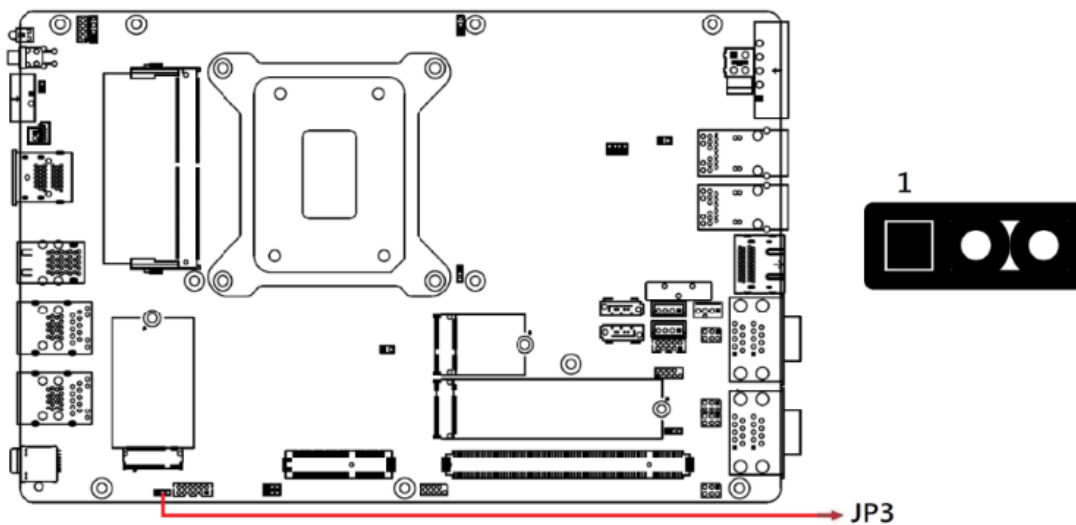


MBE240 Motherboard

2.4 Jumpers Quick Reference

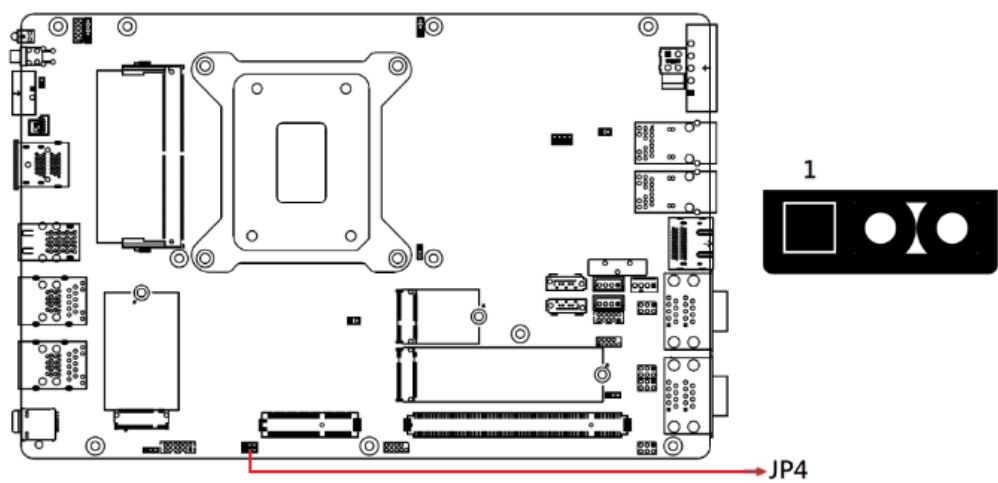
Function	Jumper
Sierra EM9 series 5G Card USB/PCIe Select	JP3
Clear ME Contents	JP4
Clear CMOS Data	JP5
Flash Descriptor Security Override	JP6
PCIe (x16) Bifurcation Select	JP7
Factory use only	JP8, JP9, JP10, JP11
COM1/COM2/COM3/COM4 RS232 RI/+5V/+12V Power Setting	JP16, JP15, JP14, JP13
ATX/AT Select	JP12

2.4.1 JP3: Sierra EM9 series 5G Card USB/PCIe Select



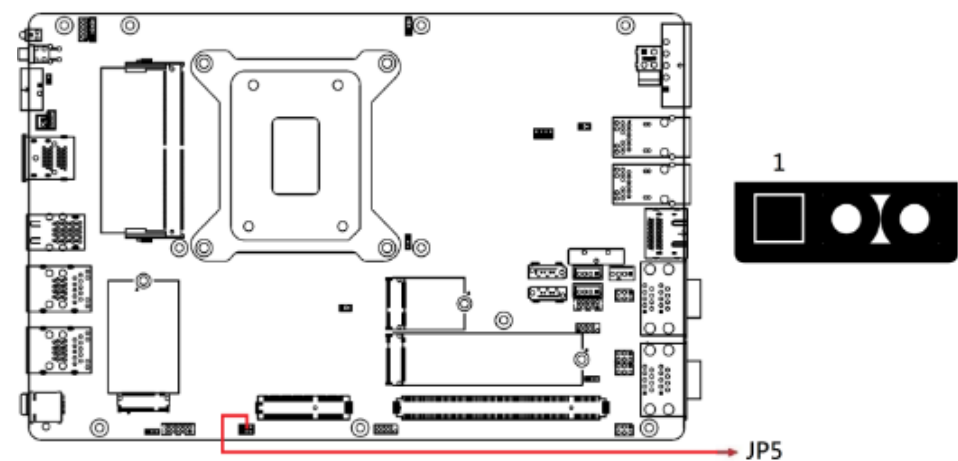
Function	Pin closed	Illustration
USB	1-2	1
PCIe (default)	2-3	1

2.4.2 JP4: Clear ME Contents



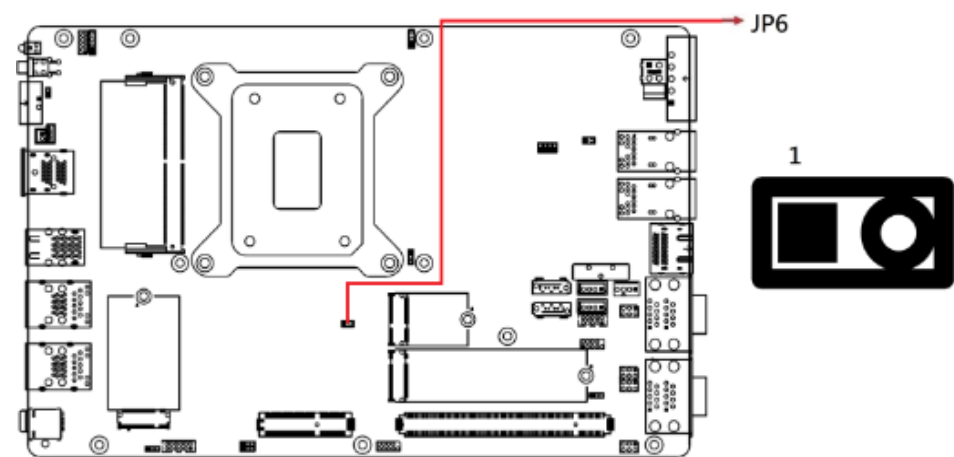
Function	Pin closed	Illustration
Normal	1-2	1
Clear ME	2-3	1

2.4.3 JP5: Clear CMOS Data



Function	Pin closed	Illustration
Normal (default)	1-2	1
Clear CMOS	2-3	1

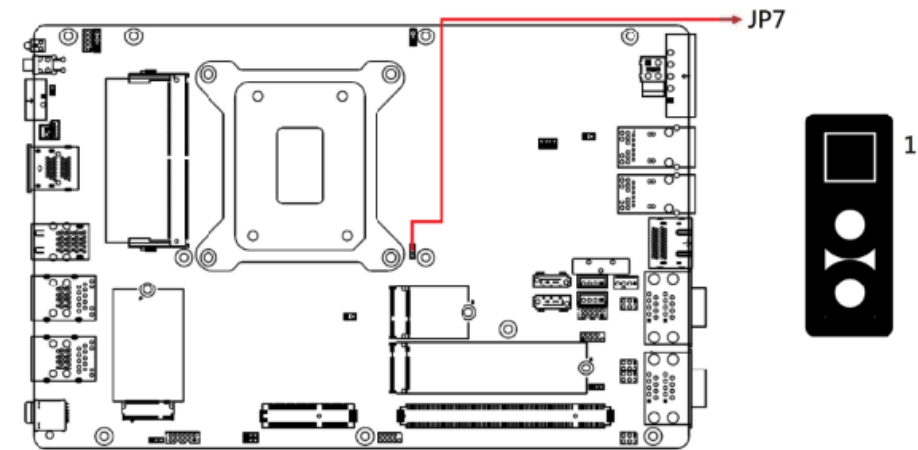
2.4.4 JP6: Flash Descriptor Security Override



Note: JP6 is for factory use only.

Flash Descriptor Security Override	Pin	Illustration
Disabled (default)	Open	 1
Enabled	Close	 1

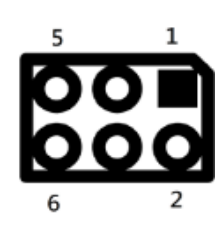
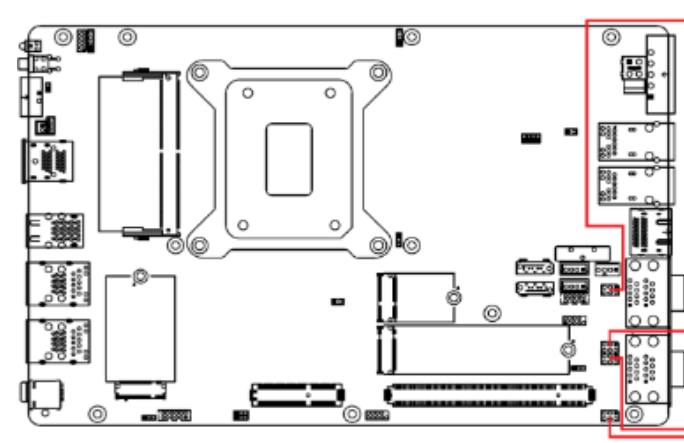
2.4.5 JP7: PCIe (x16) Bifurcation Select

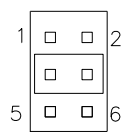


Function	Pin closed	Illustration
1 x PCIe (x16) (default)	1-2	1 
2 x PCIe (x8)	2-3	1 

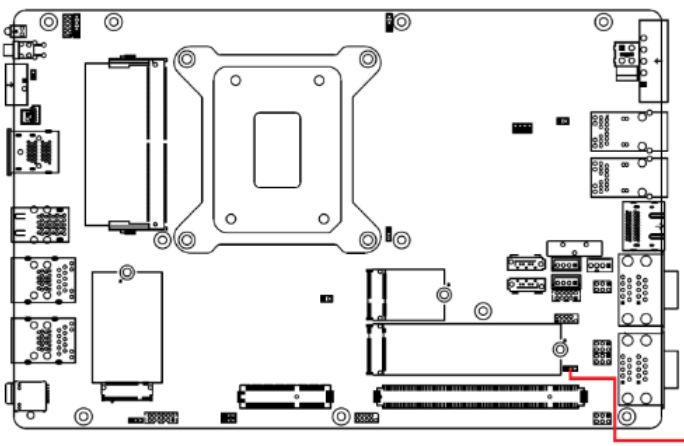
2.4.6 JP8, JP9, JP11: Factory use only

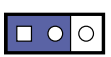
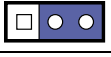
2.4.7 JP16, JP15, JP14, JP13: COM1/COM2/COM3/COM4 RS232
RI/+5V/+12V Power Setting



	Setting	Function
	Pin 1-3, Short/Closed	+12V
	Pin 3-4, Short/Closed	RI (Default)
	Pin 3-5, Short/Closed	+5V

2.4.8 JP12: ATX/AT Select

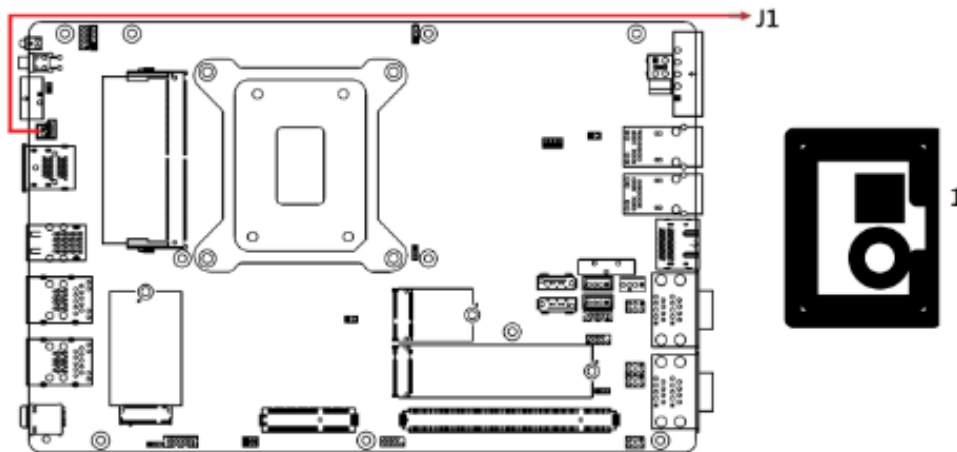


Function	Pin closed	Illustration
ATX (default)	1-2	1 
AT	2-3	1 

2.5 Connectors Quick Reference

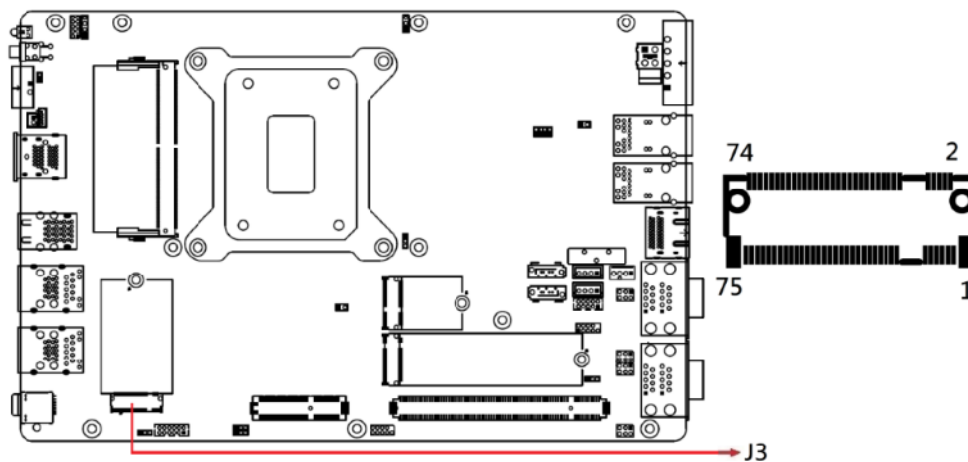
Function	Connector	Page
Reset Button Connector	J1	35
M.2 B-Key 3052 Slot	J3	35
Audio Connector	J5	36
SPI Flash Connector	J6	36
M.2 M-Key M2280 Slot	J7	37
Factory use only	J8	37
M.2 E-Key E2230 Slot	J9	38
SATA Power Connector	J11, J12	38
DDR5 Memory Sockets	J4, J17	39
80 Port Debug (Factory use only)	J13	39
Digital I/O Connector	J14	40
DC-in Connector	J15	40
DC-in Connector	J16	41
HDD LED	LED1	41
CN13 PoE LED	LED3	42
CN12 PoE LED	LED4	42
Power On Button	SW1	42
Nano SIM card 2, Nano SIM card 1 Slot	CN1, CN14	43
2.5 Gigabit LAN (Intel I226-V) + USB 3.2	CN2	43
2.5 Gigabit LAN (Intel I226-LM) + USB 3.2	CN3	43
USB 3.2 Connector	CN4	44
HDMI Port X2	CN5	44
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SATA III Connector	CN8, CN7	45
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Gigabit LAN (Intel I210IT) + PoE+	CN13	48
PCI-E x1, USB 2.0 Sys_Fan, SATA, COM TX/RX Signal	PCIE1	49
PCI-E-E x16 Connector	PCIE2	49

2.5.1 J1: Reset Button Connector (Techbest 01017021001-L)



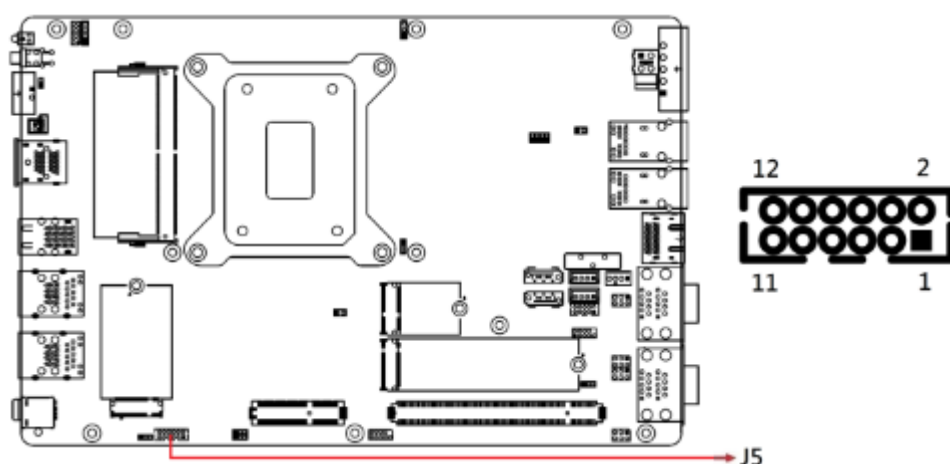
Pin	Signal Name
1	Ground
2	Reset BTN

2.5.2 J3: M.2 B-Key 3052 Slot



Note: J3 supports PCIe(1x) + USB 3.2 for 5G Sierra EM9 series 5G modules

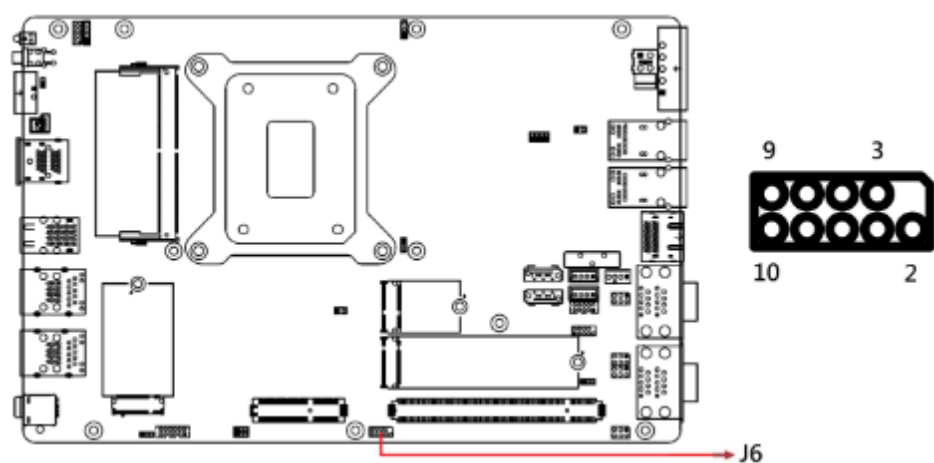
2.5.3 J5: Audio Connector



Pin	Assignment	Pin	Assignment
1	LINE OUT_L	2	LINE OUT_R
3	LINE OUT_Sense	4	Ground
5	LINE_L	6	LINE_R
7	LINE_Sense	8	Ground
9	MIC IN_L	10	MIC IN_R
11	MIC_Sense	12	Ground

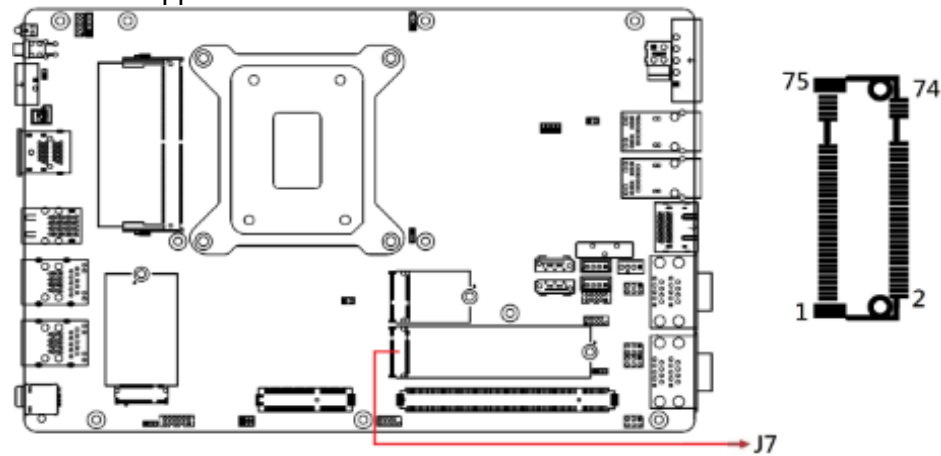
2.5.4 J6: SPI Flash Connector

Note: J6 is for factory use only.

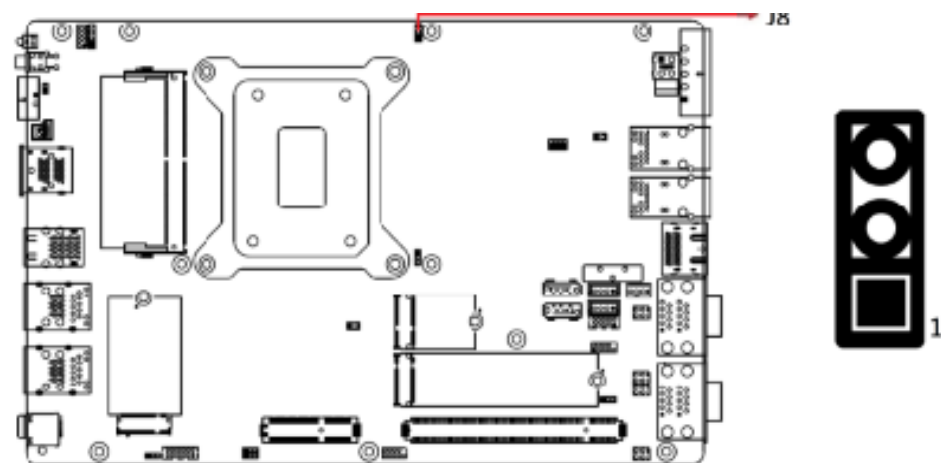


2.5.5 J7: M.2 M-Key M2280 Slot

Note: J7 supports NVME/SATA.

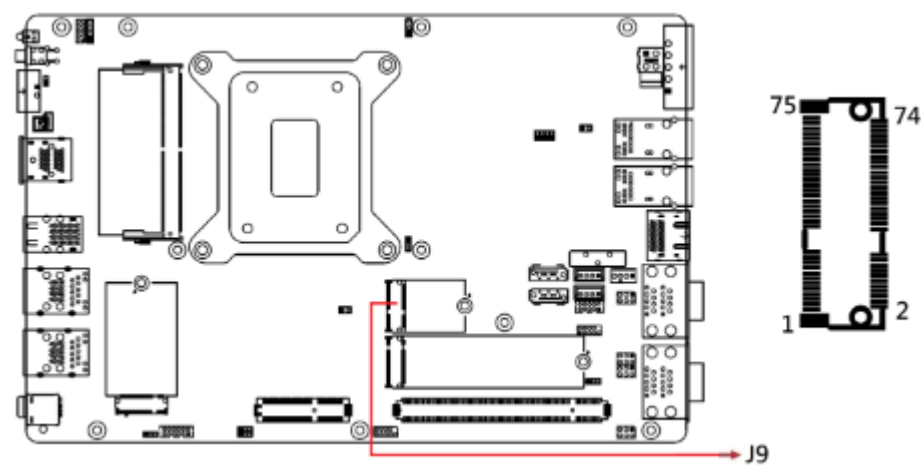


2.5.6 J8: Factory use only



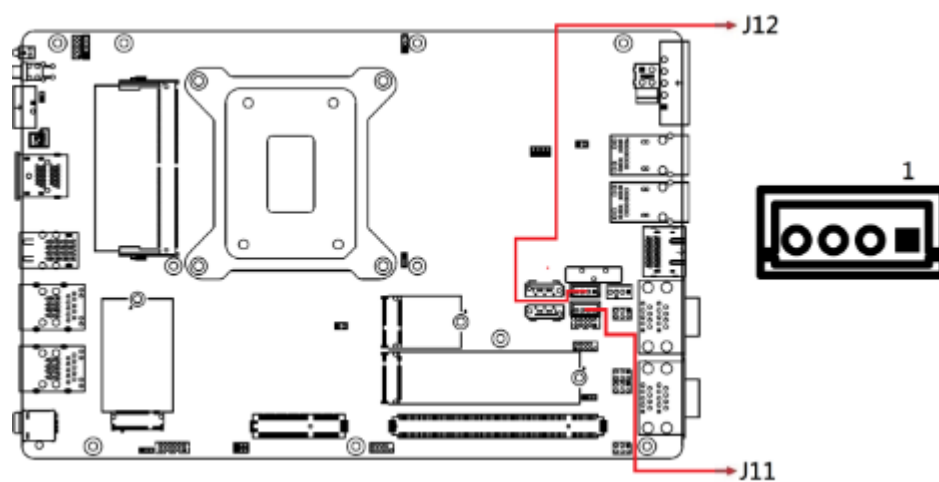
2.5.7 J9: M.2 E-Key E2230 Slot

Note: J9 supports CNVi / PCIe(1x) / USB2.0



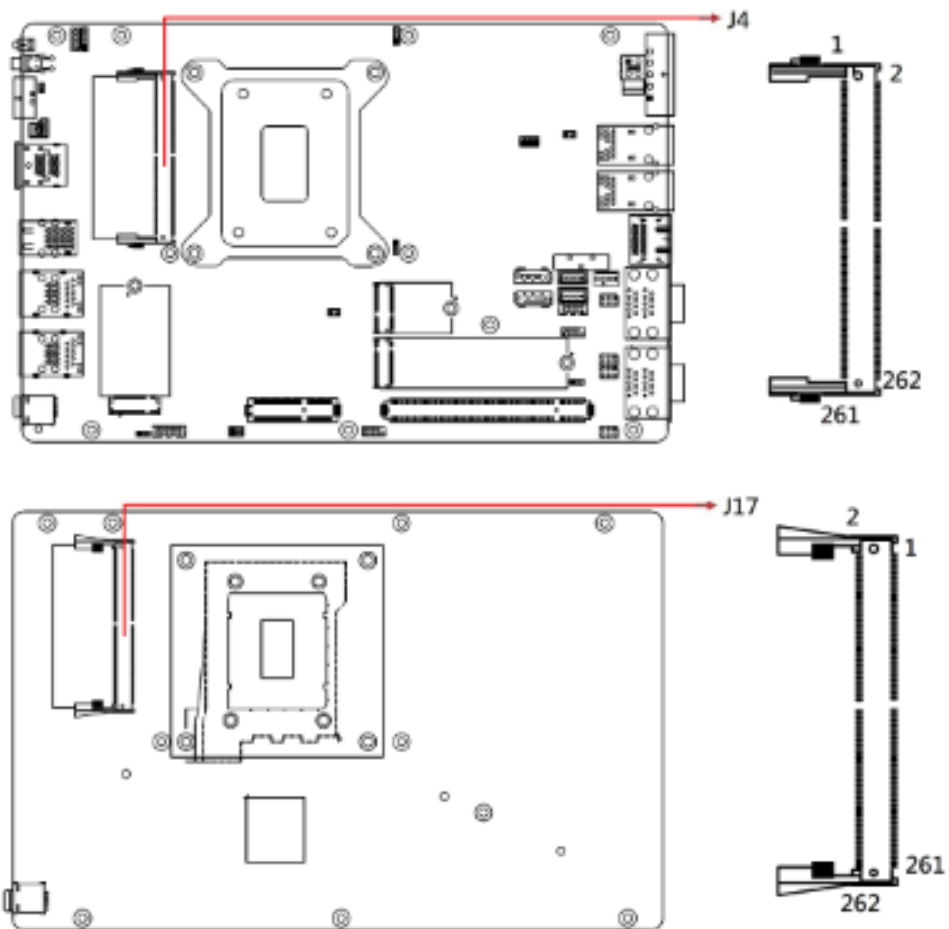
2.5.8 J11, J12: SATA Power Connector

Note: (JST_B4B-XH-A → Compatible Mating JST_XHP-4)

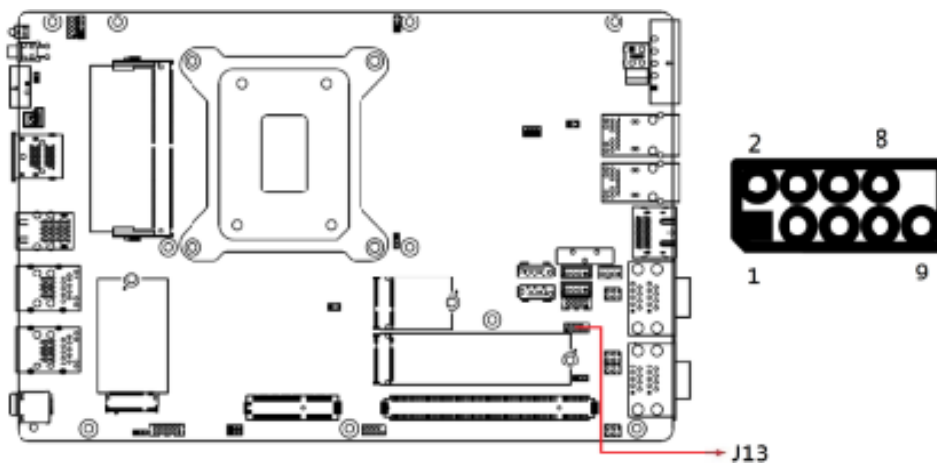


Pin	Signal Name
1	+5V
2	Ground
3	Ground
4	+12V

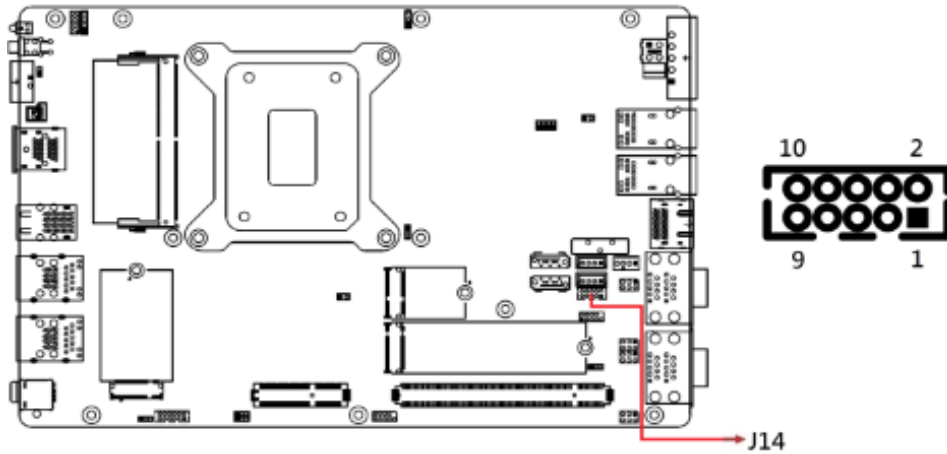
2.5.9 J4, J17: DDR5 Memory Sockets



2.5.10 J13: 80 Port Debug (Factory use only)



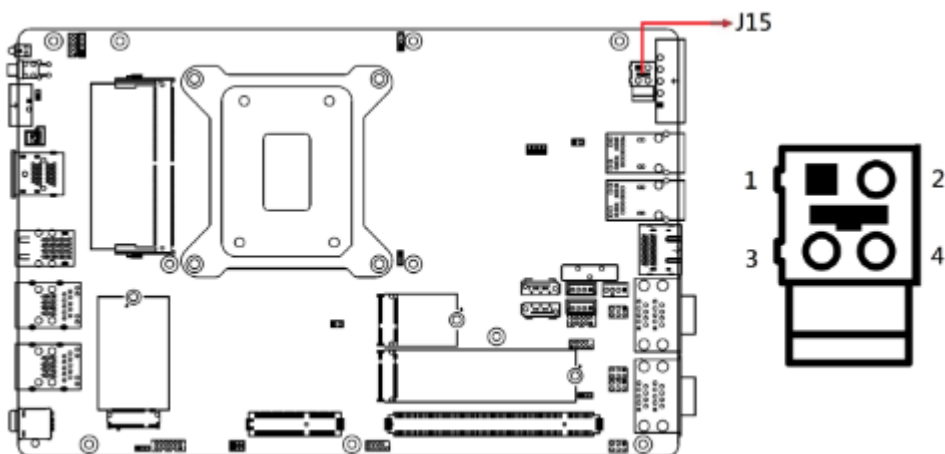
2.5.11 J14: Digital I/O Connector



Note: DF11-10S-PA66H HRS Compatible Mating DF11-10DS-2C

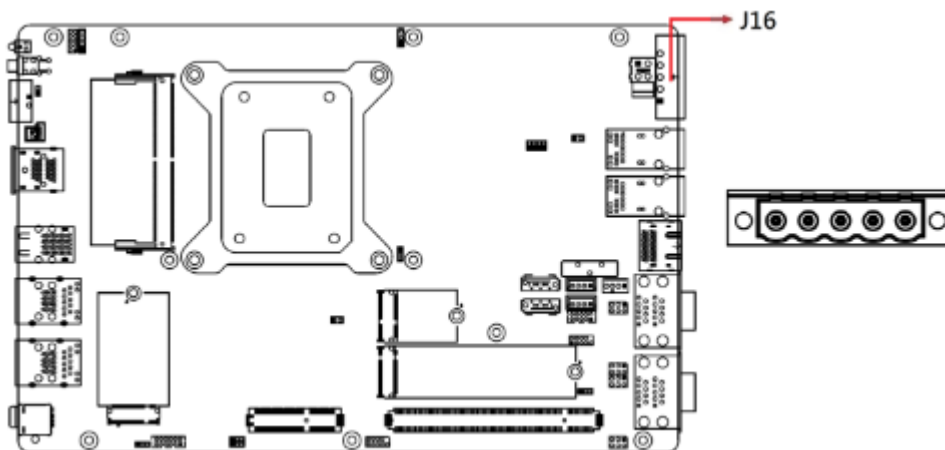
Pin	Signal Name	Pin	Signal Name
1	Ground	2	+5V(0.5A)
3	OUT3	4	OUT1
5	OUT2	6	OUT0
7	IN3	8	IN1
9	IN2	10	IN0

2.5.12 J15: DC-in Connector



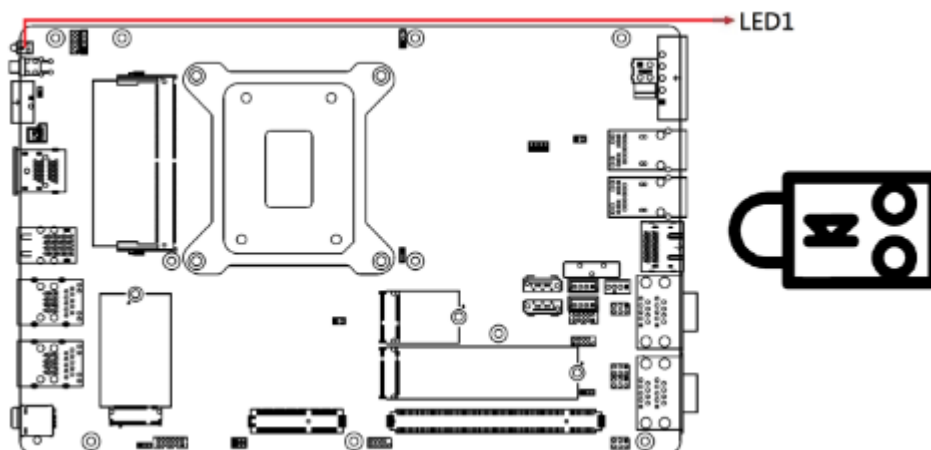
Pin	Signal Name
1	Power Ground
2	Case Ground
3	+24V
4	+24V

2.5.13 J16: DC-in Connector (Dinkle 5EHDRM-05P)

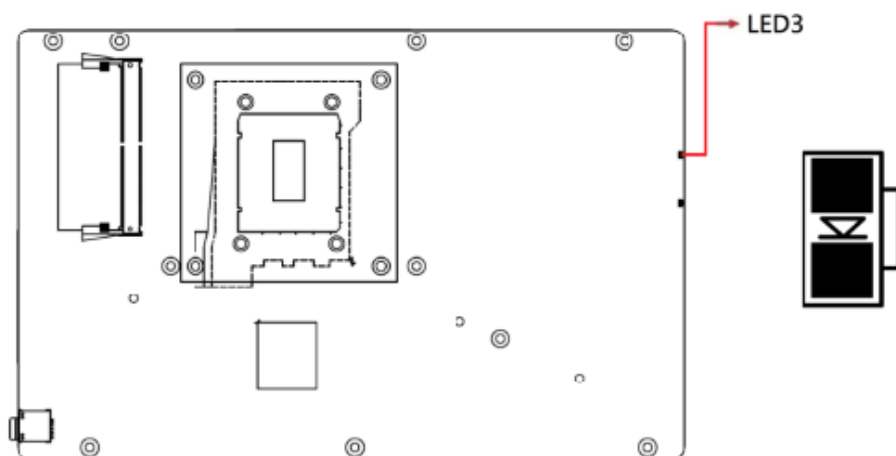


Pin	Signal Name
1	+24V
2	+24V
3	Case Ground
4	Power Ground
5	Power Ground

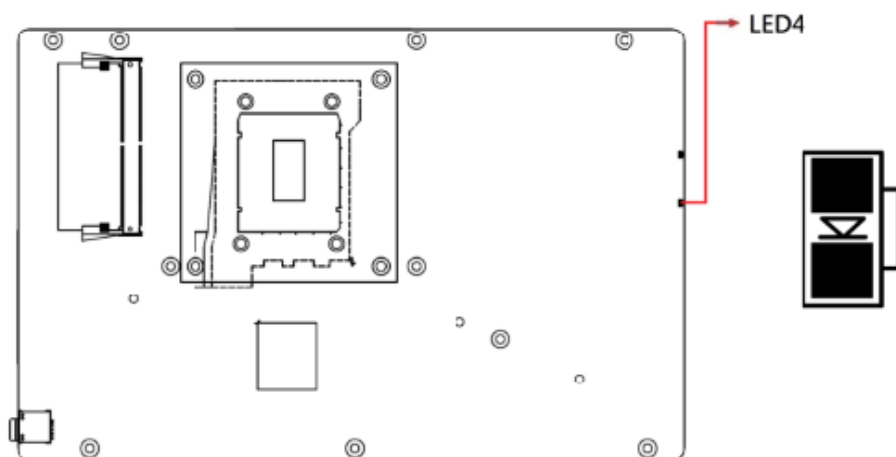
2.5.14 LED1 : HDD LED



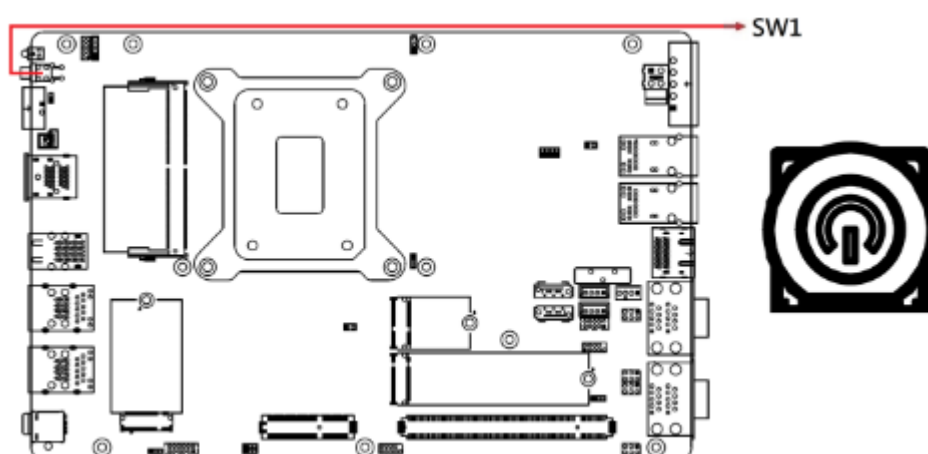
2.5.15 LED3: CN13 PoE LED



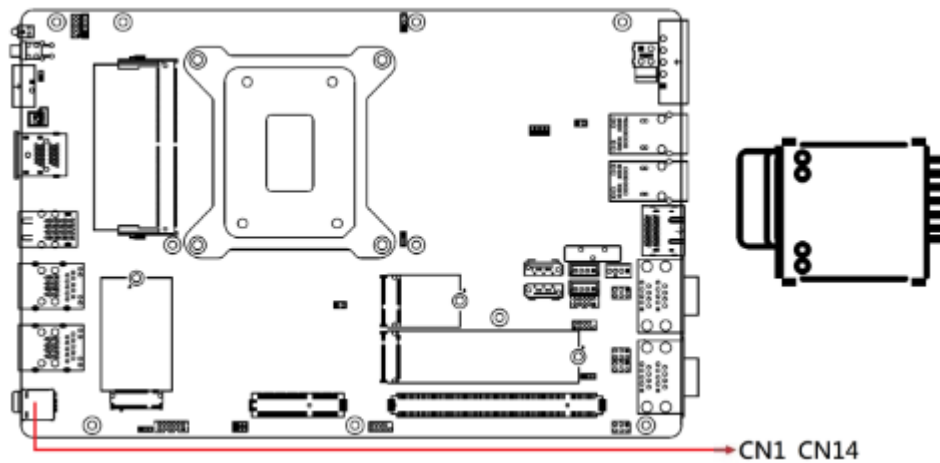
2.5.16 LED4: CN12 PoE LED



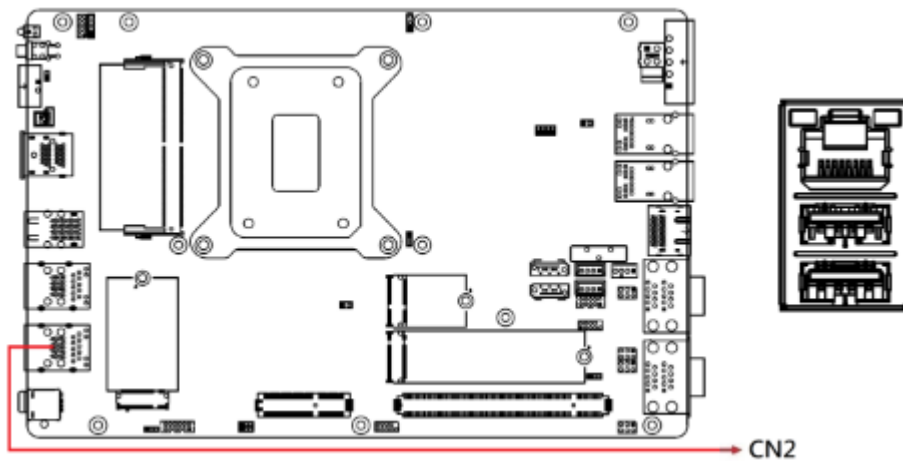
2.5.17 SW1: Power On Button



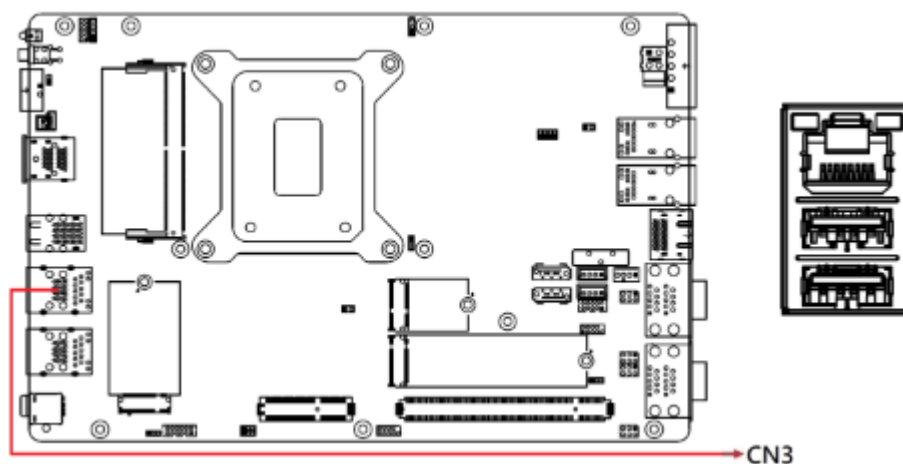
2.5.18 CN1, CN14 : Nano SIM card 2, Nano SIM card 1 Slot



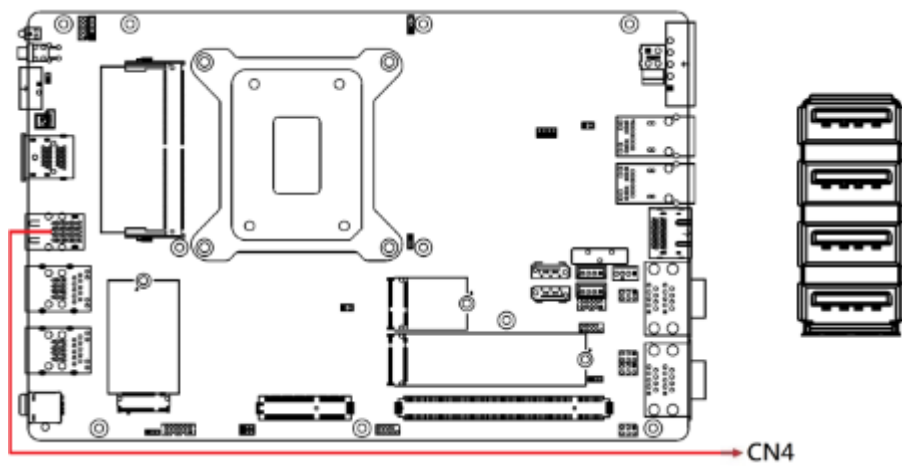
2.5.19 CN2: 2.5 Gigabit LAN (Intel I226-V) + USB 3.2



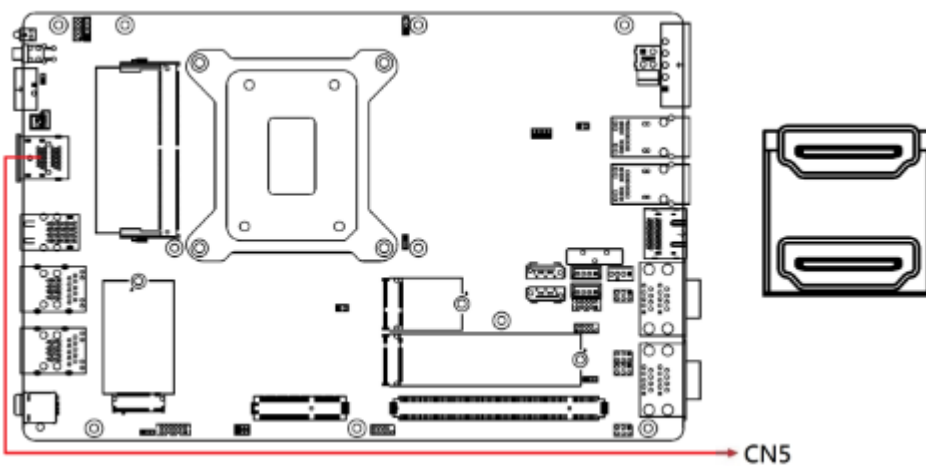
2.5.20 CN3: 2.5 Gigabit LAN (Intel I226-LM) + USB 3.2



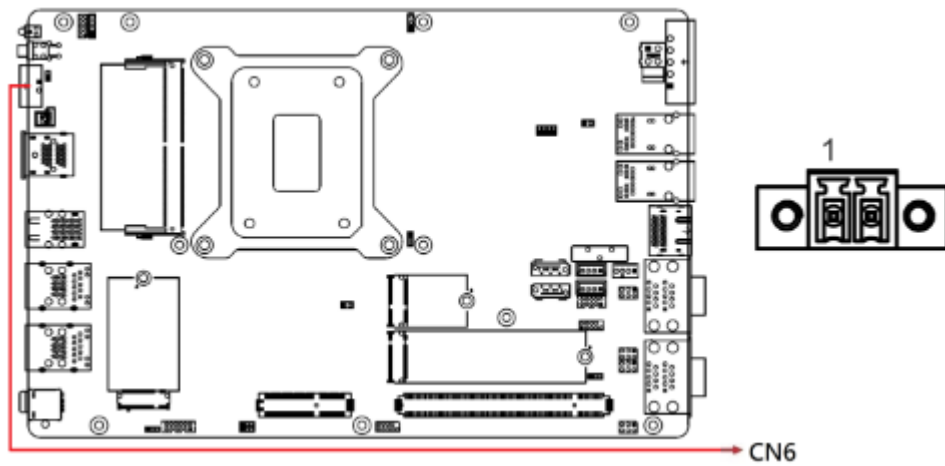
2.5.21 CN4: USB 3.2 Connector



2.5.22 CN5: HDMI Port x2

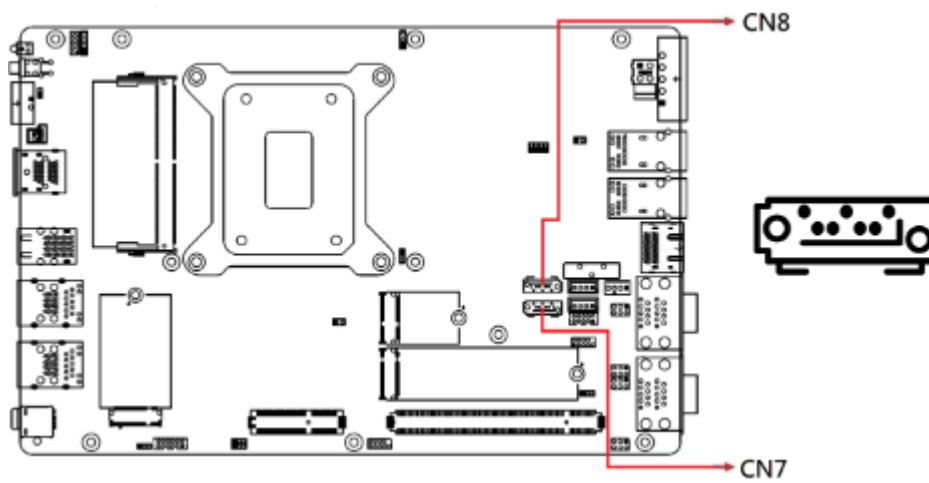


2.5.23 CN6: Power Button Connector (Dinkle ECH350RM-02P)

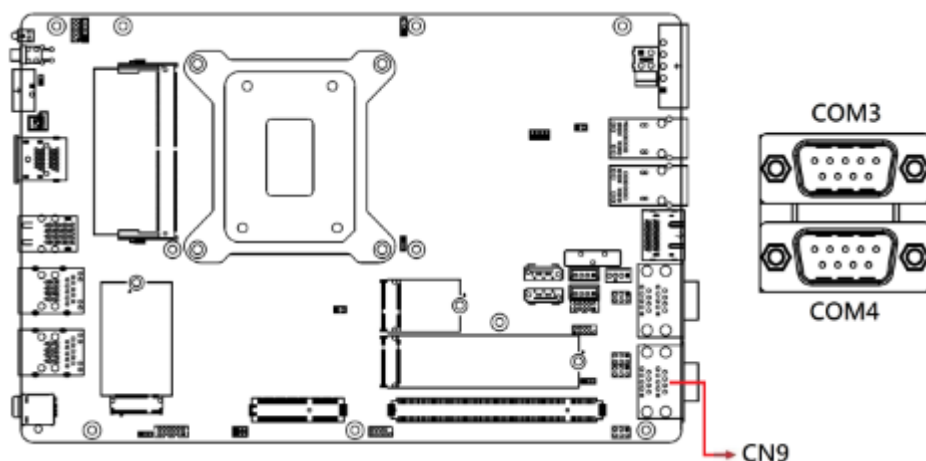


Pin	Signal Name
1	Power BTN
2	Ground

2.5.24 CN8, CN7: SATA III Connectors

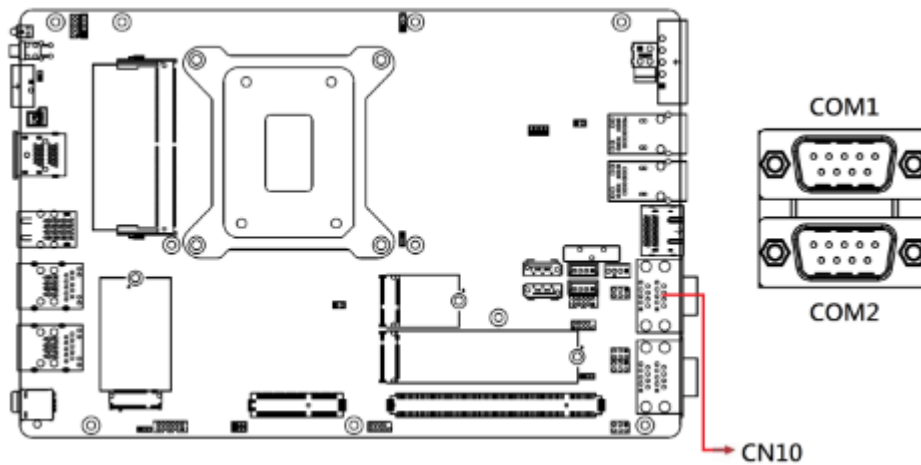


2.5.25 CN9: COM3 & COM4 RS-232 Ports



Pin	Signal Name	Pin	Signal Name
1	DCD, Data carrier detect	6	DSR, Data set ready
2	RXD, Receive data	7	RTS, Request to send
3	TXD, Transmit data	8	CTS, Clear to send
4	DTR, Data terminal ready	9	RI, Ring indicator
5	Ground		

2.5.26 CN10: COM1 & COM2 RS-232/422/485 Ports

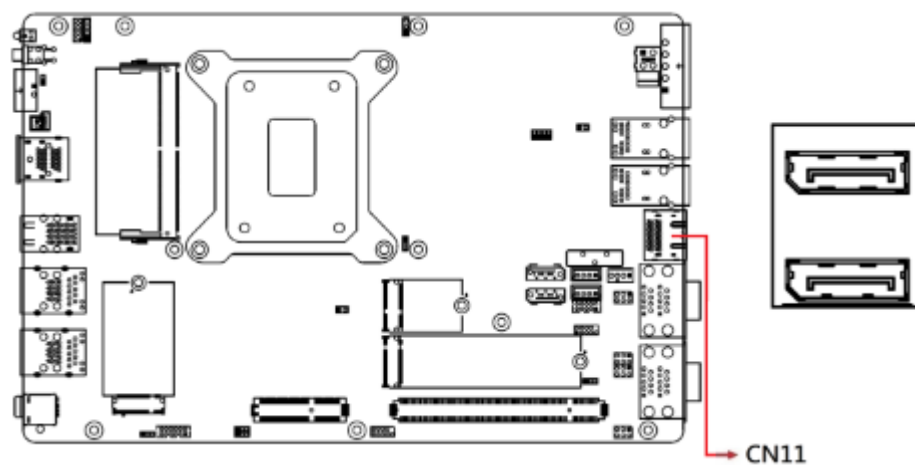


Pin	Signal Name	Pin	Signal Name
1	DCD, Data carrier detect	6	DSR, Data set ready
2	RXD, Receive data	7	RTS, Request to send
3	TXD, Transmit data	8	CTS, Clear to send
4	DTR, Data terminal ready	9	RI, Ring indicator
5	Ground		

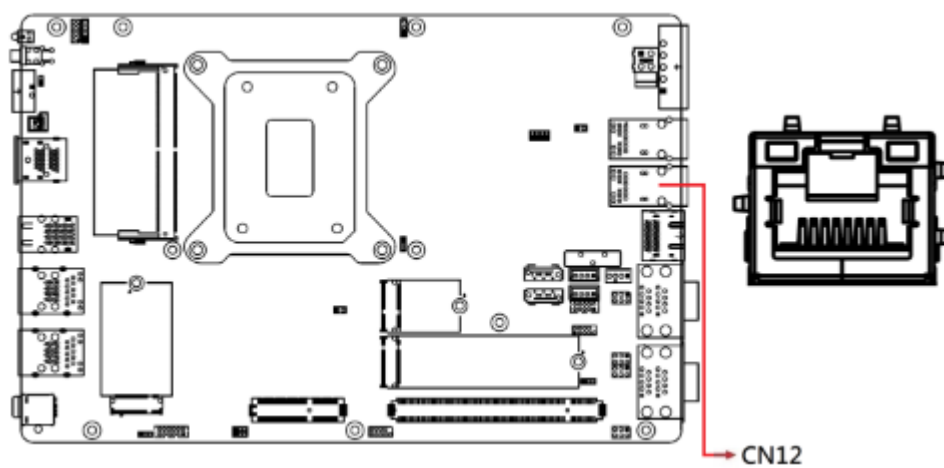
COM1/COM2 RS-232/422/485 are jumperless, configurable in BIOS.

Pin	Signal Name		
	RS-232	RS-422	RS-485
1	DCD	TX-	DATA-
2	RX	TX+	DATA+
3	TX	RX+	NC
4	DTR	RX-	NC
5	Ground	Ground	Ground
6	DSR	NC	NC
7	RTS	NC	NC
8	CTS	NC	NC
9	RI	NC	NC

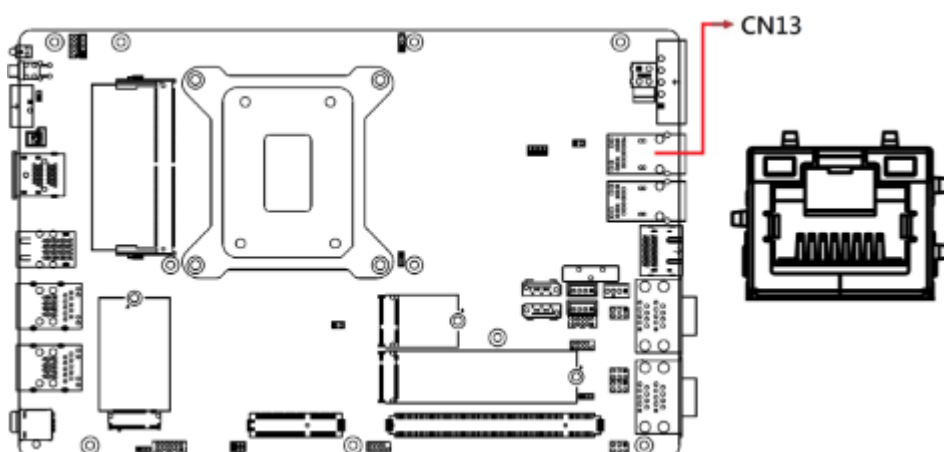
2.5.27 CN11: DisplayPort x2



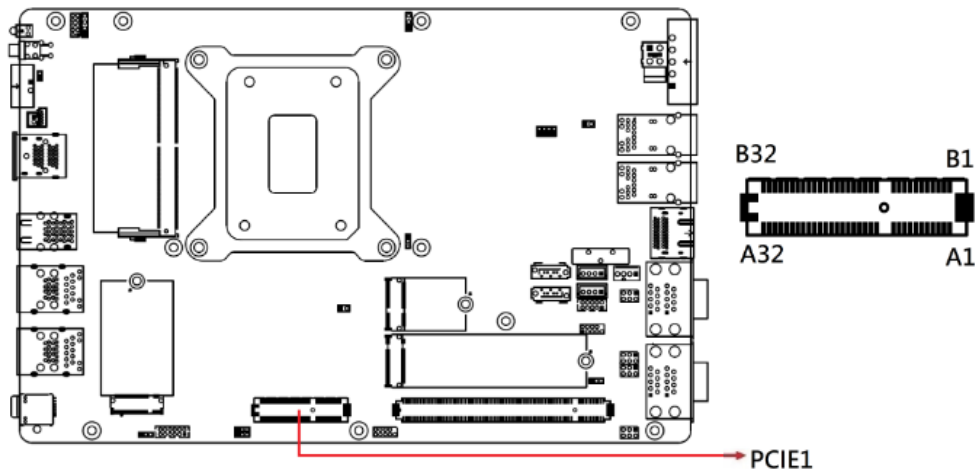
2.5.28 CN12: Gigabit LAN (Intel I210IT) + PoE+



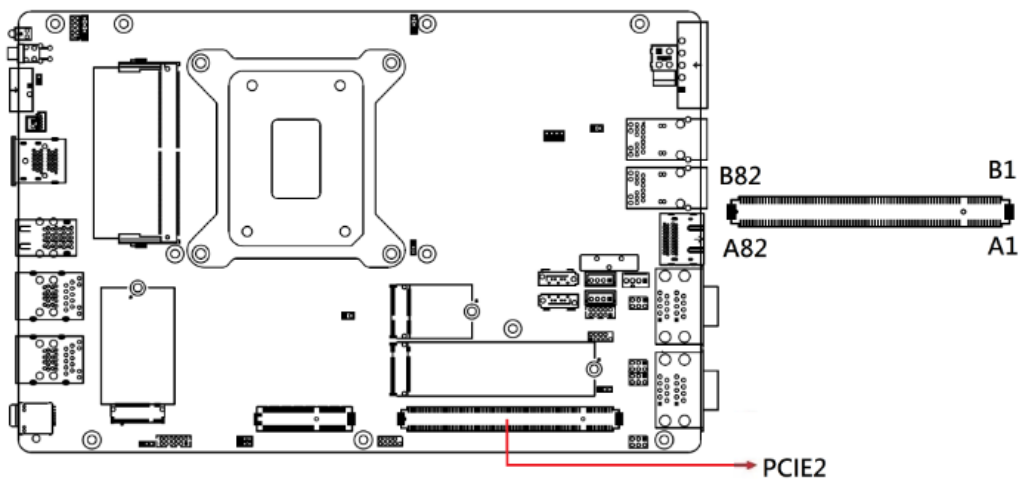
2.5.29 CN13: Gigabit LAN (Intel I210IT) + PoE+



2.5.30 PCIE1: For PCI-E x1, USB 2.0 Sys_Fan, SATA, COM TX/RX Signal



2.5.31 PCIE2: PCIE-E x16 Connector



Chapter 3

Driver Installation

The information provided in this chapter includes:

- Intel® Chipset Software Installation Utility
- Graphics Driver Installation
- HD Audio Driver Installation
- LAN Drivers Installation
- Intel® Management Engine Drivers Installation
- Intel® Serial IO Drivers Installation

3.1 Introduction

This section describes the installation procedures for software drivers.

Note: After installing your Windows operating system, install the Intel® Chipset Software Installation Utility before proceeding with drivers installation.

3.2 Intel® Chipset Software Installation Utility

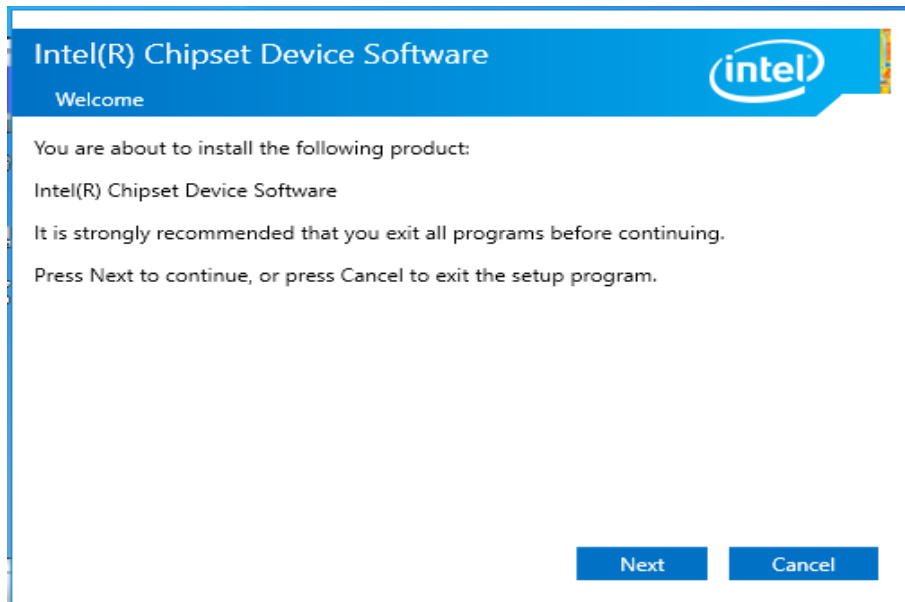
Before installing any software drivers, start with the Intel® Chipset drivers to ensure the operating system can effectively manage the motherboard's chipset components for optimal Plug & Play functionality.

3. Go to the download page of the product. Copy the compressed drivers file to your computer. Double click the file to decompress it. Run "CDGuide" to go to the main drivers page as shown. Click **Intel** on the left pane and then **Intel(R) AlderLake-S/RaptorLake-S Chipset Drivers**, and **Intel(R) Chipset Software Installation Utility** on the right pane.





4. When the *Welcome* screen to the Intel® Chipset Device Software appears, click **Next** to continue.



5. Accept the *License Agreement* and click **Accept**.
6. On the *Readme File Information* screen, click **Install**.
7. When the driver has been completely installed, click **Finish** to complete the setup process.

3.3 Graphics Driver Installation

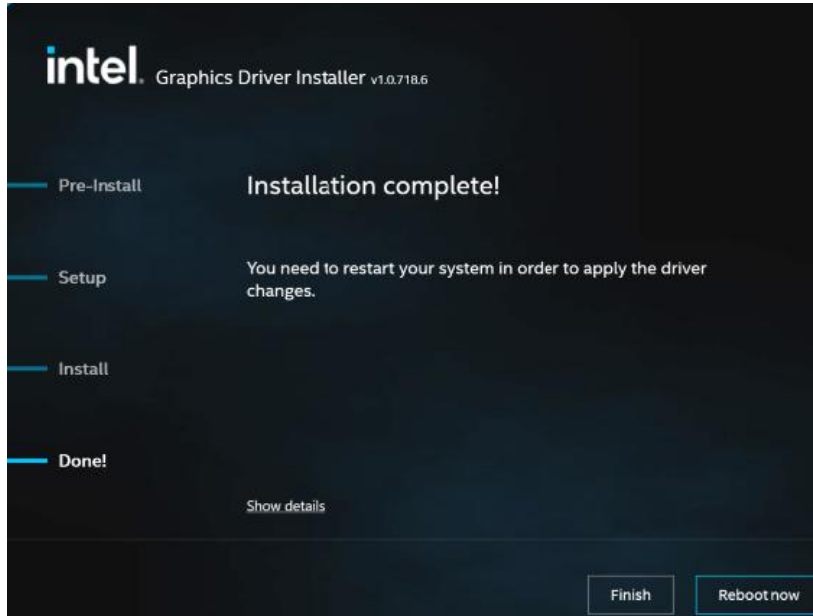
1. Click **Intel** on the left pane and then **Intel(R) AlderLake-S/ RaptorLake-S Chipset Drivers**, and **Intel(R) HD Graphics Driver** on the right pane.



2. When the **Intel Graphics Driver Installer** screen appears, click **Begin installation**.



3. Click **I agree** to accept the INTEL SOFTWARE LICENSE AGREEMENT.
4. In the Pre-Install stage, “The installer will install the following components:
 - Intel® Graphics Driver
 - Intel® Graphics Command CenterClick **Start** to start installing the new graphics driver.
5. The next screen will indicate that the new graphics driver is being installed. When the message “**Installation complete!**” appears, restart your system in order to apply the driver changes.

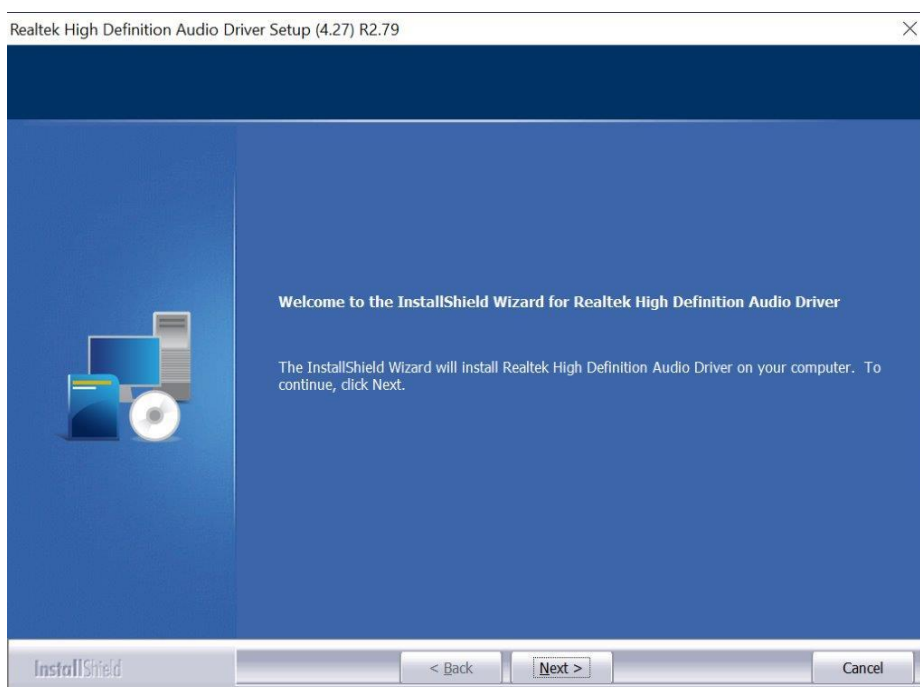


3.4 HD Audio Driver Installation

1. Click **Intel** on the left pane and then **Intel(R) AlderLake-S/ RaptorLake-S Chipset Drivers**, and **Realtek High Definition Audio Driver** on the right pane.



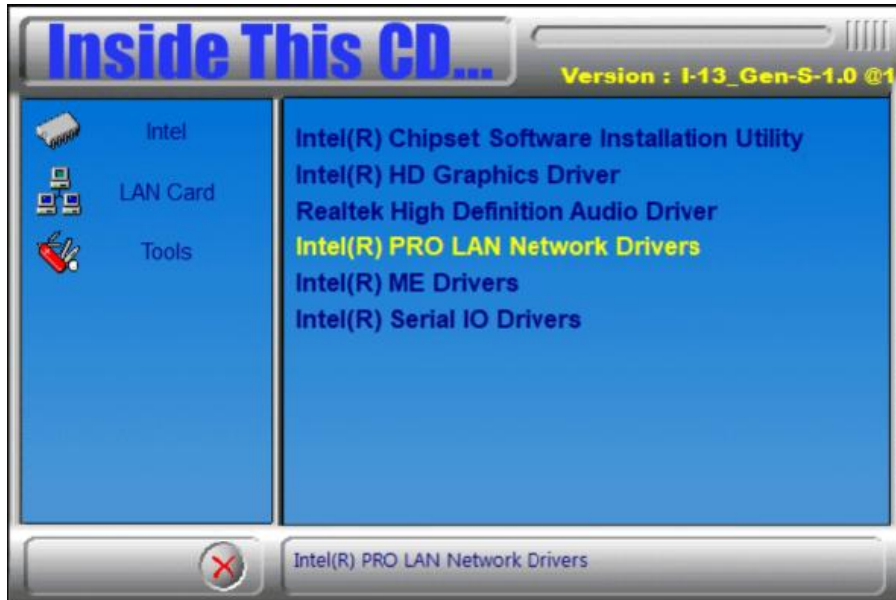
2. On the Welcome screen of the InstallShield Wizard, click **Next** to install the driver.



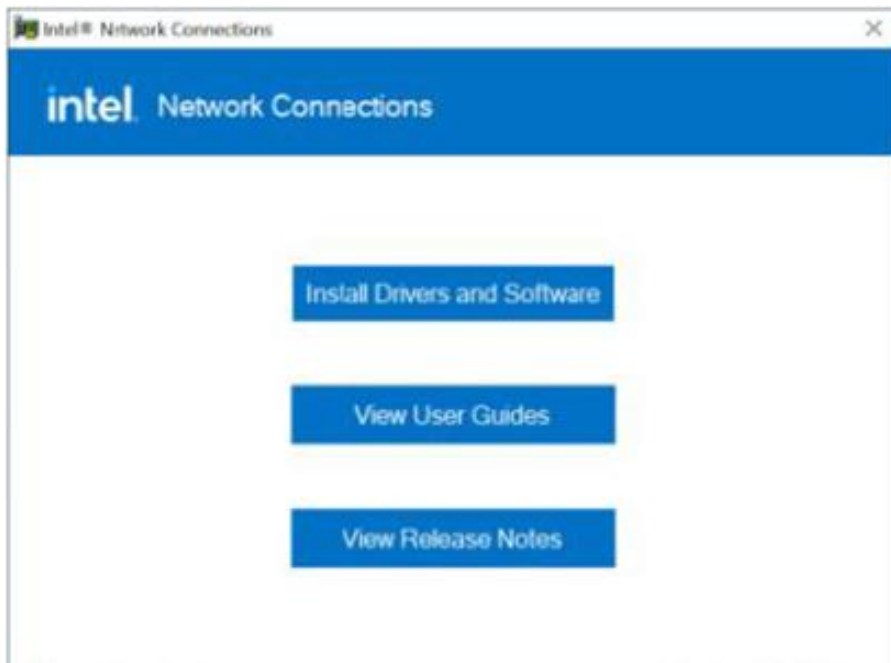
3. When the audio driver has been successfully installed, click **Finish** to restart the computer.

3.5 LAN Drivers Installation

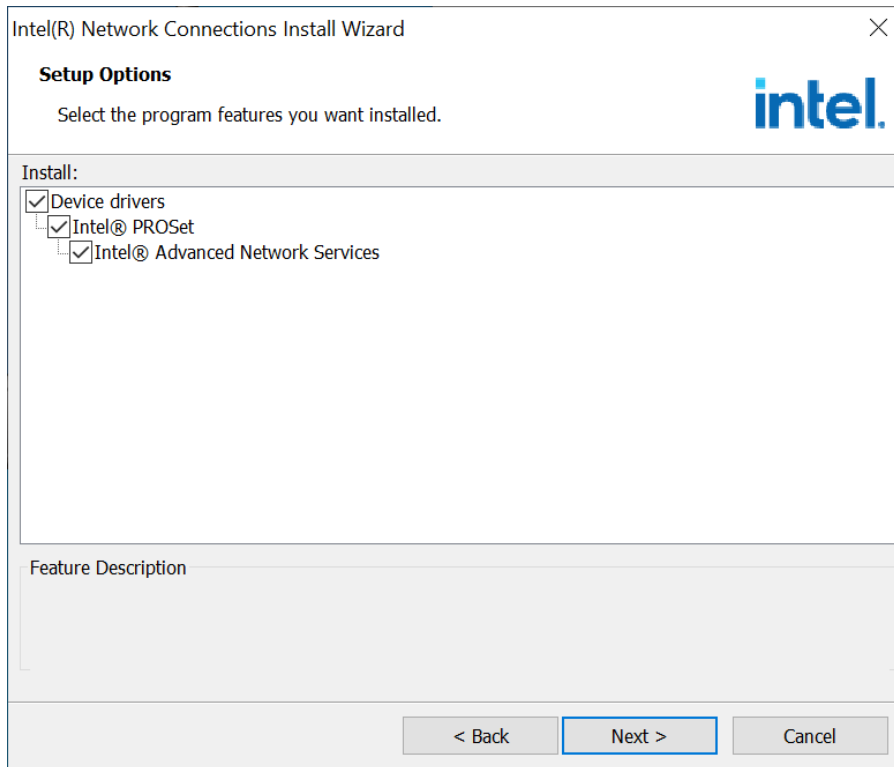
1. Click **LAN Card** on the left pane and then **Intel PRO LAN Network Drivers** on the right pane.



2. Click **Intel Drivers and Software**.



3. When the Welcome to the install wizard for Intel(R) Network Connection screen appears, click **Next**. On the next screen, accept the terms in the License Agreement and click **Next**.
4. On the Setup Options screen, select the program features you want installed. Then click **Next** to continue.



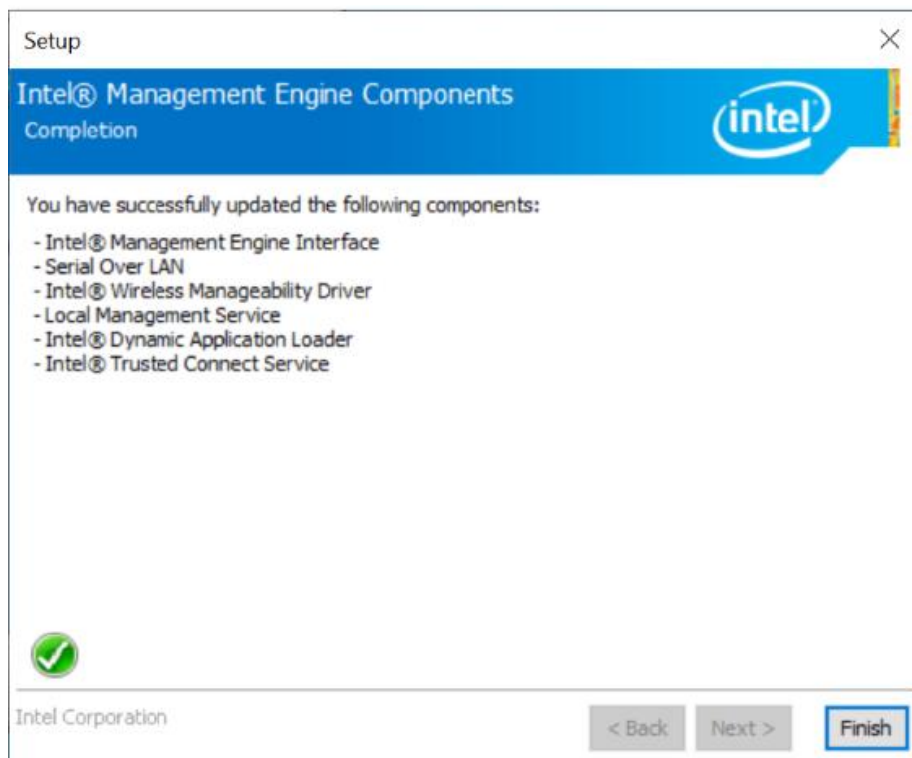
5. On the Ready to Install the Program screen, click **Install** to begin the installation.
6. When the Install wizard Completed screen appears, click **Finish**.

3.6 Intel® Management Engine Driver Installation

1. Click **Intel** on the left pane and then **Intel(R) AlderLake-S/ RaptorLake-S Chipset Drivers**, and **Intel(R) ME Drivers** on the right pane.



2. When the Welcome screen to the Intel® Management Engine Components appears, click **Next**.
3. Accept the terms in the License Agreement and click **Next**.
4. On the next screen, click **Next** to install to the default folder.
5. Click **Finish** when the necessary components have been successfully installed.

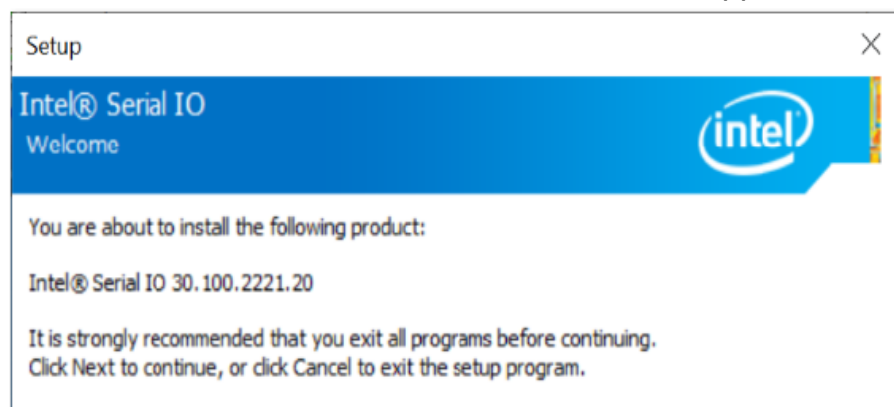


3.7 Intel® Serial IO Drivers Installation

1. Click **Intel** on the left pane and then **Intel(R) AlderLake-S/ RaptorLake-S Chipset Drivers**, and **Intel(R) Serial IO Drivers** on the right pane.



2. When the Welcome screen to the Intel® Serial IO appears, click **Next**.



3. Accept the terms in the license agreement and click **Next**.
4. On the Readme File Information and Confirmation screens, click **Next**.
5. Click **Finish** when the Completion screen appears.

Chapter 4

BIOS Setup

This chapter describes the different settings available in the AMI BIOS that comes with the board. The topics covered in this chapter are as follows:

- Main Settings
- Advanced Settings
- Chipset Settings
- Security Settings
- Boot Settings
- Save & Exit

4.1 Introduction

The BIOS (Basic Input/Output System) installed in the ROM of your computer system supports Intel® processors. The BIOS provides critical low-level support for standard devices such as disk drives and serial ports. It also provides password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

4.2 BIOS Setup

The BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the BIOS is immediately activated. Press the key immediately allows you to enter the Setup utility. If you are a little bit late pressing the key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. You can also press <F7> to call the pop-up Boot menu immediately.

If you still need to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again.

The following message will appear on the screen:

```
Press <DEL> to Enter Setup
```

In general, press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help, and <Esc> to quit.

When you enter the BIOS Setup utility, the *Main Menu* screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

Warning: It is strongly recommended that you avoid making any changes to the chipset defaults.

These defaults have been carefully chosen by both AMI and your system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could make the system unstable and crash in some cases.

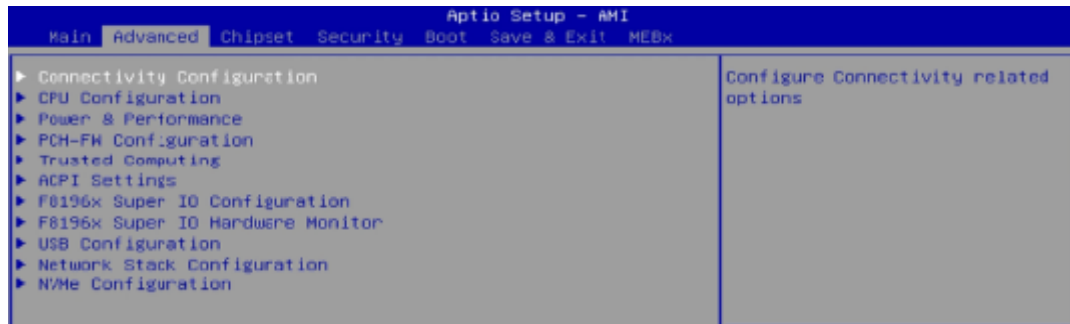
4.3 Main Settings



BIOS Setting	Description
System Language	Choose the system default language.
System Date	Sets the date. Use the <Tab> key to switch between the date elements.
System Time	Set the time. Use the <Tab> key to switch between the time elements.

4.4 Advanced Settings

This section allows you to configure, improve your system and to set up some system features according to your preference.



4.4.1 Connectivity Configuration



BIOS Setting	Description
CNVi Mode	This option configures Connectivity. [Auto Detection] means that if discrete solution is discovered it will be enabled by default. Otherwise, integrated solution (CNVi) will be enabled; [Disable Integrated] disables Integrated Solution.
BT Audio Offload	This is an option to enable/disable BT audio offload which enables audio DSP and enables power efficient audio output to BT device.
RFI Mitigation	This is an option intended to enable/disable DDR-RFIM feature for connectivity. This RFI mitigation feature may result in temporary slowdown of the DDR speed.
CoExistence Manager	CoEx Manager mitigates radio coexistence issues between Intel WWAN (modem) and Intel WLAN (WiFi/BT). This should be enabled only if both WWAN and WLAN solutions are based on Intel components.
Preboot BLE	This will be used to enable Preboot Bluetooth function.

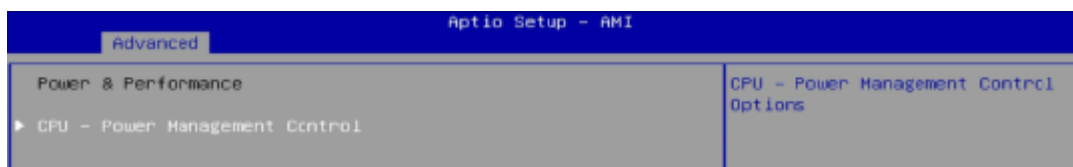
Discrete Bluetooth Module	Seriallo UART0 needs to be enabled to select BT module.
BT Tile Mode	Enable/Disable Tile
Advanced Settings	Configure ACPI objects for wireless devices
WWAN Device	Select the M.2 WWAN Device options to enable 4G – 7360/7560 (Intel) 5G – MediaTek Modems

4.4.2 CPU Configuration



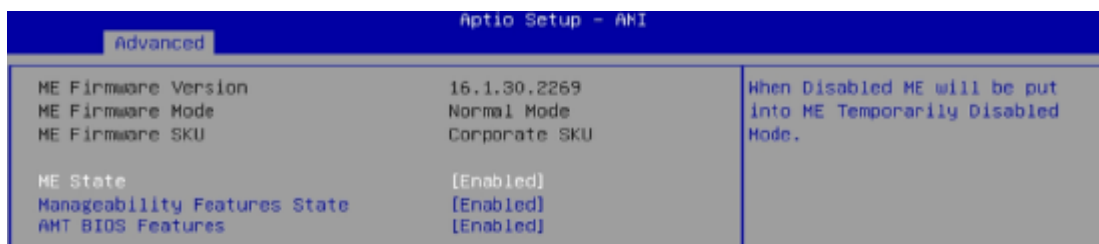
BIOS Setting	Description
Intel (VMX) Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
Active Processor Cores	Number of cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are (0,0), Pcode will enable all cores. Options: All, 3, 2, 1
Hyper-Threading	Enable or Disable Hyper-Threading Technology.
Legacy Game Compatibility Mode	When enable, pressing the scroll lock key will toggle the Efficient-cores between being parked with scroll lock LED is on and un-parked when LED is off.

4.4.3 Power & Performance



BIOS Setting	Description
CPU – Power Management Control	CPU – Power Management Control Options
Intel(R) SpeedStep(tm)	Willows more than two frequency ranges to be supported.
Intel(R) Speed Shift Technology	Enable/Disable Intel(R) Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.
Turbo Mode	Enable/Disable processor turbo mode (requires EMTTM enabled too). AUTO means enabled.

4.4.4 PCH-FN Configuration



BIOS Setting	Description
ME State	When Disabled, ME will be put into ME Temporarily Disabled Mode.
Manageability Features State	Enable/Disable Intel(R) manageability features. Note: This option disables/enables manageability features support in FW. To disable support platform must be in an unprovisioned state first.
AMI BIOS Features	When disabled AMT BIOS Features are no longer supported and user is no longer able to access MEBx Setup. Note: This option does not disable Manageability Features in FW.

4.4.5 Trusted Computing



BIOS Setting	Description
Security Device Support	Enables / Disables BIOS support for security device. OS will not show security device. TCG EFI protocol and INTIA interface will not be available.
SHA256 PCR Bank	Enable or Disable SHA256 PCR Bank
Pending operation	Schedule an operation for the security device. NOTE: Your computer will reboot during restart in order to change state of security device.
Platform Hierarchy	Enable or Disable Platform Hierarchy
Storage Hierarchy	Enable or Disable Storage Hierarchy
Endorsement Hierarchy	Enable or Disable Endorsement Hierarchy
Physical Presence Spec Version	Select to tell OS to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.
Device Select	TPM 1.2 will restrict support to TPM 1.2 devices. TPM 2.0 will restrict to support TPM 2.0 devices. Auto will support both, with the default set to TPM 2.0 devices. If not found, TPM 1.2 devices will be enumerated.

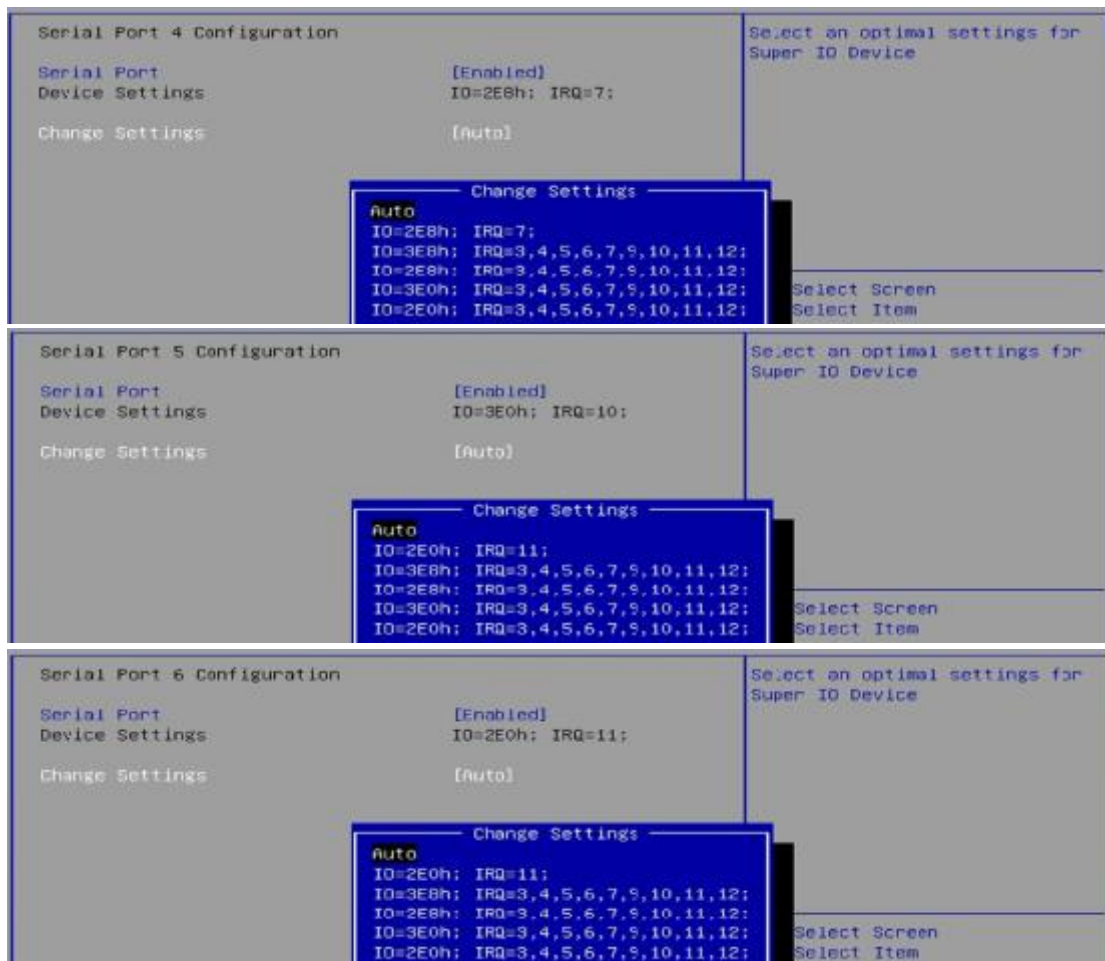
4.4.6 ACPI Settings



BIOS Setting	Description
Enable ACPI Auto Configuration	Enables / Disables BIOS SCPI Auto Configuration.
Enable Hibernation	Enables / Disables the system ability to hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems.
ACPI Sleep State	Selects the highest ACPI sleep state for the system will enter when the SUSPEND button is pressed. Options: Suspend Disabled S3 (Suspend to RAM)

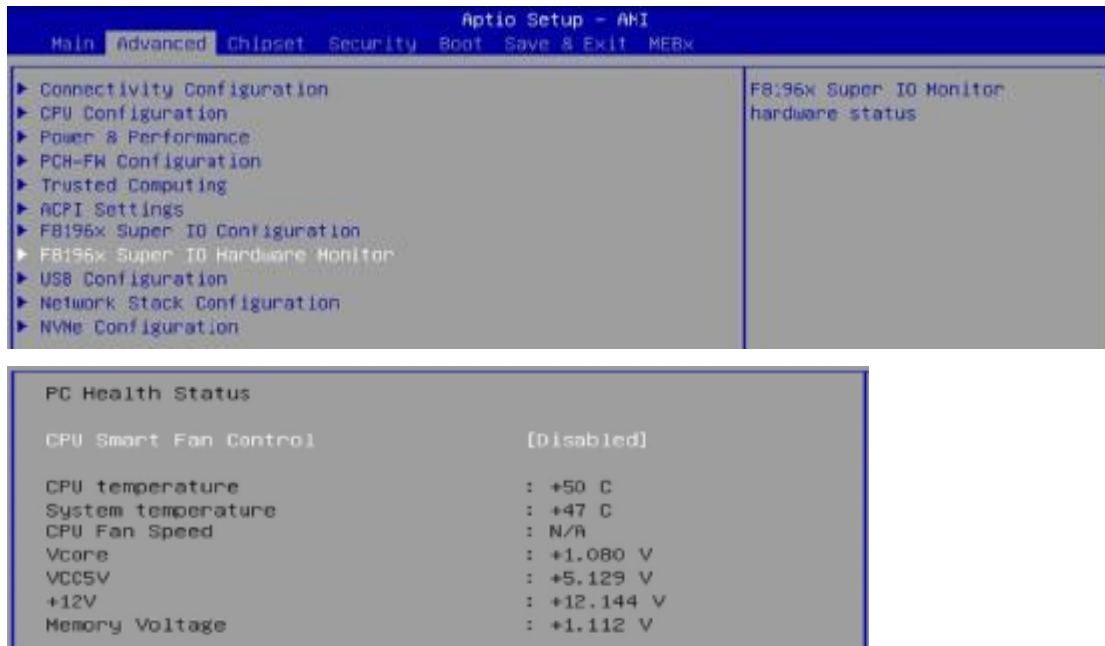
4.4.7 F8196x Super IO Configuration

Advanced Aptio Setup - AHI	
F8196x Super IO Configuration Super IO Chip: F8196x Power Failure: [Always off]	
▶ Serial Port 1 Configuration ▶ Serial Port 2 Configuration ▶ Serial Port 3 Configuration ▶ Serial Port 4 Configuration ▶ Serial Port 5 Configuration ▶ Serial Port 6 Configuration	
Serial Port 1 Configuration Serial Port: [Enabled] Device Settings: IO=3F8h; IRQ=4; Change Settings: [Auto] Device Mode: [RS232]	Change the Serial Port mode. Select Screen Select Item
Device Mode RS232 RS485 TX Low Active RS485 with Termination TX Low Active RS422 RS422 with Termination	
Serial Port 2 Configuration Serial Port: [Enabled] Device Settings: IO=2F8h; IRQ=3; Change Settings: [Auto] Device Mode: [RS232]	Select an optimal settings for Super IO Device Select Screen Select Item
Change Settings Auto IO=2F8h; IRQ=3; IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	
Serial Port 2 Configuration Serial Port: [Enabled] Device Settings: IO=2F8h; IRQ=3; Change Settings: [Auto] Device Mode: [RS232]	Change the Serial Port mode. Select Screen Select Item
Device Mode RS232 RS485 TX Low Active RS485 with Termination TX Low Active RS422 RS422 with Termination	
Serial Port 3 Configuration Serial Port: [Enabled] Device Settings: IO=3E8h; IRQ=5; Change Settings: [Auto]	Select an optimal settings for Super IO Device Select Screen Select Item
Change Settings Auto IO=3E8h; IRQ=5; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E0h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	



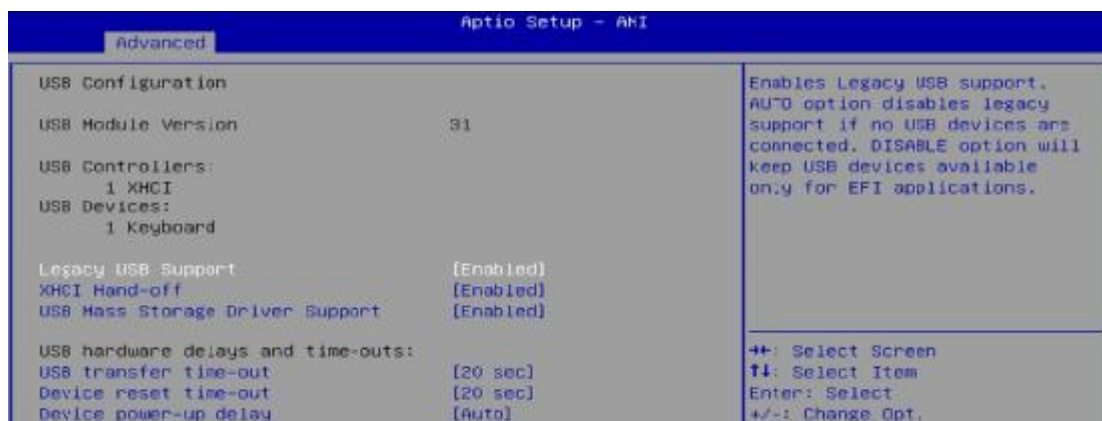
BIOS Setting	Description
Serial Port Configuration	Sets parameters of Serial Ports. Enables / Disables the serial port and select an optimal setting for the Super IO device.
Power Failure	Options: Always on, Always off

4.4.8 F8196x Hardware Monitor



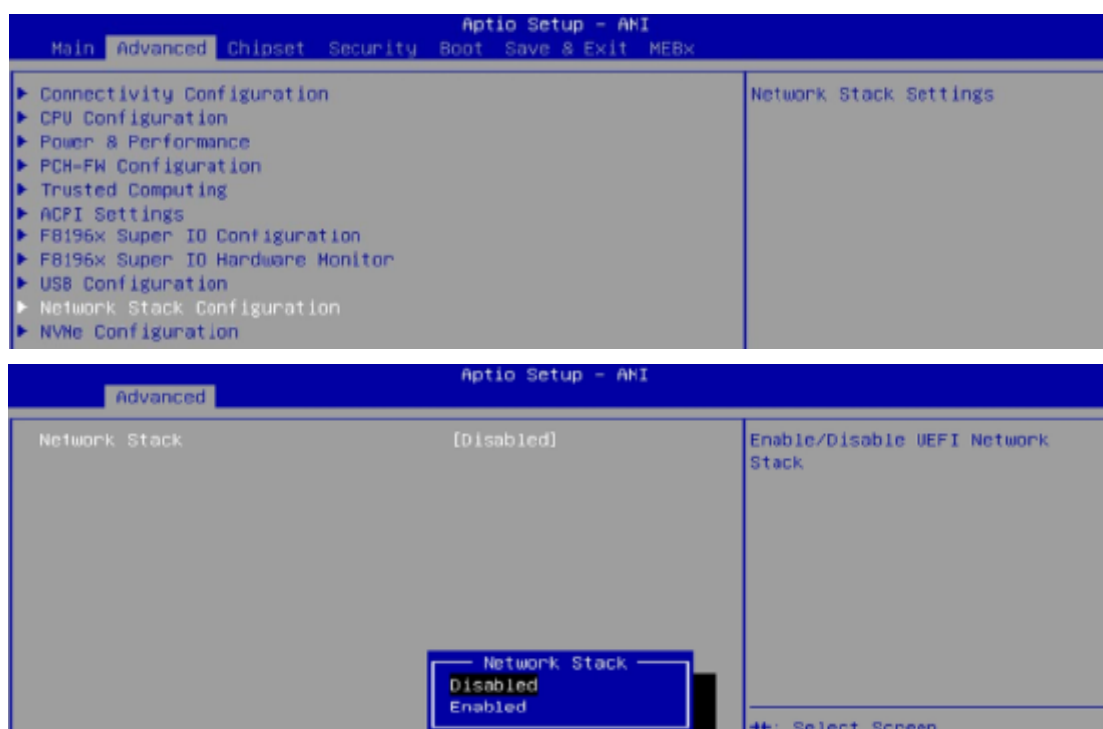
BIOS Setting	Description
CPU Smart Fan Control	Enables / Disables the CPU smart fan feature. Options: Disabled / 50°C / 60°C / 70°C / 80°C
Temperatures / Voltages	These fields are the parameters of the hardware monitoring function feature of the motherboard. The values are read-only values as monitored by the system and show the PC health status.

4.4.9 USB Configuration



BIOS Setting	Description
Legacy USB Support	Enables Legacy USB support. • Auto disables legacy support if there is no USB device connected. • Disable keeps USB devices available only for EFI applications.
XHCI Hand-off	This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	Enables / Disables the support for USB mass storage driver.
USB Transfer time-out	The time-out value for control, bulk, and Interrupt transfers. Options: 1 sec / 5 sec / 10 sec / 20 sec
Device reset time-out	Seconds of delaying execution of start unit command to USB mass storage device. Options: 10 sec / 20 sec / 30 sec / 40 sec
Device power-up delay	The maximum time the device will take before it properly reports itself to the Host Controller. Auto uses default value for a Root port it is 100ms. But for a Hub port, the delay is taken from Hub descriptor. Options: Auto / Manual

4.4.10 Network Stack Configuration

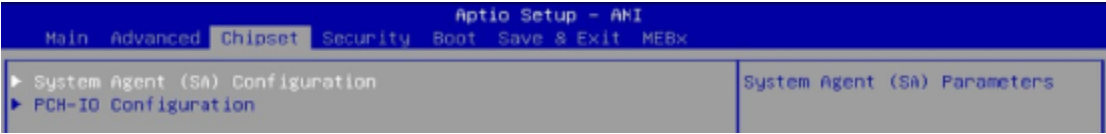


BIOS Setting	Description
Network Stack	Enable / Disable UEFI Network Stack. Options: Disabled / Enabled

4.4.11 NVME Configuration

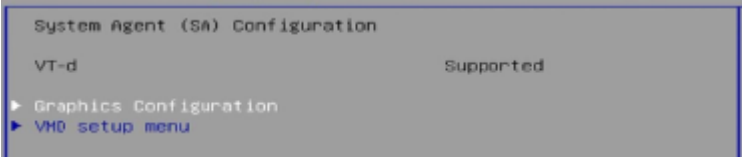


4.5 Chipset Settings



BIOS Setting	Description
System Agent (SA) Configuration	System Agent (SA) parameters
PCH-IO Configuration	PCH parameters

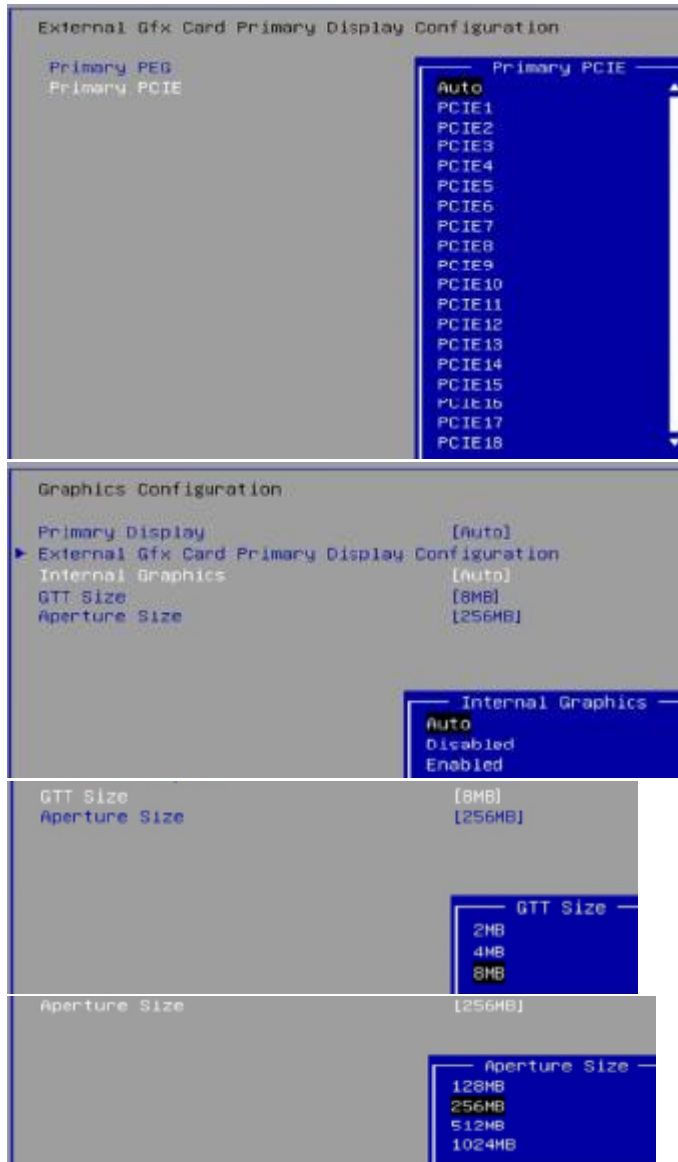
4.5.1 System Agent (SA) Configuration



BIOS Setting	Description
VT-d	Checks if VT-d function on MCH is supported.
Graphics Configuration	Configures the graphics settings.
VMD Configuration	Enable/Disable VMD controller.

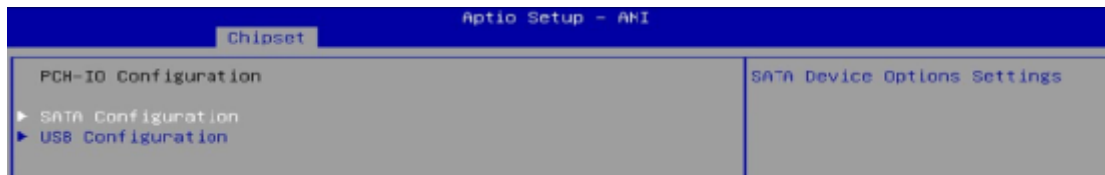
4.5.1.1 Graphics Configuration



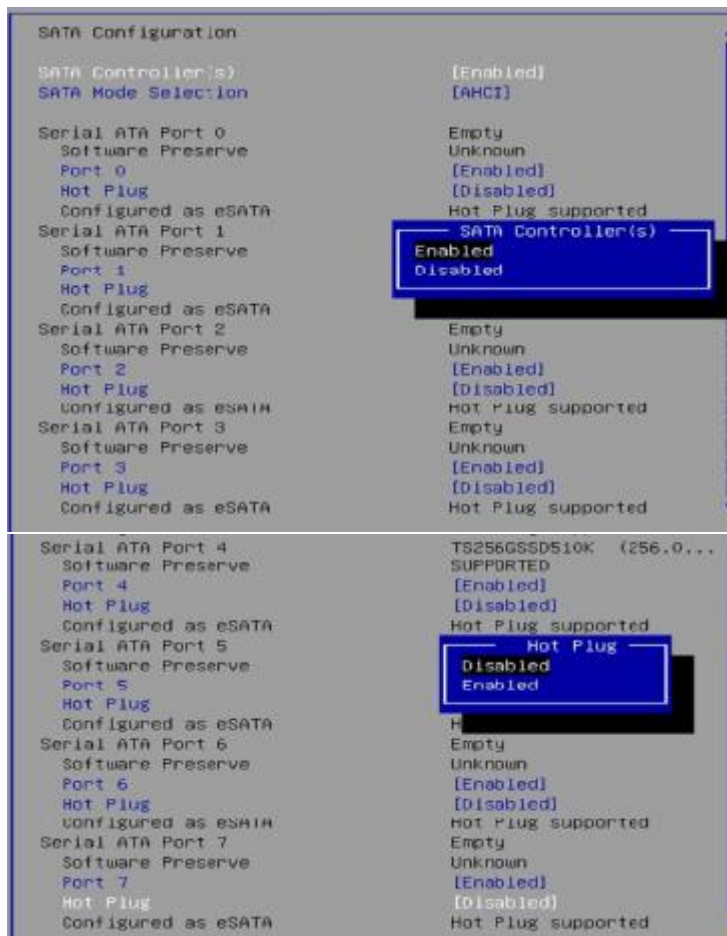


BIOS Setting	Description
Primary Display	Selects which of PEG0/PEG1/PEG2/PEG3 graphics device which should be primary PEG.
External Gfx card primary display configuration	Selects which of IGFX/PEG/PCI device to be primary display or select HG for Hybrid Gfx.
Internal Graphics	Keep IGFX enabled based on the setup options.
GTT Size	Sets the GTT size as 2 MB, 4 MB, or 8 MB.
Aperture Size	Select the aperture size. Note: Above 4 GB MMIO BIOS assignment is automatically enabled when selecting 2048 MB aperture. To use this feature, disable CSM support.

4.5.2 PCH-IO Configuration

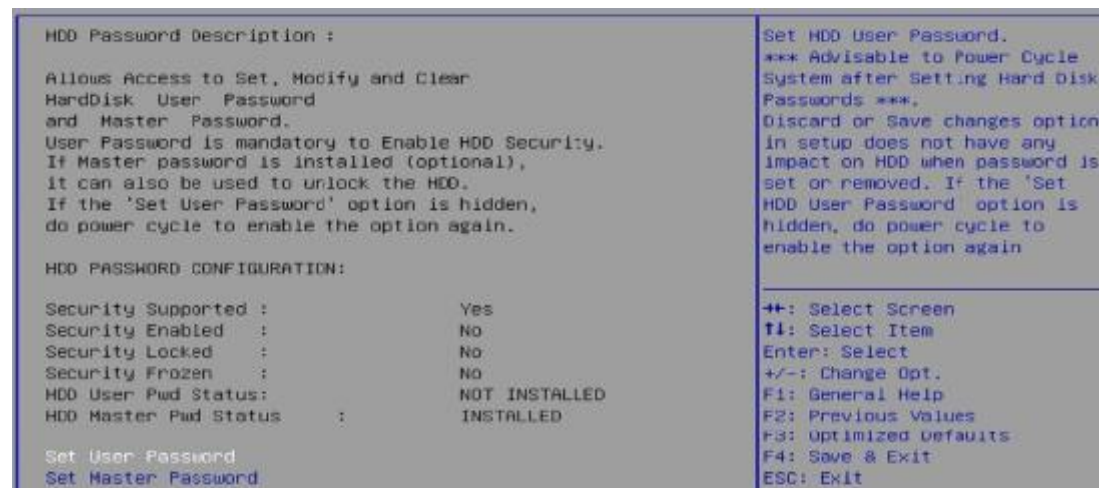
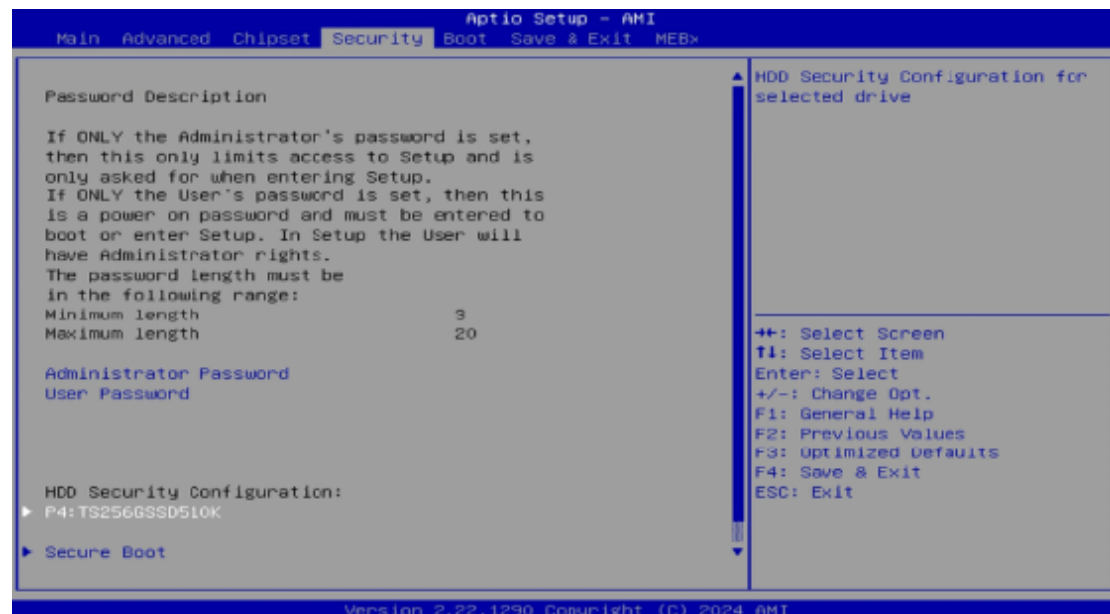


BIOS Setting	Description
SATA Configuration	Configures SATA devices.
USB Configuration	USB configuration settings.



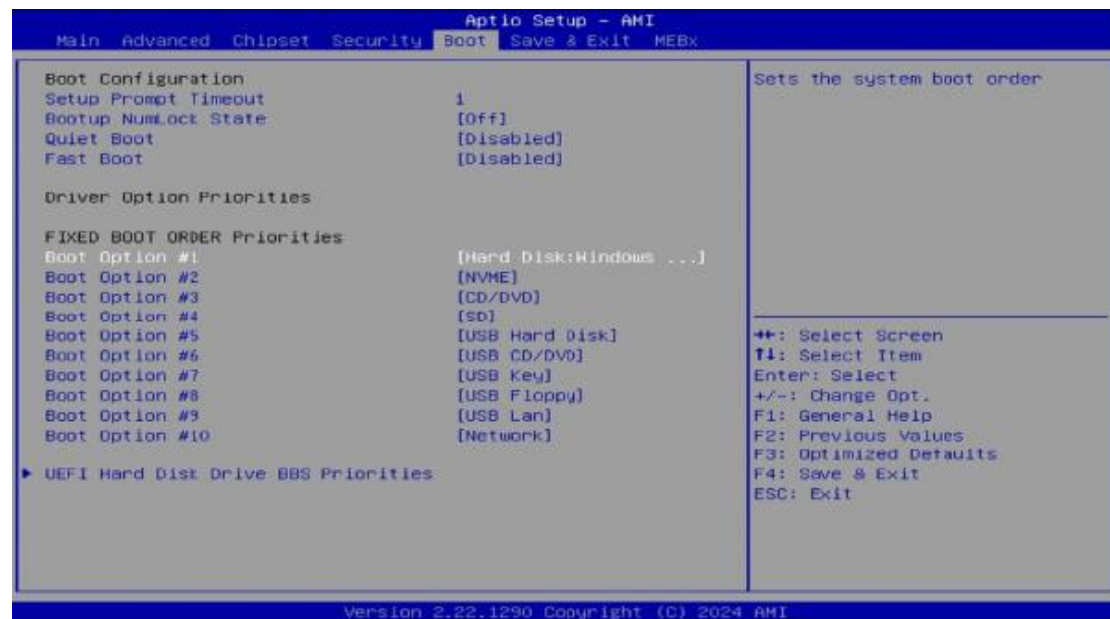
PCH-IO Configuration		
▶ SATA Configuration		
▶ USB Configuration		
USB Configuration		
M.2 B-Key USB2.0 Control	[Enabled]	Enable/Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS.

4.6 Security Settings



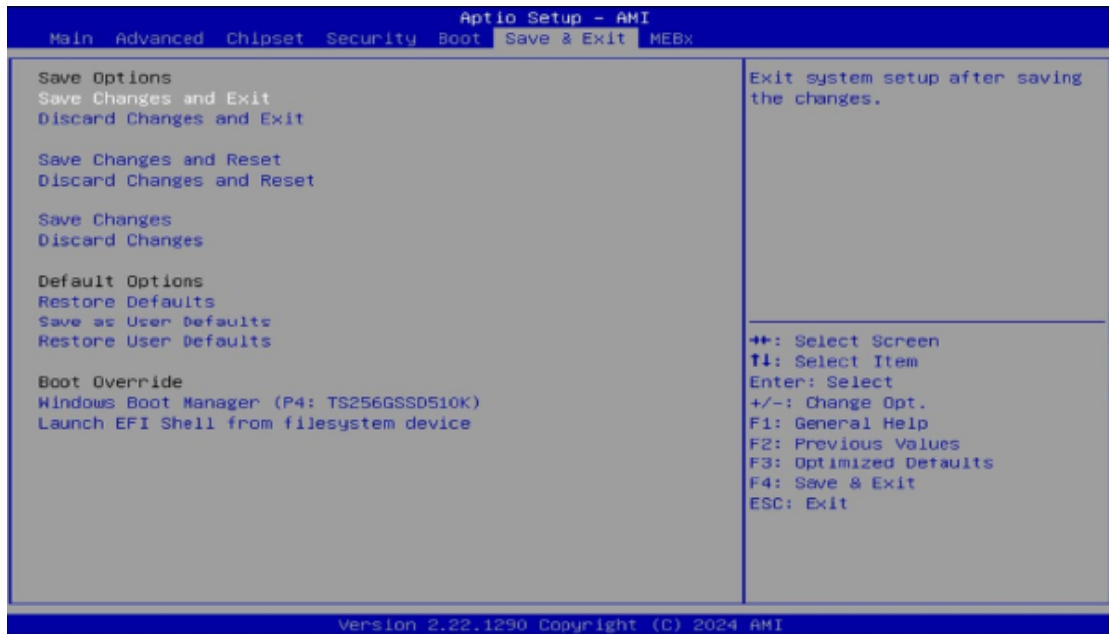
BIOS Setting	Description
Administrator Password	Sets an administrator password for the setup utility.
User Password	Sets a user password.
Secure Boot	Secure Boot feature is Active if Secure Boot is Enabled. Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset.
Secure Boot Mode	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.

4.7 Boot Settings



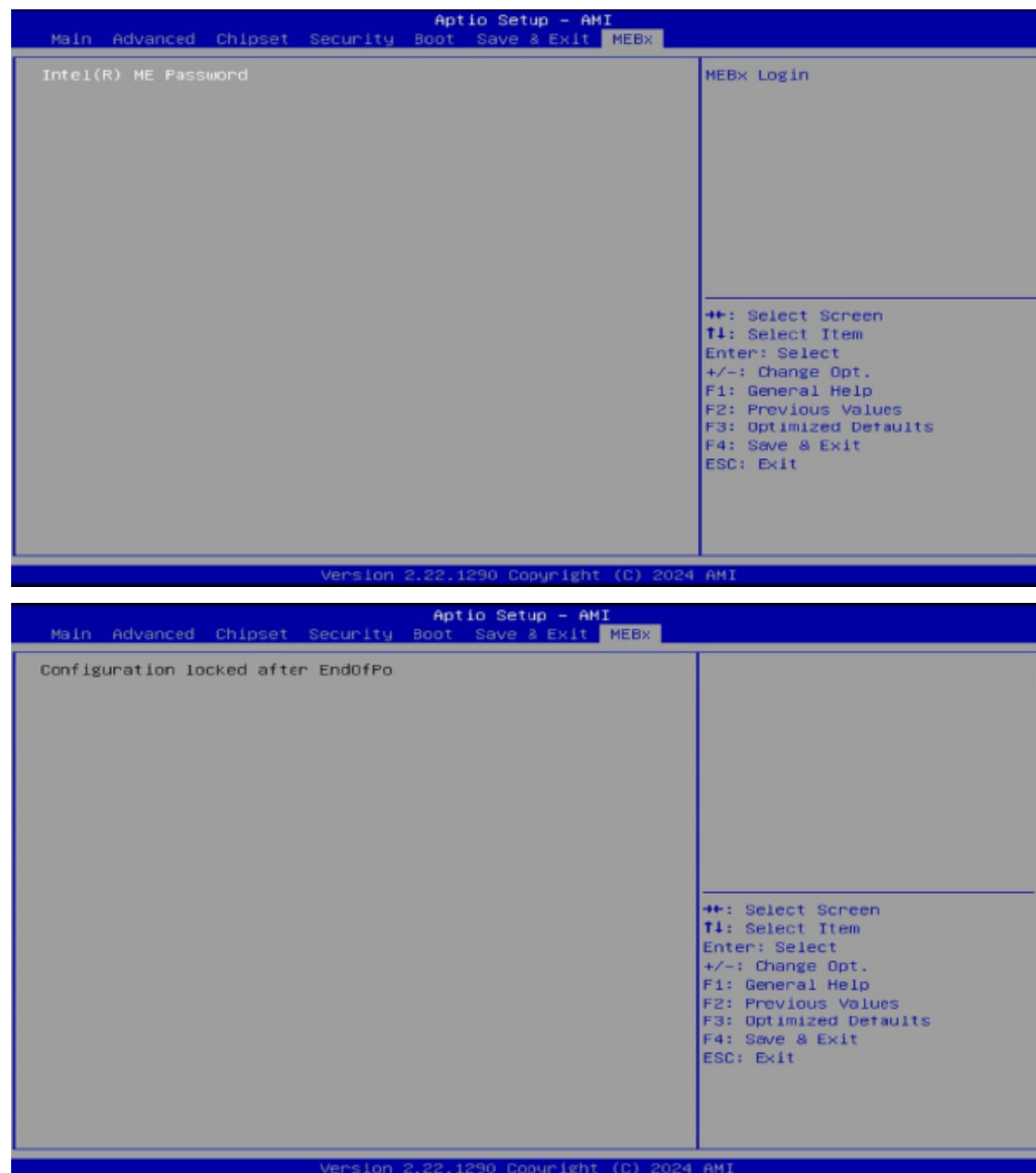
BIOS Setting	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting.
Bootup NumLock State	Selects the keyboard NumLock state.
Quiet Boot	Enables / Disables Quiet Boot option.
Fast Boot	Enables / Disables boot with initialization of a minimal set of devices required to launch the active boot option. Has no effect for BBS boot options.
Boot mode select	Selects a Boot mode, Legacy / UEFI.
Fixed Boot Order Priorities	Sets the system boot order.
UEFI Hard Disk Drive BBS Priorities	Specifies the Boot Device Priority sequence from available UEFI Hard Disk Drives

4.8 Save & Exit Settings



BIOS Setting	Description
Save Changes and Exit	Exits system setup after saving the changes.
Discard Changes and Exit	Exits system setup without saving any changes.
Save Changes and Reset	Resets the system after saving the changes.
Discard Changes and Reset	Resets system setup without saving any changes.
Save Changes	Saves changes done so far to any of the setup options.
Discard Changes	Discards changes done so far to any of the setup options.
Restore Defaults	Restores / Loads defaults values for all the setup options.
Save as User Defaults	Saves the changes done so far as user defaults.
Restore User Defaults	Restores the user defaults to all the setup options.

4.9 MEBx



Appendix

This section provides the mapping addresses of peripheral devices and the sample code of watchdog timer configuration.

- I/O Port Address Map
- Interrupt Request Lines (IRQ)
- Watchdog Timer Configuration

A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. The following table lists the I/O port addresses used.

Resource	Device
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x0000002E-0x0000002F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000B2-0x000000B3	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x0000164E-0x0000164F	Motherboard resources
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer
0x00004000-0x00004FFF	PCI Express Root Port
0x00004000-0x00004FFF	NVIDIA RTX A2000
0x000003F8-0x000003FF	Communications Port (COM1)
0x000002F8-0x000002FF	Communications Port (COM2)
0x000003E8-0x000003EF	Communications Port (COM3)
0x000002E8-0x000002EF	Communications Port (COM4)
0x000003E0-0x000003E7	Communications Port (COM5)
0x000002E0-0x000002E7	Communications Port (COM6)
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller

0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x00002000-0x000020FE	Motherboard resources
0x00000060-0x00000060	Standard PS/2 Keyboard
0x00000064-0x00000064	Standard PS/2 Keyboard
0x00001854-0x00001857	Motherboard resources

B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Resource	Device
IRQ 4294967281~86	Intel(R) I210 Gigabit Network Connection #3
IRQ 4294967287	Intel(R) USB 3.20 eXtensible Host Controller - 1.20 (Microsoft)
IRQ 32	Intel(R) Serial IO I2C Host Controller - 7AFD
IRQ 0	System timer
IRQ 43	Intel(R) Serial IO I2C Host Controller - 7ACF
IRQ 4294967270	Intel(R) Management Engine Interface #1
IRQ 4294967291	PCI Express Root Port
IRQ 4	Communications Port (COM1)
IRQ 3	Communications Port (COM2)
IRQ 5	Communications Port (COM3)
IRQ 7	Communications Port (COM4)
IRQ 10	Communications Port (COM5)
IRQ 11	Communications Port (COM6)
IRQ 4294967289	Standard SATA AHCI Controller
IRQ 19	Intel(R) Active Management Technology - SOL (COM7)
IRQ 4294967293	PCI Express Root Port
IRQ 31	Intel(R) Serial IO I2C Host Controller - 7AFC
IRQ 16	Intel(R) Serial IO UART Host Controller - 7AA8
IRQ 4294967271~75	Intel(R) Ethernet Controller I226-V #2
IRQ 29	Intel(R) Serial IO I2C Host Controller - 7ACE
IRQ 4294967290/92	PCI Express Root Port
IRQ 55~ IRQ 511	Microsoft ACPI-Compliant System
IRQ 17	High Definition Audio Controller
IRQ 17	High Definition Audio Controller

IRQ 4294967276~80	Intel(R) Ethernet Controller I226-LM #2
IRQ 37	Intel(R) Serial IO SPI Host Controller - 7AAB
IRQ 1	Standard PS/2 Keyboard
IRQ 12	Microsoft PS/2 Mouse
IRQ 14	Intel(R) Serial IO GPIO Host Controller - INTC1056
IRQ 4294967288	NVIDIA RTX A2000

C. Watchdog Timer Configuration

The Watchdog Timer (WDT) is used to generate a variety of output signals after a user programmable count. The WDT is suitable for the use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sorts of circumstances, the timer will count to zero and the selected outputs will be driven.

Under normal circumstance, you will need to restart the WDT at regular intervals before the timer counts to zero.

Sample Code

```
//-----
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A
// PARTICULAR PURPOSE.
//
//-----
#include <dos.h>
#include <conio.h>
#include <stdio.h>
#include <stdlib.h>
#include "F81966.H"
//-----
int main (int argc, char *argv[]);
void EnableWDT(int);
void DisableWDT(void);
//-----
int main (int argc, char *argv[])
{
    unsigned char bBuf;
    unsigned char bTime;
    char **endptr;

    char SIO;

    printf("Fintek 81966 watch dog program\n");
    SIO = Init_F81966();
    if (SIO == 0)
    {
        printf("Can not detect Fintek 81966, program abort.\n");
        return(1);
    }
    if (SIO == 0)

    if (argc != 2)
    {
        printf(" Parameter incorrect!!\n");
        return (1);
    }

    bTime = strtol (argv[1], endptr, 10);
```

```

    printf("System will reset after %d seconds\n", bTime);

    if (bTime)
    { EnableWDT(bTime); }
    else
    { DisableWDT(); }
    return 0;
}
//-----
void EnableWDT(int interval)
{
    unsigned char bBuf;

    bBuf = Get_F81966_Reg(0x2B);
    bBuf &= (~0x20);
    Set_F81966_Reg(0x2B, bBuf); //Enable WDTO

    Set_F81966_LD(0x07); //switch to logic device 7
    Set_F81966_Reg(0x30, 0x01); //enable timer

    bBuf = Get_F81966_Reg(0xF5);
    bBuf &= (~0x0F);
    bBuf |= 0x52;
    Set_F81966_Reg(0xF5, bBuf); //count mode is second

    Set_F81966_Reg(0xF6, interval); //set timer

    bBuf = Get_F81966_Reg(0xFA);
    bBuf |= 0x01;
    Set_F81966_Reg(0xFA, bBuf); //enable WDTO output

    bBuf = Get_F81966_Reg(0xF5);
    bBuf |= 0x20;
    Set_F81966_Reg(0xF5, bBuf); //start counting
}
//-----
void DisableWDT(void)
{
    unsigned char bBuf;

    Set_F81966_LD(0x07); //switch to logic device 7

    bBuf = Get_F81966_Reg(0xFA);
    bBuf &= ~0x01;
    Set_F81966_Reg(0xFA, bBuf); //disable WDTO output

    bBuf = Get_F81966_Reg(0xF5);
    bBuf &= ~0x20;
    bBuf |= 0x40;
    Set_F81966_Reg(0xF5, bBuf); //disable WDT
}
//-----

```



```

//-----
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A
// PARTICULAR PURPOSE.
//
//-----
#include "F81966.H"
#include <dos.h>
//-----
unsigned int F81966_BASE;
void Unlock_F81966 (void);
void Lock_F81966 (void);
//-----
unsigned int Init_F81966(void)
{
    unsigned int result;
    unsigned char ucDid;

    F81966_BASE = 0x4E;
    result = F81966_BASE;

    ucDid = Get_F81966_Reg(0x20);
    if (ucDid == 0x07)        //Fintek 81966
    { goto    Init_Finish; }

    F81966_BASE = 0x2E;
    result = F81966_BASE;

    ucDid = Get_F81966_Reg(0x20);
    if (ucDid == 0x07)        //Fintek 81966
    { goto    Init_Finish; }

    F81966_BASE = 0x00;
    result = F81966_BASE;

Init_Finish:
    return (result);
}
//-----
void Unlock_F81966 (void)
{
    outportb(F81966_INDEX_PORT, F81966_UNLOCK);
    outportb(F81966_INDEX_PORT, F81966_UNLOCK);
}
//-----
void Lock_F81966 (void)
{
    outportb(F81966_INDEX_PORT, F81966_LOCK);
}
//-----
void Set_F81966_LD( unsigned char LD)
{

```

```

        Unlock_F81966();
        outportb(F81966_INDEX_PORT, F81966_REG_LD);
        outportb(F81966_DATA_PORT, LD);
        Lock_F81966();
    }
    //-----
void Set_F81966_Reg( unsigned char REG, unsigned char DATA)
{
    Unlock_F81966();
    outportb(F81966_INDEX_PORT, REG);
    outportb(F81966_DATA_PORT, DATA);
    Lock_F81966();
}
//-----
unsigned char Get_F81966_Reg(unsigned char REG)
{
    unsigned char Result;
    Unlock_F81966();
    outportb(F81966_INDEX_PORT, REG);
    Result = inportb(F81966_DATA_PORT);
    Lock_F81966();
    return Result;
}
//-----

//-----
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A
// PARTICULAR PURPOSE.
//
//-----
#ifndef F81966_H
#define F81966_H 1
//-----
#define F81966_INDEX_PORT (F81966_BASE)
#define F81966_DATA_PORT (F81966_BASE+1)
//-----
#define F81966_REG_LD 0x07
//-----
#define F81966_UNLOCK 0x87
#define F81966_LOCK 0xAA
//-----
unsigned int Init_F81966(void);
void Set_F81966_LD( unsigned char);
void Set_F81966_Reg( unsigned char,
unsigned char); unsigned char
Get_F81966_Reg( unsigned char);
//-----
#endif // F81966_H

```