MPPC1201PC
Industrial Panel PC

User's Manual

Version 1.0a
(September 2024)

iBASE

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Compliance

CE

This is a class A product. In a domestic environment, this product may cause radio interference in which case users may be required to take adequate measures.

FC

This product has been tested and found to comply with the limits for a Class B device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with manufacturer's instructions, may cause harmful interference to radio communications.
Operation is subject to the following two conditions:

- This product may not cause harmful interference
- This product must accept any interference received including interference that may cause undesired operation.

However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception which can be determined by turning the equipment off and on, you may correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and the receiver.
- Connect the equipment to an outlet on a circuit different from that to which the receiver is connected.
- Consult the distributor or an experienced radio/TV technician for help.

WEEE

This product must not be disposed of as normal household waste, in accordance with the EU directive of for waste electrical and electronic equipment (WEEE - 2012/19/EU). Instead, it should be disposed of by returning it to a municipal recycling collection point. Check local regulations for disposal of electronic products.

Green IBASE

RoHS Compliant

This product complies with RoHS 2 restrictions, which prohibit the use of certain hazardous substances in electrical and electronic equipment. The following substances must not exceed the specified concentrations:

- Hexavalent chromium: 1,000 ppm
- Poly-brominated biphenyls (PBBs): 1,000 ppm
- Poly-brominated diphenyl ethers (PBDEs): 1,000 ppm
- Cadmium: 100 ppm
- Mercury: 1,000 ppm
- Lead: 1,000 ppm
- Bis(2-ethylhexyl) phthalate (DEHP): 1,000 ppm
- Butyl benzyl phthalate (BBP): 1,000 ppm
- Dibutyl phthalate (DBP): 1,000 ppm
- Diisobutyl phthalate (DIBP): 1,000 ppm

Important Safety Information

Carefully read the precautions before using the device.

Environmental conditions:

- Lay the device horizontally on a stable and solid surface during installation in case the device may fall, causing serious damage.
- Leave plenty of space around the device for ventilation.
- Keep the device away from humidity to avoid fog or condensation from accumulating on the inner surface of the panel.

Care for your IBASE products:

- Before cleaning the device, turn it off and unplug all cables such as power in case a small amount of electrical current may still flow.
- Use neutral cleaning agents or diluted alcohol to clean the device chassis with a cloth. Then wipe the chassis with a dry cloth.
- Vacuum the dust with a computer vacuum cleaner to prevent the air vent or slots from being clogged.

WARNING

Attention during use:

- Operate with fingers on the panel. Sharp-pointed articles are prohibited.
- Do not use this product near water.
- Do not spill water or any other liquids on your device.
- Do not place heavy objects on the top of the device.
- Operate this device from the type of power indicated on the marking label. If you are not sure of the type of power available, consult your distributor or local power company.
- Do not walk on the power cord or allow anything to rest on it.
- If you use an extension cord, make sure that the total ampere rating of the product plugged into the extension cord does not exceed its limits.

Avoid Disassembly

You are not suggested to disassemble, repair or make any modification to the device. Disassembly, modification, or any attempt at repair could generate hazards and cause damage to the device, even bodily injury or property damage, and will void any warranty.

Warranty Policy

- **IBASE standard products:**

  24-month (2-year) warranty from the date of shipment. If the date of shipment cannot be ascertained, the product serial numbers can be used to determine the approximate shipping date.

- **3rd-party parts:**

  12-month (1-year) warranty from delivery for the 3rd-party parts that are not manufactured by IBASE, such as CPU, CPU cooler, memory, storage devices, power adapter, panel and touchscreen.

* Products, however, that fail due to misuse, accident, improper installation or unauthorized repair shall be treated as out of warranty and customers shall be billed for repair and shipping charges.

Technical Support & Services

1. Visit the IBASE website at www.ibase.com.tw to find the latest information about the product.
2. If you need any further assistance from your distributor or sales representative, prepare the following information of your product and elaborate upon the problem.

   - Product model name
   - Product serial number
   - Detailed description of the problem
   - The error messages in text or in screenshots if there is any
   - The arrangement of the peripherals
   - Software in use (such as OS and application software, including the version numbers)

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Table of Contents

Chapter 1
General Information

The information provided in this chapter includes:

- Features
- Packing List
- Specifications
- Product View
- Dimensions

1.1 Introduction

The MPPC1201PC panel PC is a rugged and versatile computing solution with EN50155 (2017) & EN45545-2 certification for transportation and railway applications. It is powered by an Intel® Atom® x6425E processor and provides a fully IP65 waterproof protection rating to withstand exposure to harsh weather conditions and liquids. The ultra-long-life LCD panel and M12 connectors ensure durability and reliability, while the flat bezel design and 12.1" projected capacitive touch screen provide a user-friendly interface. In addition, it offers optional auto-dimming adjustment and supports power input from both 24V DC and 72/110V DC sources.

1.2 Features

- Intel® Atom® x6425E processor
- EN50155 (2017) & EN45545-2 certified
- Fully IP65 waterproof protection
- Supports ultra-long-life LCD panel, M12 connectors
- Flat bezel design, projected capacitive 12.1" touch screen
- Optional Auto dimming adjustable
- Supports 24V DC or 72/110V DC power input

1.3 Packing List

Your MPPC product's package should include the items listed below. If any of the items below is missing, contact the distributor or the dealer from whom you purchased the product.

- MPPC1201PC x 1
- M12 to RJ45 LAN cable 300cm x 1
- M12 to USB cable 250cm x 1
- M12 to wires power cable 200cm x 1
- Screw, nut for mounting

1.4 Specifications

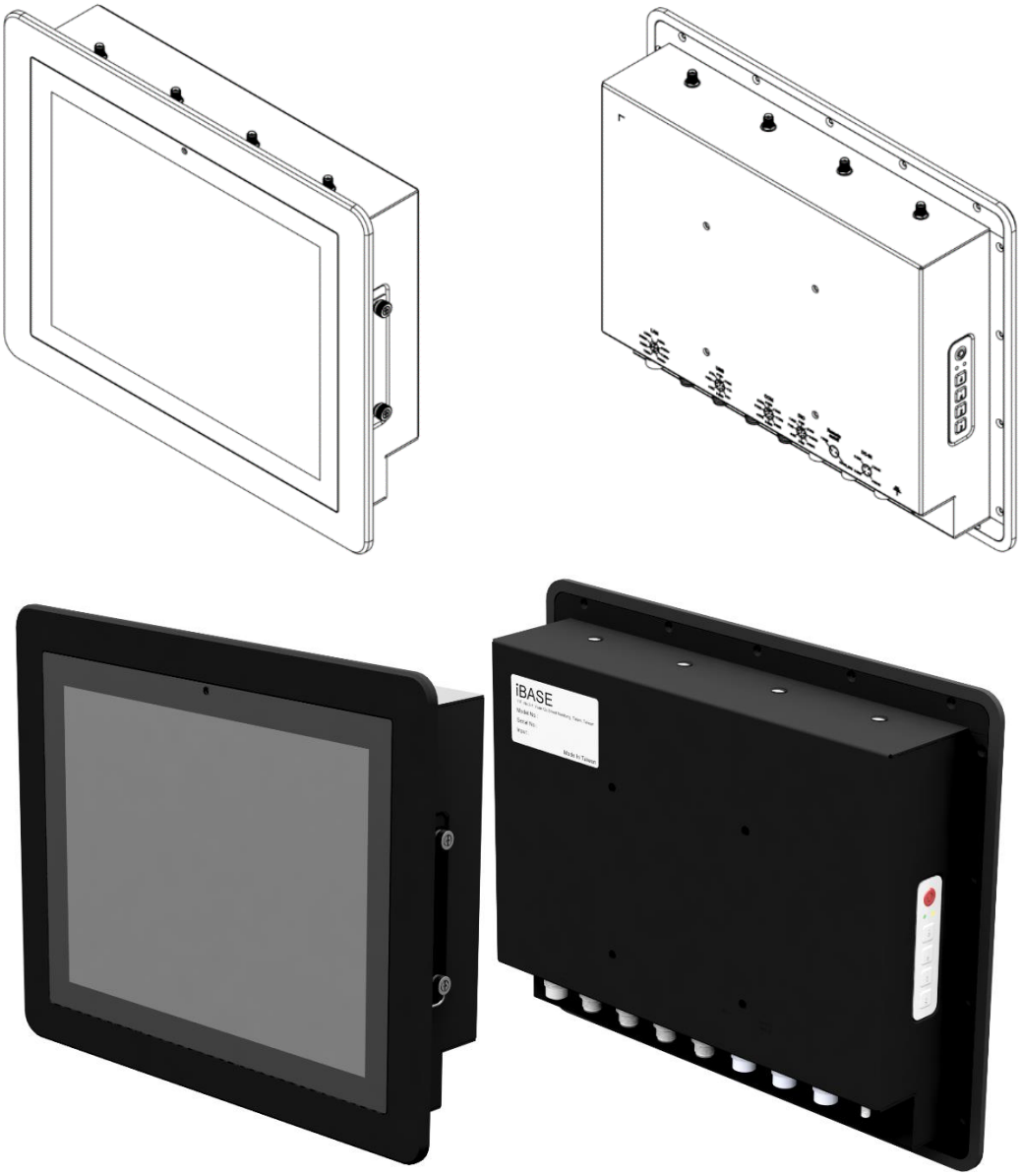
Models	
MPPC1201PC	12.1” EN50155 Certified Panel PC, Intel® Atom™ Processor x6425E, 4GB DDR4 memory, industrial-grade 64GB SSD, M12 connectors, DC 24V power input, no adaptor
MPPC1201PCH	12.1” EN50155 Certified Panel PC, Intel® Atom™ Processor x6425E, 4GB DDR4 memory, industrial-grade 64GB SSD, M12 connectors, DC 72/110V power input, no adaptor
Specifications	
Motherboard	IB836
CPU	Intel® Atom® QC x6425E Processor 2.0GHz, up to 3.0GHz
Memory	2x DDR4-3200 4GB, Max 32GB
Display Size	12.1” TFT-LCD
Max. Resolution	1024 x 768
Luminance (cd/m2)	500
Contrast	1000:1
Max. Color	16.2M
View Angle (H°/V°)	176/176
Backlight Lifetime(hrs	30,000
Touch Type	Projected capacitive
Touch Interface	USB
Light Transmission (%)	86
Point of touch	10
I/O Interface	• 2x M12 X-code 8P GbE • 2x M12 D-code 4P USB 2.0 • 1x M12 A-code 8P RS232/422/485 • 1x M12 A-code 8P DIO (4in/2out) • 1x M12 A-code 4P DC-input • 1x M12 A-code 2P remote control • 1x M4 size earth grounding

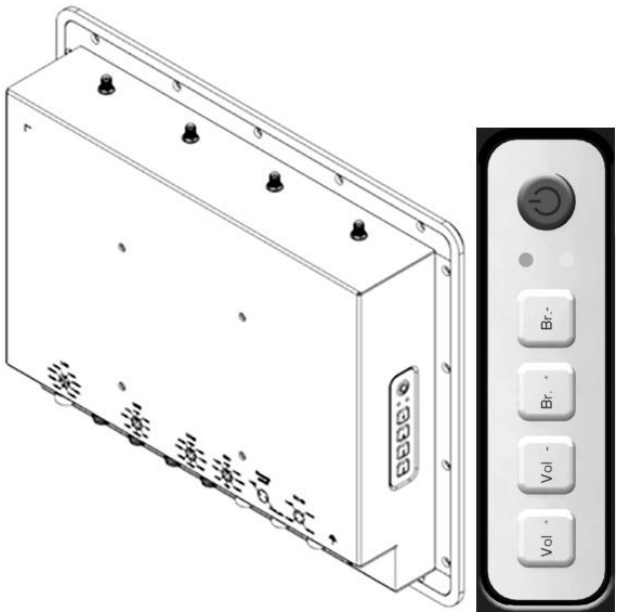
Power Button	Membrane key
Power Input Range	DC24V or DC 72V / 110V
Membrane Control	Brightness up/down, volume up/down, power on/off
Audio	2x speakers (optional)
Internal Expansion	• 1x Full sized Mini-PCI-E slot (USB2.0 + SATA +PCI-E(x1)) • 1x M.2 E-Key 2230 socket (USB 2.0)
Wireless	Optional 4G and WiFi modules
Storage	1x 2.5” device bay for SSD
Thermal Design	Fanless
Construction	Aluminum
Chassis Color (Front/Back)	Black/Black
IP Rating	IP65
Mounting	VESA 100x100 Panel mount (IP65 front panel only)
Dimensions	335mm (W) x274mm (D) x69mm (H) 13.19”(W) x 10.79”(D) x 2.7”(H)
Net Weight	3.1 kg
Operating Temperature	-40°C ~70°C, with industrial-grade SSD, not included add-on cards
Storage Temperature	-40°C~85°C (-40°F~185°F)
Storage Humidity	5%~90% @40°C (non-condensing)
Certification	• EN50155:2017 • EN45545-2 • CE/FCC class-A
Operating System	• Windows 10, 64bits • Linux Kernel 4+

All specifications are subject to change without prior notice.

1.5 Product View and Dimensions

Oblique View





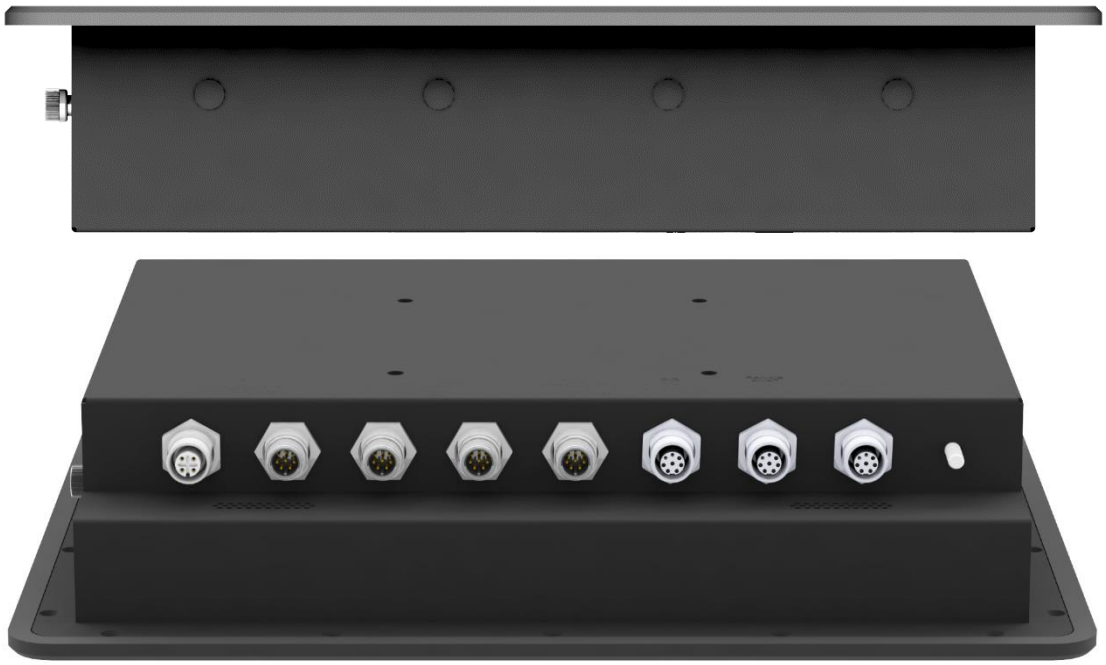
From top to bottom, the buttons/LEDs are as follows:

- Power Button
- LED indicators for HDD and Power
- Brightness Control
- Volume Control

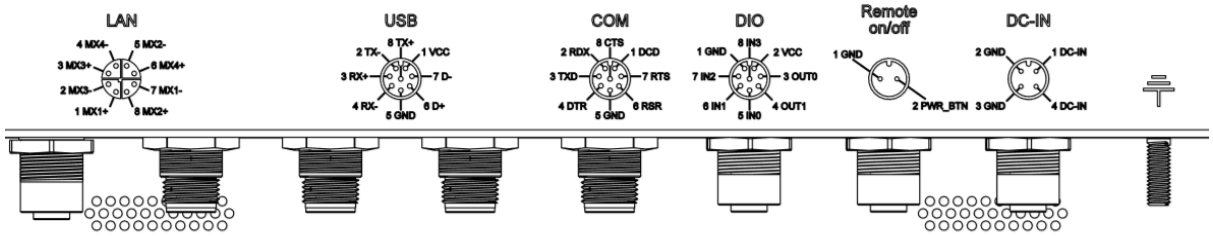
Power Button: The push button is used to power on or power off the system. The push button can be configured in the Windows power setting.
Volume Control: The two buttons (up and downs) are used to adjust the volume in 10 steps in terms of the gain of amplier (dB)
Brightness Control: The two buttons (up and down) are used to adjust the panel brightness in 10 steps in terms of backlight output %.

Side View

Top and Bottom View

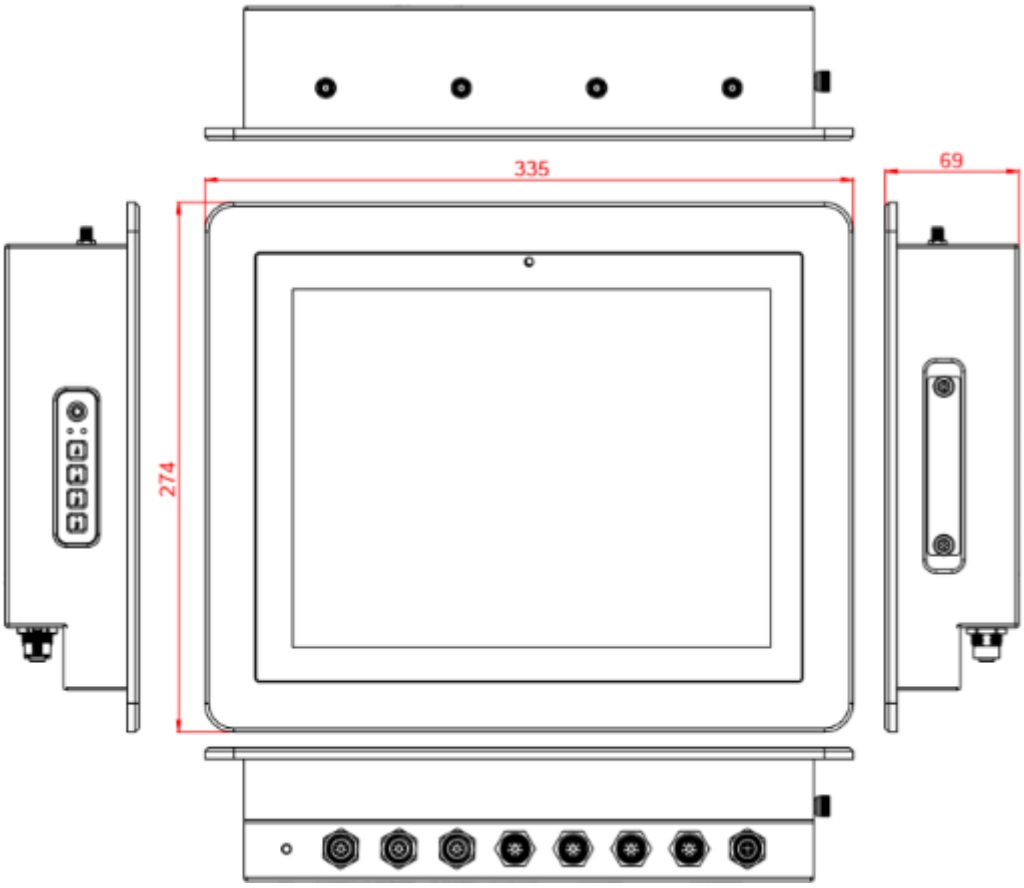


I/O View



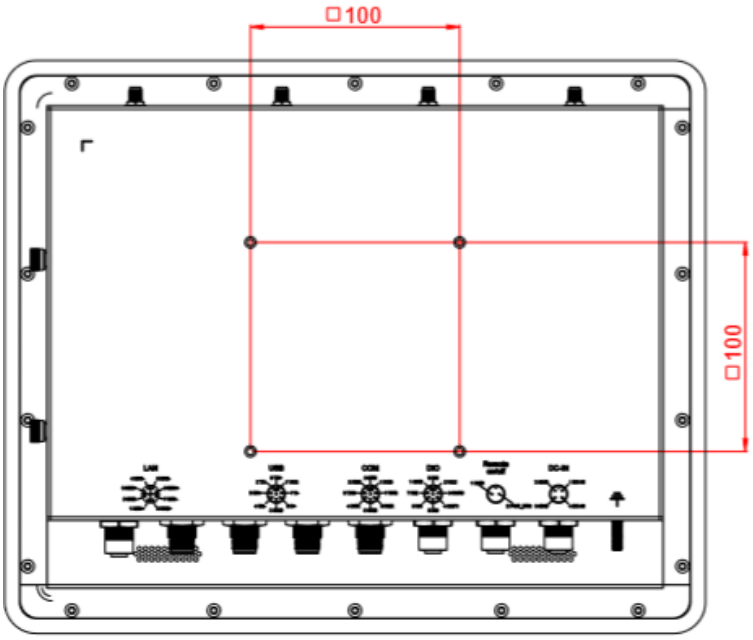
System Dimensions

Unit: mm



Mounting Holes

Unit: mm



Chapter 2
Hardware Installation & Motherboard Information

The information provided in this chapter includes:

- HDD Installtion
- Memory installation
- VESA mount installation
- Information and locations of jumpers/connectors

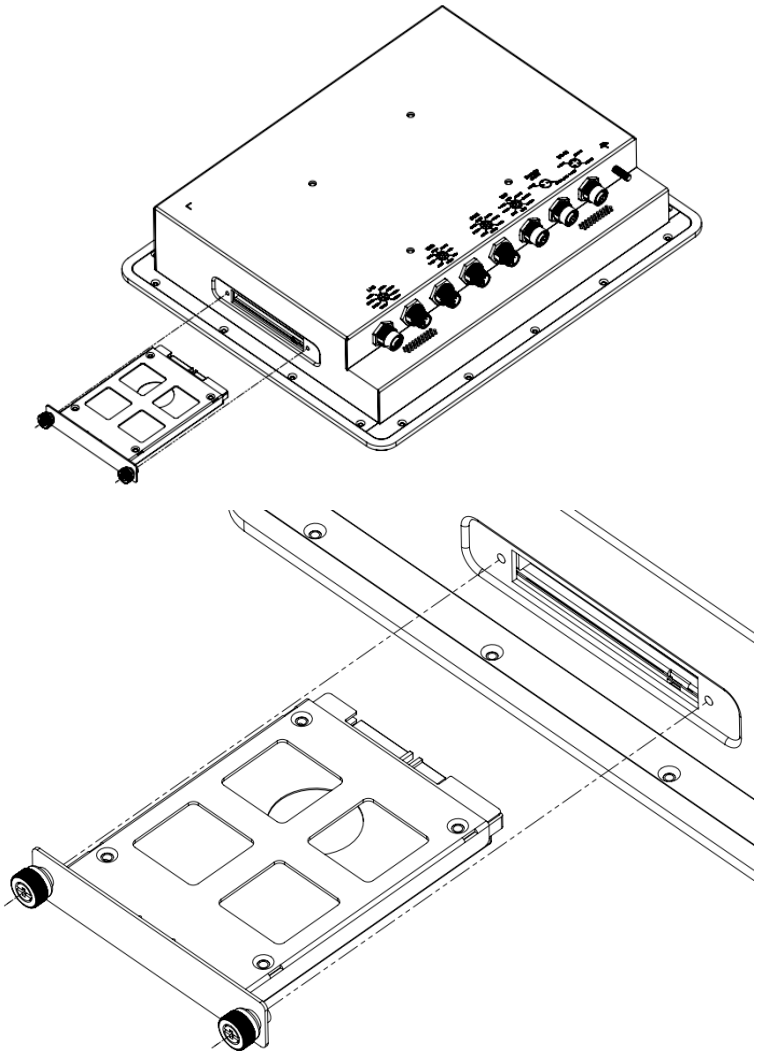
2.1 Hardware Installation

Avoid Disassembly: You are not suggested to disassemble, repair or make any modification to the device. Disassembly, modification, or any attempt at repair could generate hazards and cause damage to the device and will void any warranty. If you need to make any change to the device, be sure to have qualified engineers or technicians for disassembly or installation.

2.1.1 HDD Installation

You can install or replace the HDD by following the steps below.

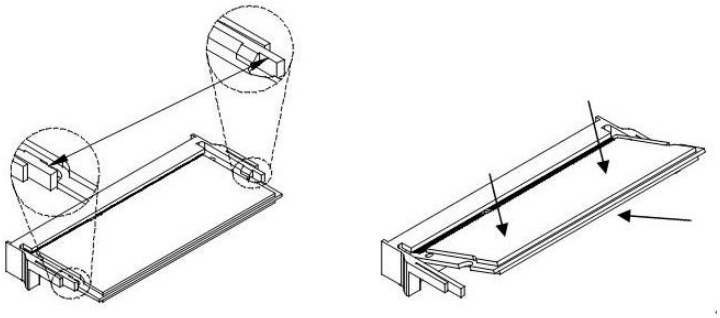
1. For the MPPC1201PC panel PC, release the screws that is holding the HDD tray and pull the tray out as shown below.



2.1.2 Memory Installation

Notice: We do not suggest that the end-user intend to install/change the memory, which may cause the IP65 sealing fault.

The MPPC system supports one SO- DIMM DDR3L memory slot for a maximum capacity of 8GB. To install the modules, locate the memory slot on the board and perform the following steps:

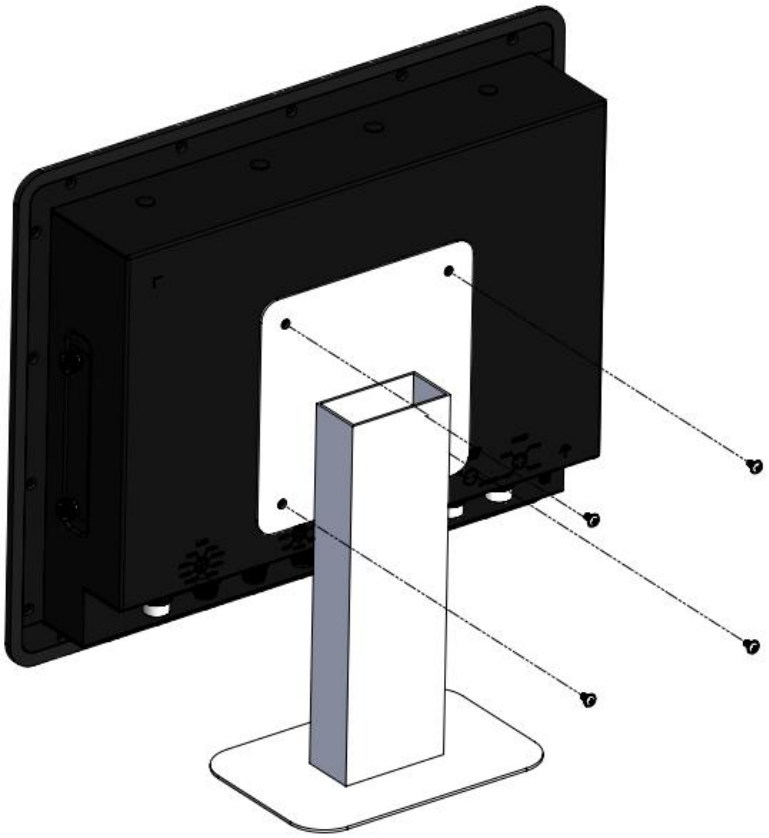


1. Align the key of the memory module with that on the memory slot and insertl the module slantwise.
2. Gently push the module in an upright position until the clips of the slot close to hold the module in place when the module touches the bottom of the slot.To remove the module, press the clips outwards with both hands.

2.1.3 VESA Mount Installation

1. VESA Mount

You will need to prepare the VESA mount bracket in advance.
Use 4 screws to attach the panel PC to the bracket.

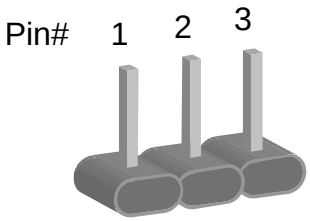


2.2 Setting the Jumpers

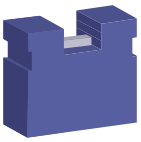
Notice: End-users should avoid changing the jumper settings, as doing so may lead to system errors..

Set up and configure your MPPC system by using jumpers for various settings and features according to your needs and applications. Contact your supplier if you have doubts about the best configuration for your use.

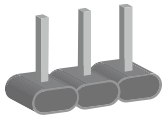
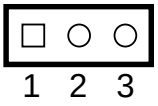
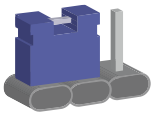
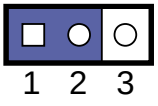
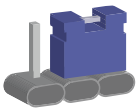
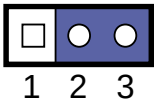
Jumpers are short-length conductors consisting of several metal pins mounted on a circuit board with a non-conductive base. Jumper caps are used to enable or disable functions and features. If a jumper has 3 pins, you can connect either PIN1 to PIN2 or PIN2 to PIN3 by shorting.



A 3-pin jumper



A jumper cap

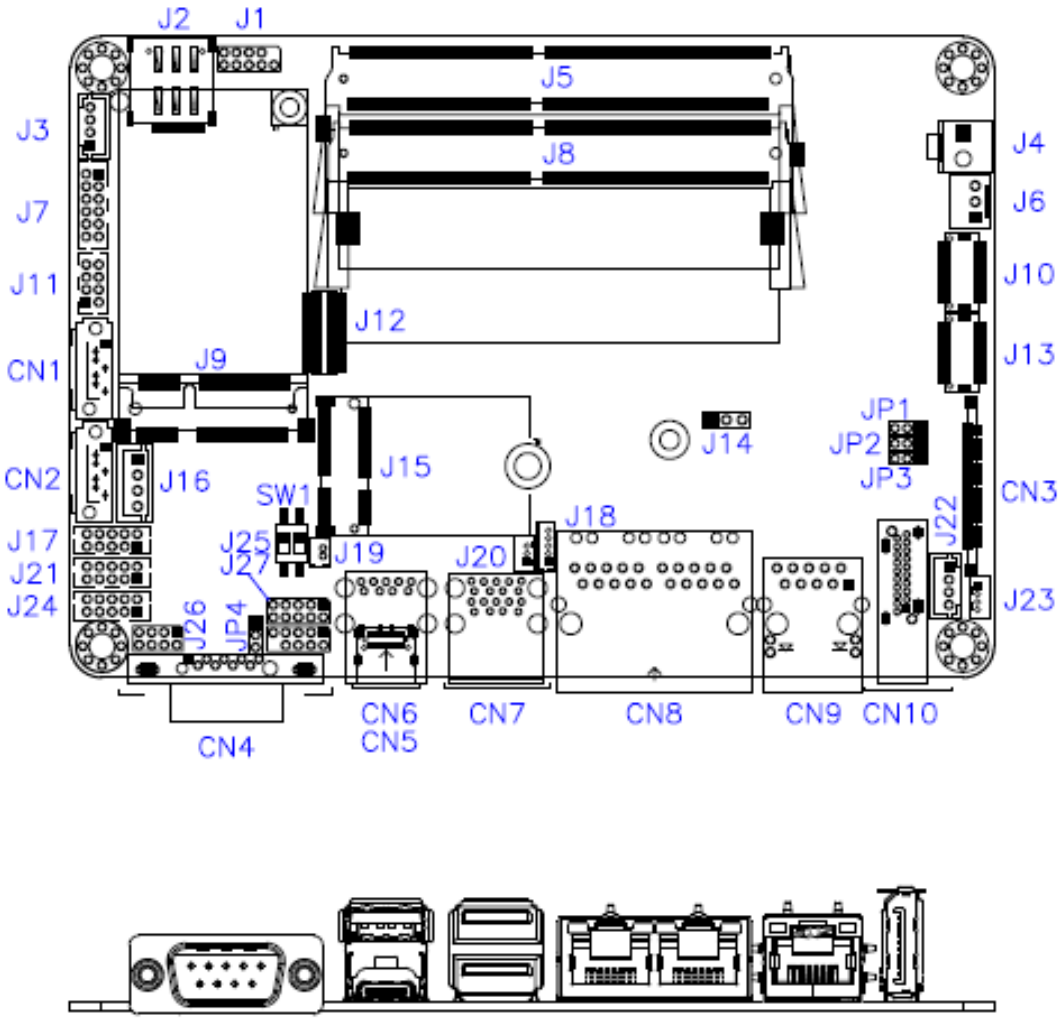
Pin closed	Oblique view	Illustration
Open		
1-2		
2-3		

Pin closed (On): When two pins of a jumper are encased in a jumper cap, this jumper is closed.

Open (Off): When a jumper cap is removed from two jumper pins, this jumper is open.

2.3 Jumper & Connector Locations

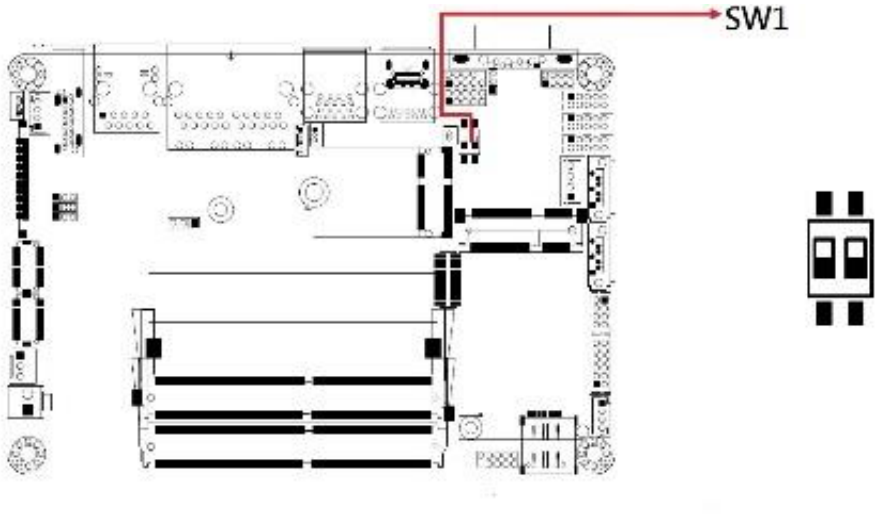
Motherboard: IB836



2.4 Jumpers Quick Reference

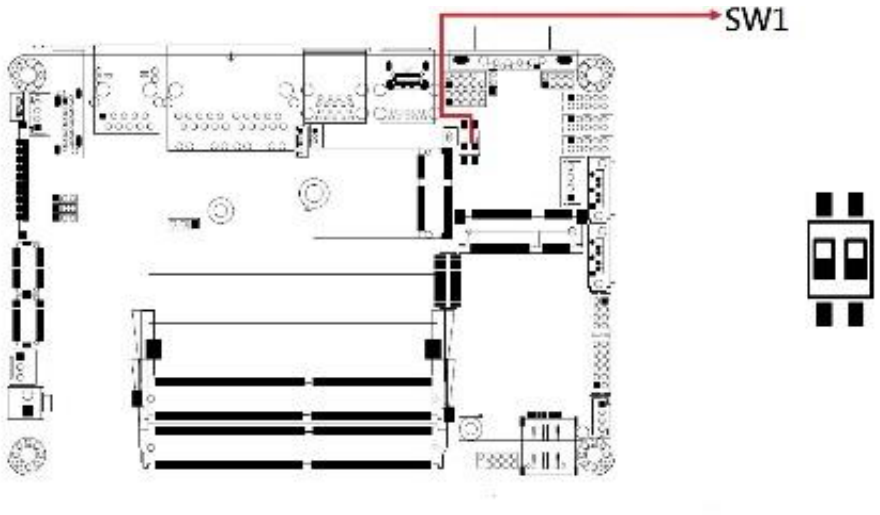
Jumper / Switch	Function
SW1	Clear CMOS Data
SW1	Clear ME Register
JP2	EDP Panel Power Select
JP1	LVDS Panel Power Select
JP3	LVDS Panel Brightness Select
JP4	ATX / AT Power Select

2.4.1 Clear CMOS Data (SW1)



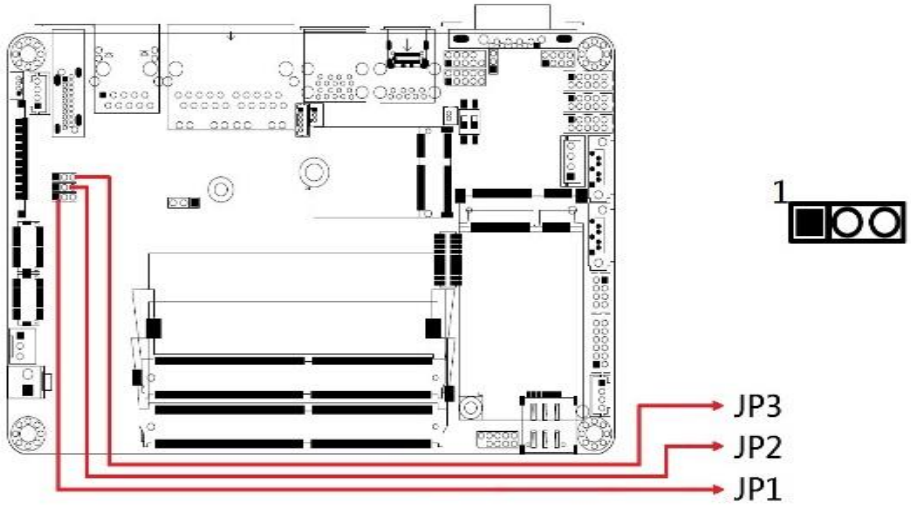
Function	Setting
Normal (default)	P1-OFF
Clear CMOS	P1-ON

2.4.2 Clear ME Register (SW1)



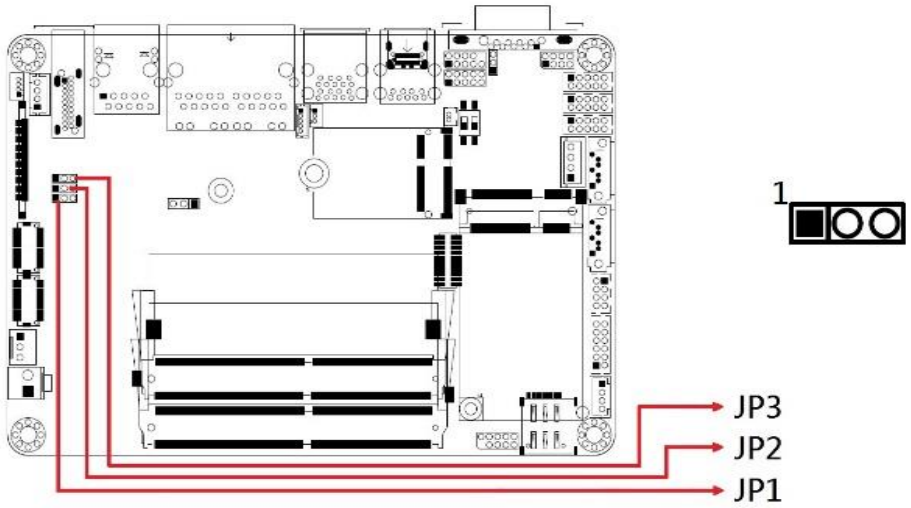
Function	Setting
Normal (default)	P2-OFF
Clear ME	P2-ON

2.4.3 EDP Panel Power Selections (JP2)



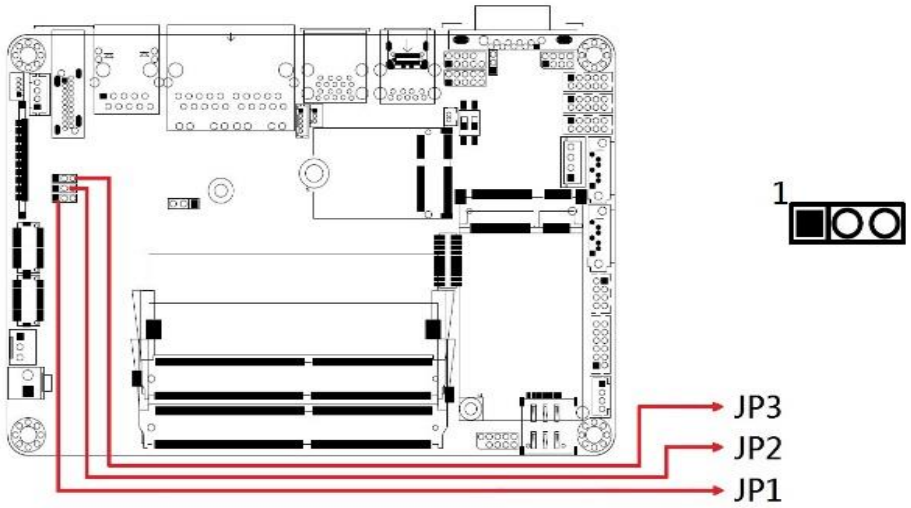
Function	Pin closed	Illustration
3.3V (default)	1-2	1
5V	2-3	1

2.4.4 LVDS Panel Power Selection (JP1)



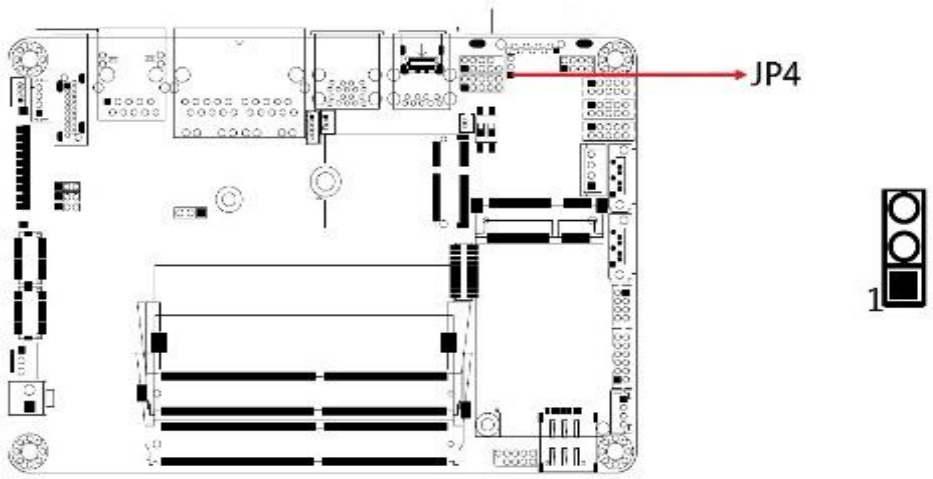
Function	Pin closed	Illustration
3.3V (default)	1-2	1
5V	2-3	1

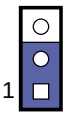
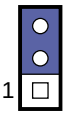
2.4.5 LVDS Panel Brightness Selection (JP3)



Function	Pin closed	Illustration
3.3V (default)	1-2	1
5V	2-3	1

2.4.6 ATX / AT Power Selection (JP4)

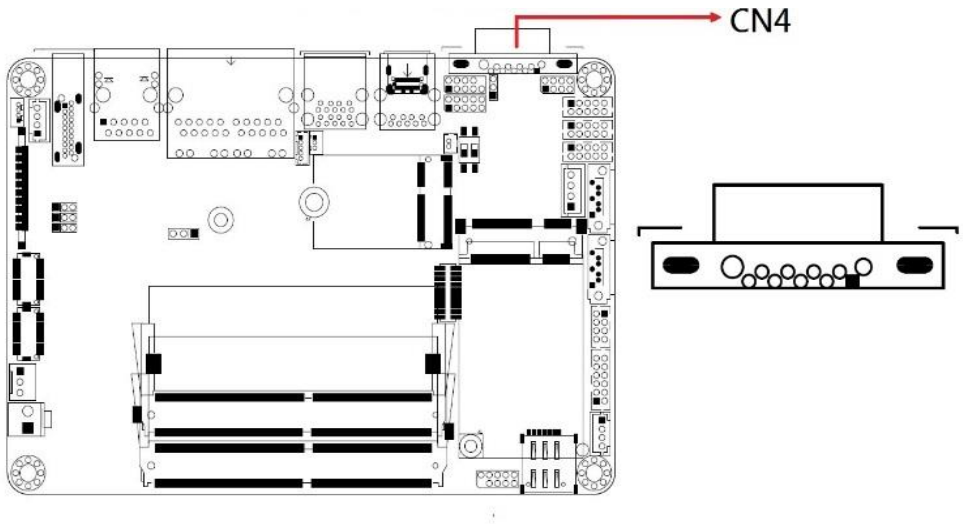


Function	Pin closed	Illustration
ATX (default)	1-2	
AT	2-3	

2.5 Connectors Quick Reference

Connector	Function
J2	SIM Card Slot
J3	Amplifier Connector
J4	DC Power Input Connector
J5, J8	DDR4 Slots
J16	SATA HDD Power Connector
J6	Fan Power Connector
J7	Audio Connector
J9	Mini PCIE Connector
J10, J13	LVDS Connector
J11	USB 2.0 Connector
J19	Battery Connector
J22	LCD Backlight Connector
J23	SMBUS Connector
J24, J21, J17	COM2, COM3, COM4 RS-232 Ports
J25	Digital I/O Connector
J26	Front Panel Connector
CN1,CN2	SATA Connectors
CN3	EDP Connector
CN4	COM1 RS-232/422/485 Port
CN5	Type-C Connector
CN6, CN7	USB3.0 Connector
CN8	LAN Connector
CN9	LAN Connector
CN10	DP Connector

2.5.1 COM1 RS-232/422/485 Port (CN4)

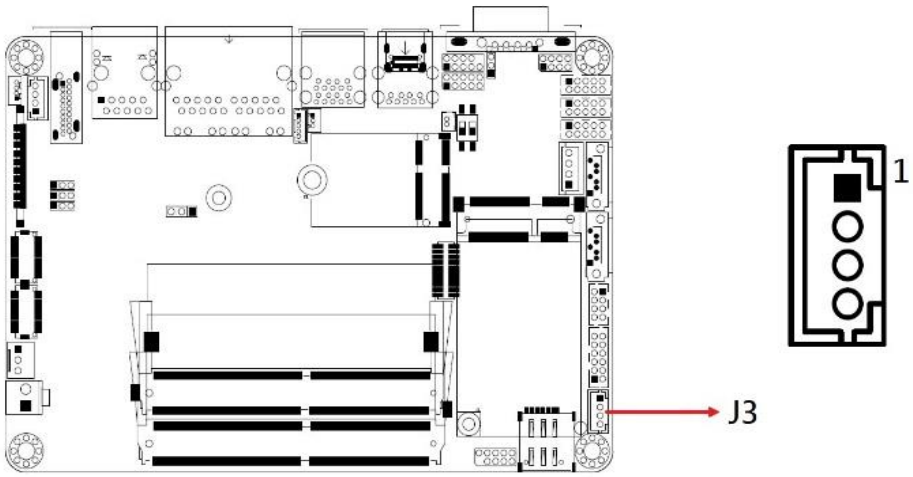


COM1 port is jumper-less and configurable in BIOS.

Pin	Assignment	Pin	Assignment
1	DSR, Data set ready	6	DCD, Data carrier detect
2	Ground	7	DTR, Data terminal ready
3	Ground	8	CTS, Clear to send
4	TX, Transmit	9	RTS, Request to send
5	RX, Receive	10	RI, Ring Indicator

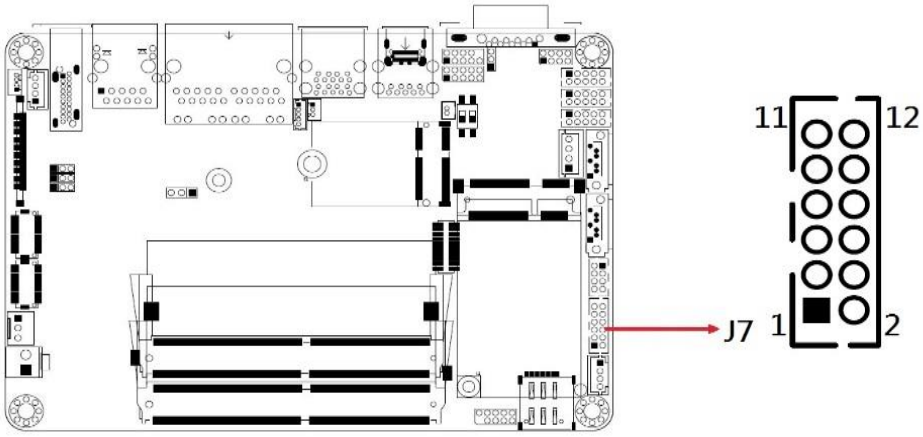
Pin	Assignment		
	RS-232	RS-422	RS-485
1	DSR	NC	NC
2	Ground	Ground	Ground
3	Ground	Ground	Ground
4	TX	RX+	NC
5	RX	TX+	Data+
6	DCD	TX-	Data-
7	DTR	RX-	NC
8	CTS	NC	NC
9	RTS	NC	NC
10	RI	NC	NC

2.5.2 Amplifier Connector (J3)



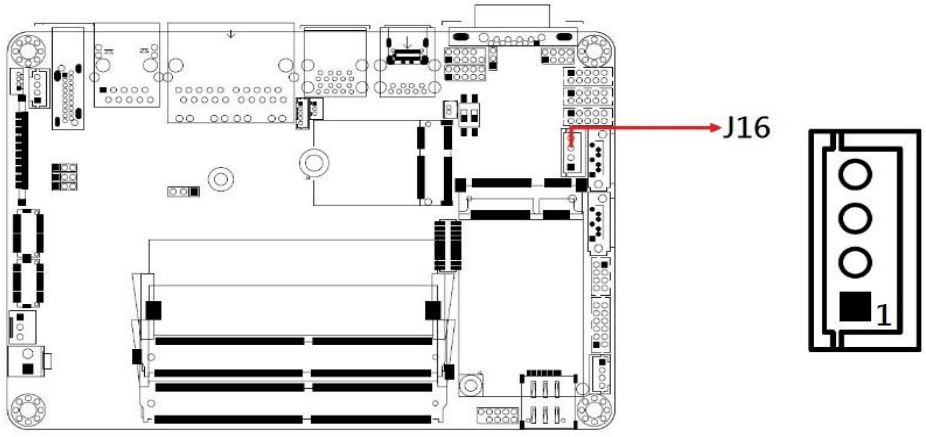
Pin	Assignment	Pin	Assignment
1	SPK_L+	3	SPK_R-
2	SPK_L-	4	SPK_R+

2.5.3 Audio Connector (J7)



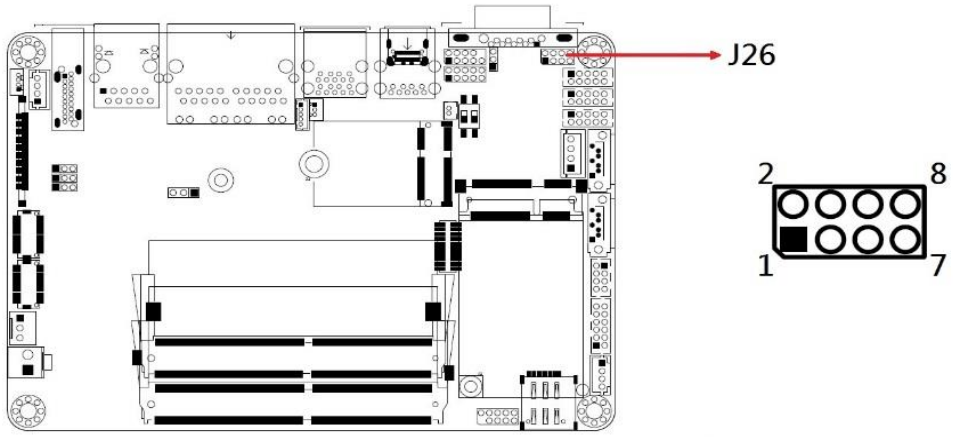
Pin	Assignment	Pin	Assignment
1	Lineout_L	2	Lineout_R
3	JD_FRONT	4	Ground
5	LINEIN_L	6	Linein_R
7	JD_LINEIN	8	Ground
9	MIC_L	10	MIC-R
11	JD_MIC1	12	Ground

2.5.4 SATA HDD Power Connector (J16)



Pin	Assignment	Pin	Assignment
1	+5V	3	Ground
2	Ground	4	+12V

2.5.5 Front Panel Connector (J26)



Pin	Assignment	Pin	Assignment
1	Ground	2	PWR_BTN
3	3.3V	4	HDD Active
5	Ground	6	Reset
7	+5V	8	Ground

J26 is utilized for system indicators to provide light indication of the computer activities and switches to change the computer status. It provides interfaces for the following functions.

- **ATX Power ON Switch (Pins 1 and 2)**

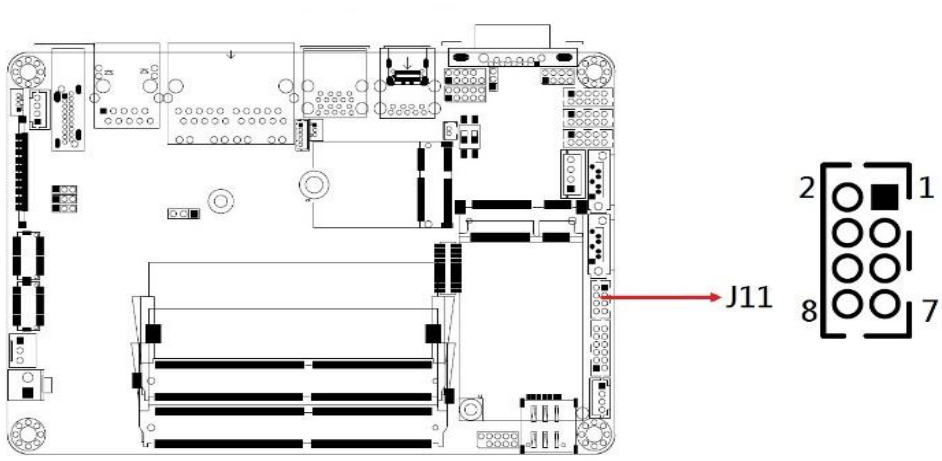
  The 2 pins form an "ATX Power Supply On/Off Switch" for the system, connecting to the power switch on the case. When pressed, the power switch will turn on the system. When pressed again, it will power off the system.
- **Hard Disk Drive LED Connector (Pins 3 and 4)**

  This connector connects to the hard drive activity LED on the control panel. This LED will flash when the HDD is being accessed.
- **Reset Switch (Pins 5 and 6)**

  The reset switch allows you to reset the system without turning the main power switch off and then on again. No specific orientation is required when making a connection to this header.
- **Power LED: Pins 7 and 8**

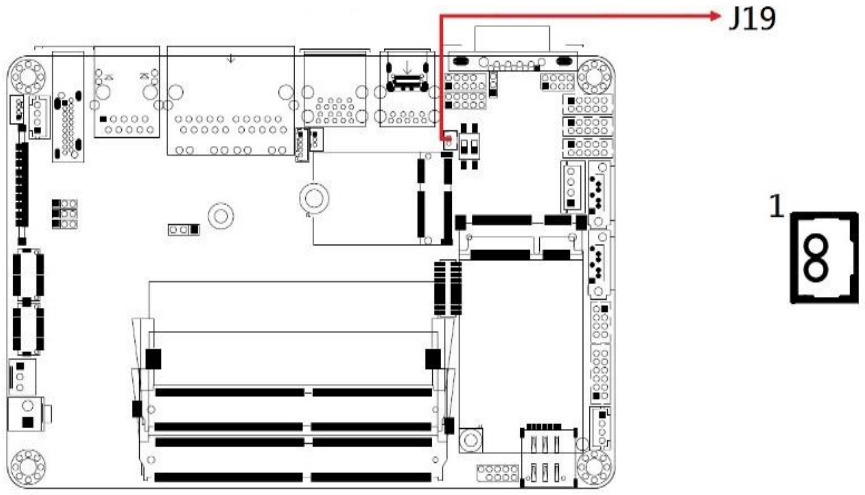
  This connector connects to the system power LED on the control panel. This LED will light when the system turns on.

2.5.6 USB 2.0 Connector (J11)



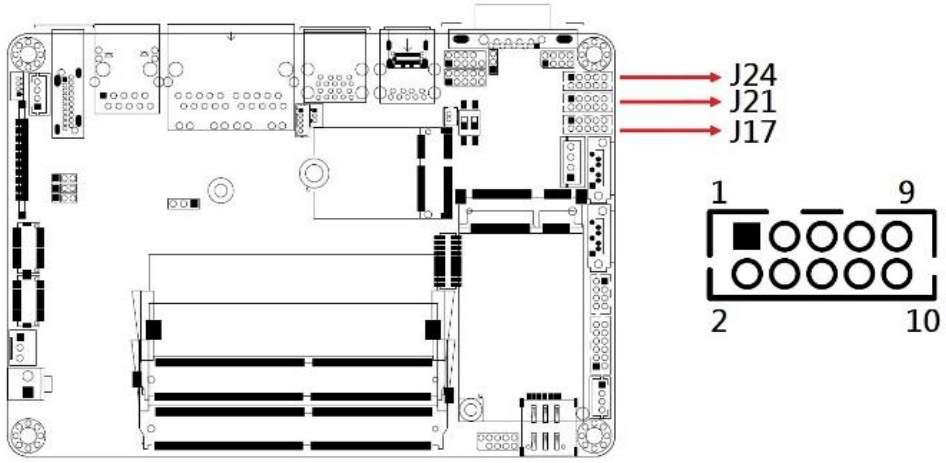
Pin	Assignment	Pin	Assignment
1	VCC	2	Ground
3	D0-	4	D1+
5	D0+	6	D1-
7	Ground	8	VCC

2.5.7 Battery Connector (J19)



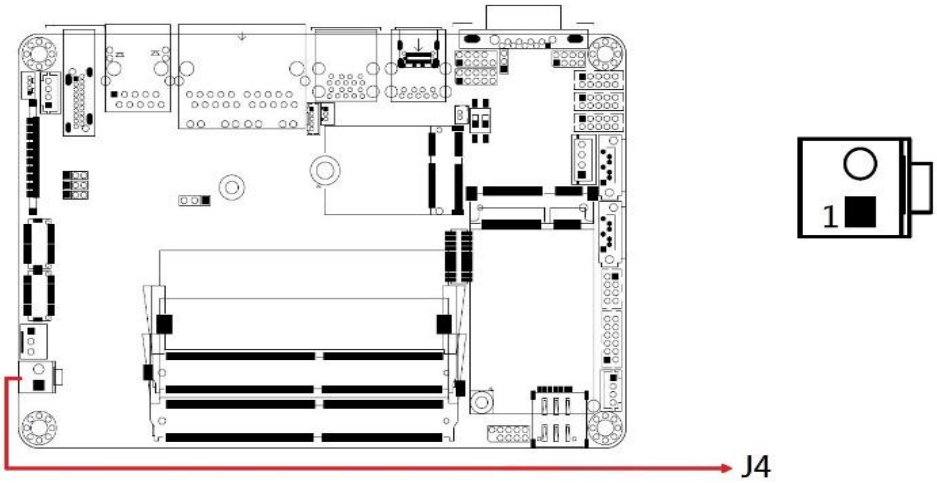
Pin	Assignment
1	Battery+
2	Ground

2.5.8 COM2, COM3, COM4 RS-232 Ports (J24, J21, J17)



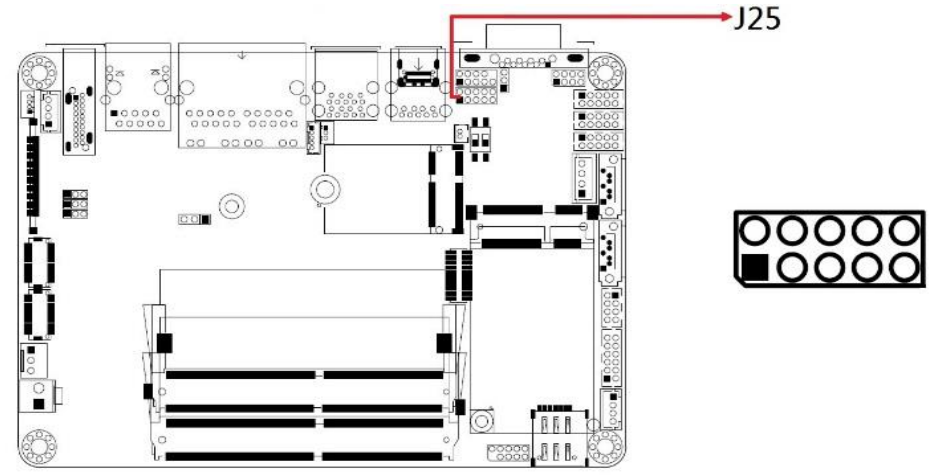
Pin	Assignment	Pin	Assignment
1	DCD, Data carrier detect	2	RXD, Receive data
3	TXD, Transmit data	4	DTR, Data terminal ready
5	Ground	6	DSR, Data set ready
7	RTS, Request to send	8	CTS, Clear to send
9	RI, Ring indicator	10	Not Used

2.5.9 DC Power Input Connector (J4)



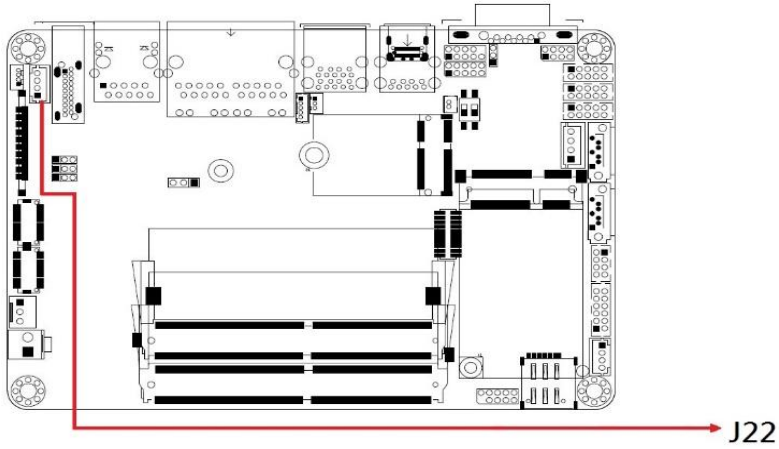
Pin	Assignment
1	+9V ~ +36V
2	Ground

2.5.10 Digital I/O Connector (J25)



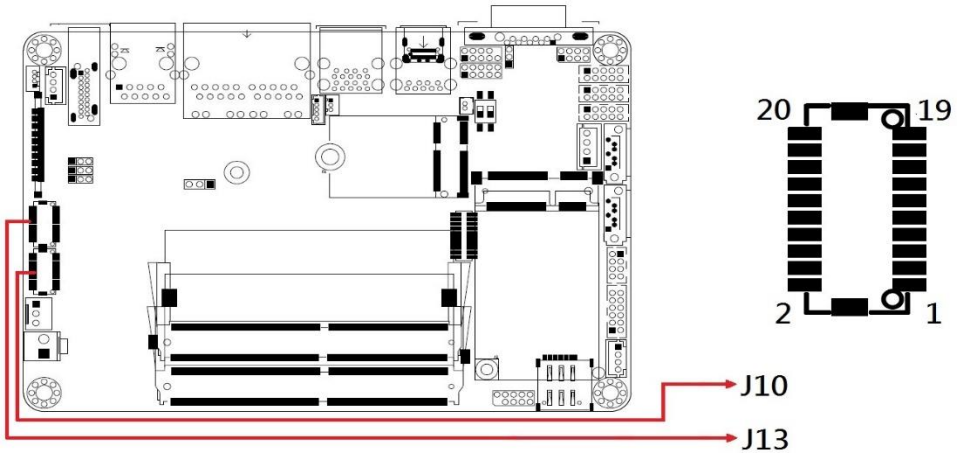
Pin	Assignment	Pin	Assignment
1	Ground	2	VCC
3	OUT3	4	OUT1
5	OUT2	6	OUT0
7	IN3	8	IN1
9	IN2	10	IN0

2.5.11 LCD Backlight Connector (J22)



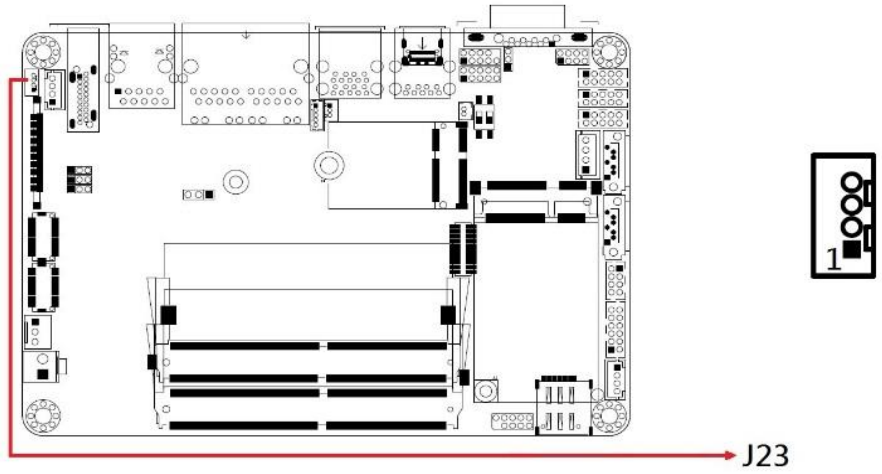
Pin	Assignment	Pin	Assignment
1	+12V	3	Brightness Control
2	Backlight Enable	4	Ground

2.5.12 LVDS Connector (J10, J13)



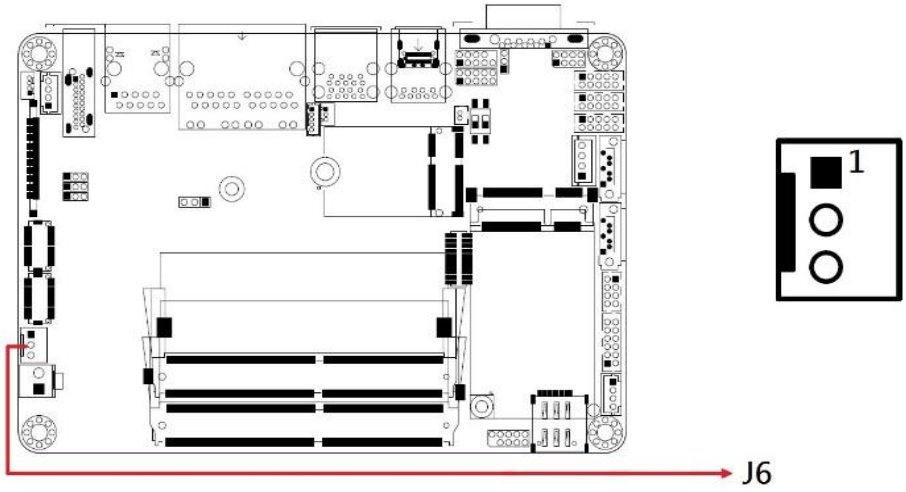
Pin	Assignment	Pin	Assignment
1	TX0P	2	TX0N
3	Ground	4	Ground
5	TX1P	6	TX1N
7	Ground	8	Ground
9	TX2P	10	TX2N
11	Ground	12	Ground
13	CLKP	14	CLKN
15	Ground	16	Ground
17	TX3P	18	TX3N
19	Power	20	Power

2.5.13 SMBUS Connector (J23)

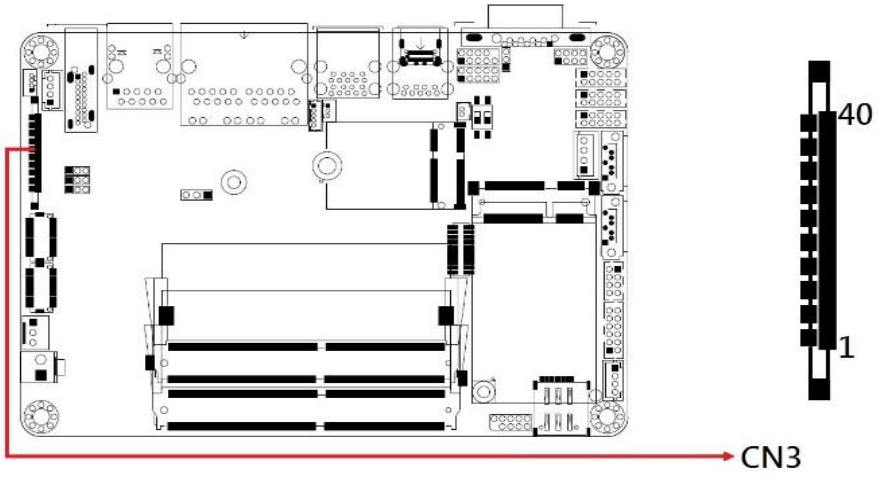


Pin	Assignment	Pin	Assignment
1	+3.3V	3	SMB_DATA
2	SMB_CLK-	4	Ground

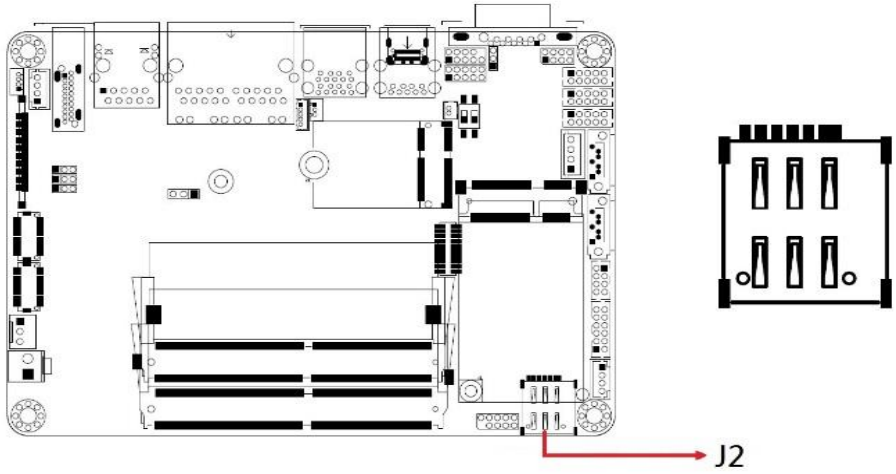
2.5.14 Fan Power Connector (J6)



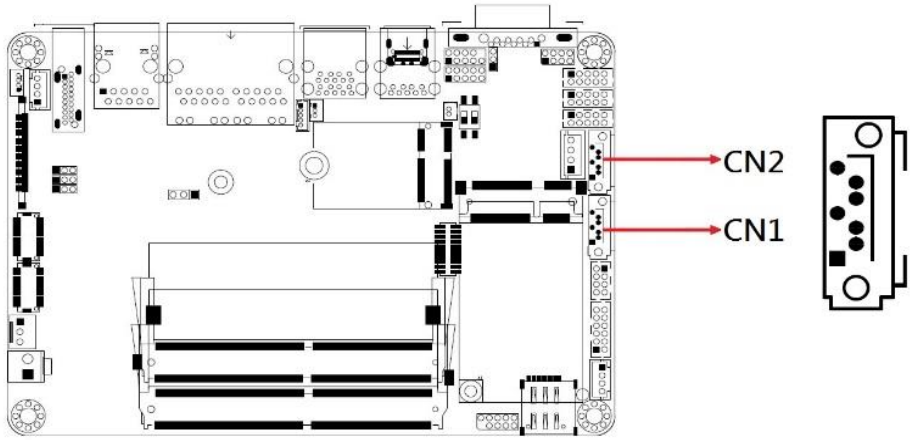
2.5.15 EDP Connector (CN3)



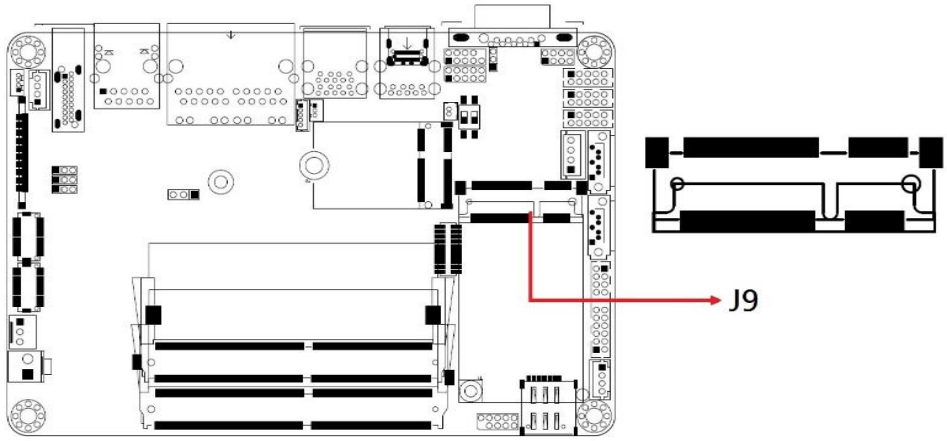
2.5.16 SIM Card Slot (J2)



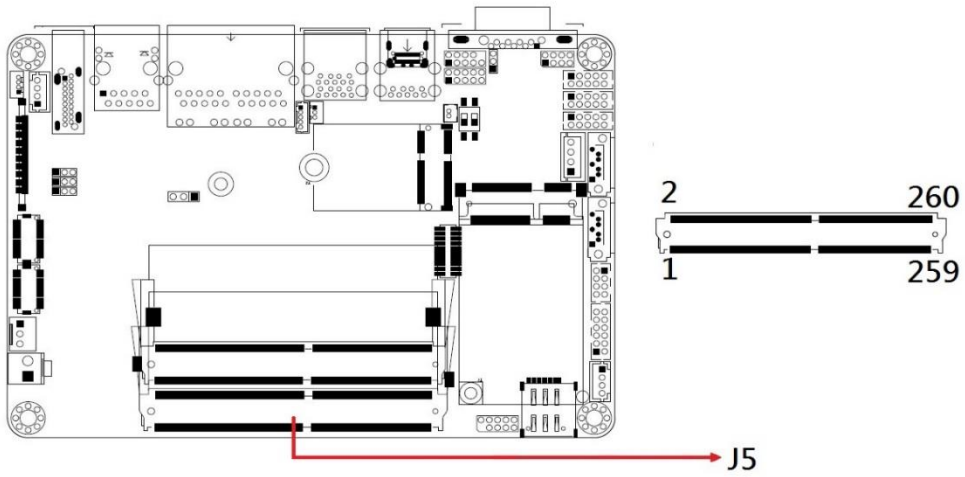
2.5.17 SATA Connectors (CN1,CN2)



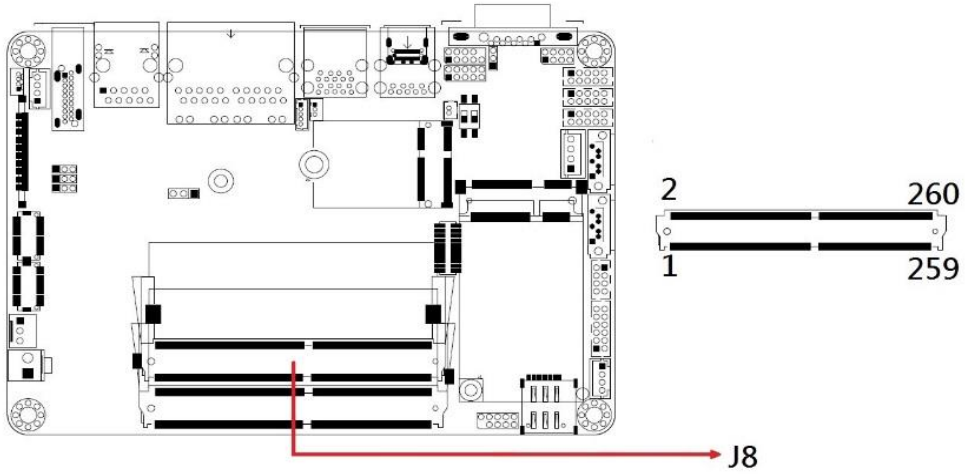
2.5.18 23. Mini PCIE Connector (J9)



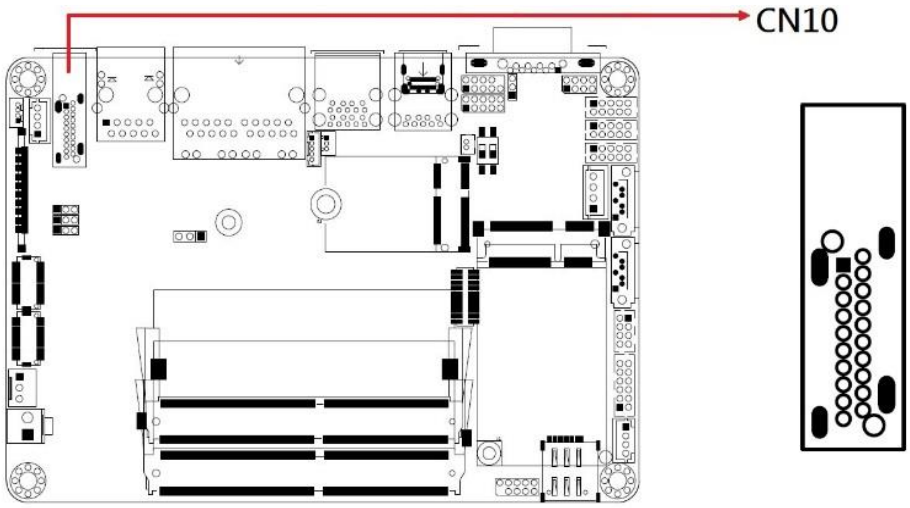
2.5.19 DDR4 Slot (J5)



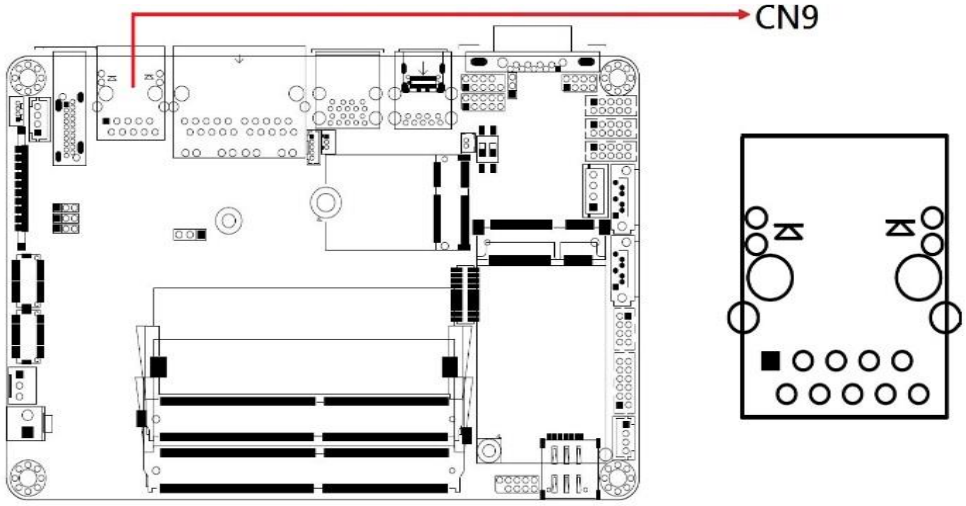
2.5.20 DDR4 Slot (J8)



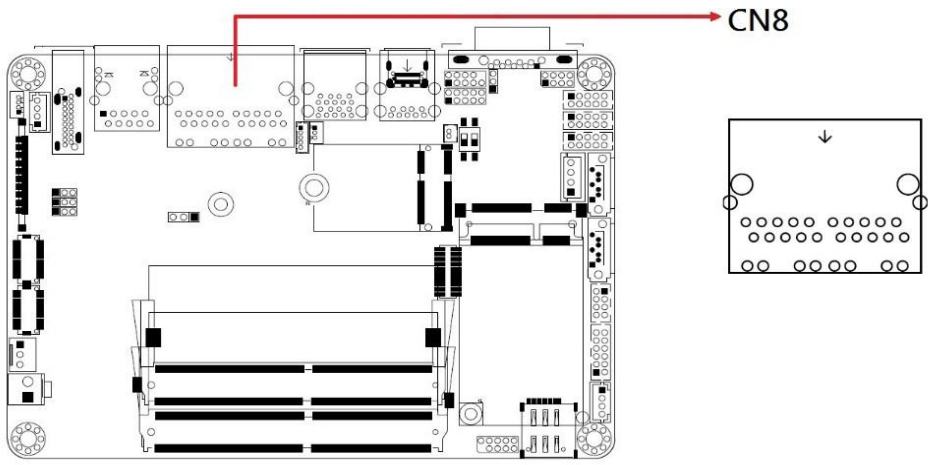
2.5.21 DP Connector (CN10)



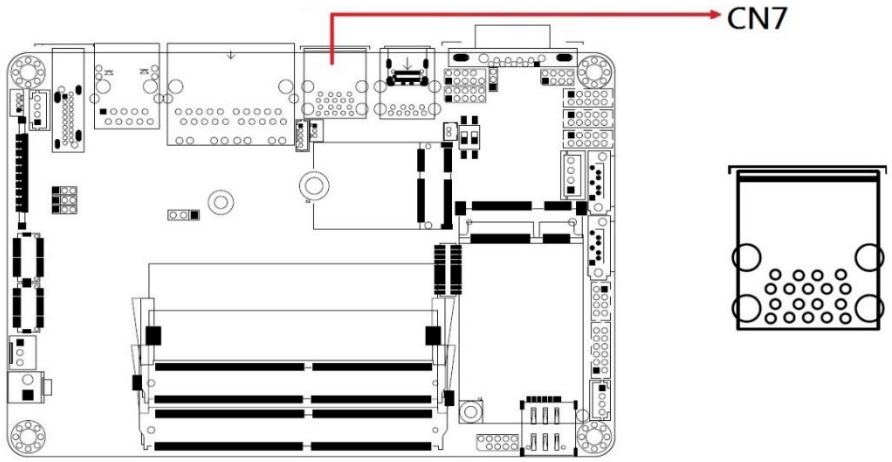
2.5.22 LAN Connector (CN9)



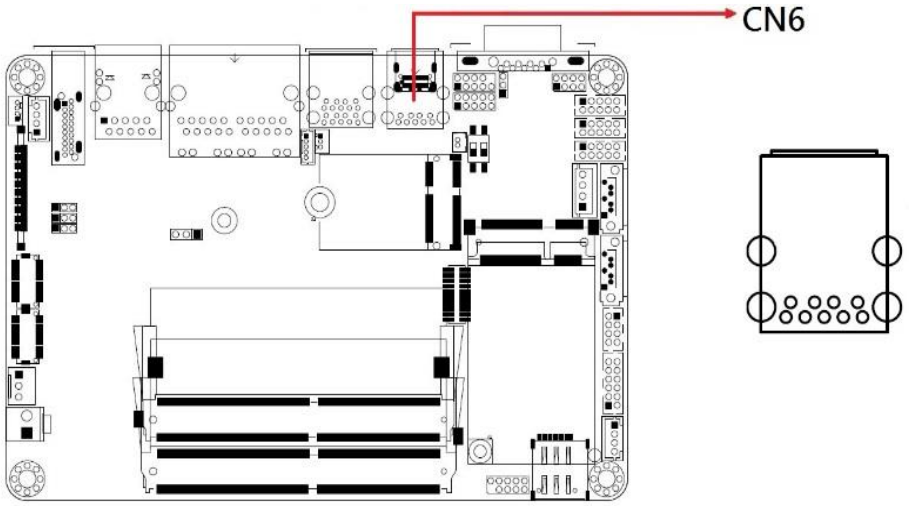
2.5.23 LAN Connector (CN8)



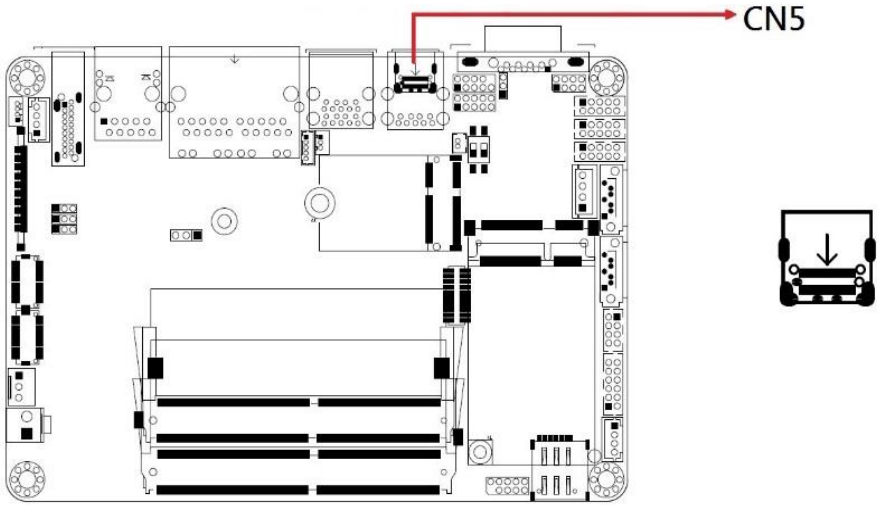
2.5.24 USB3.0 Connector (CN7)



2.5.25 USB3.0 Connector (CN6)



2.5.26 Type-C Connector (CN5)



Chapter 3
Driver Installation

The information provided in this chapter includes:

- Intel® Chipset Software Installation Utility
- VGA Driver
- HD Audio Driver
- LAN Driver
- Intel® Management Engine Drivers Installation

3.1 Introduction

This section describes the installation procedures for software drivers. T

Note: After installing your Windows operating system, you must install the Intel® Chipset Software Installation Utility first before proceeding with the drivers installation.

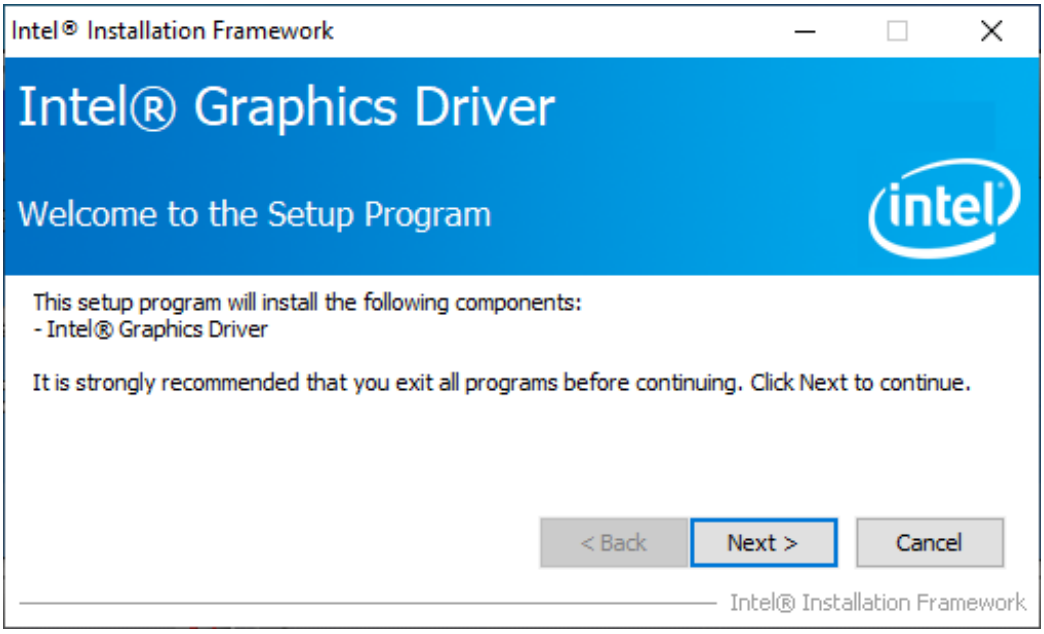
3.2 Intel® Chipset Software Installation Utility

The Intel® Chipset drivers should be installed first before the software drivers to install INF files for Plug & Play function for Intel chipset components.

1. Go to the download page of the product. Copy the compressed drivers file to your computer. Double click the file to decompress it. Run “CDGuide” to go to the main drivers page as shown. Click **Intel** on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.

2. Click **Intel(R) Chipset Software Installation Utility**.

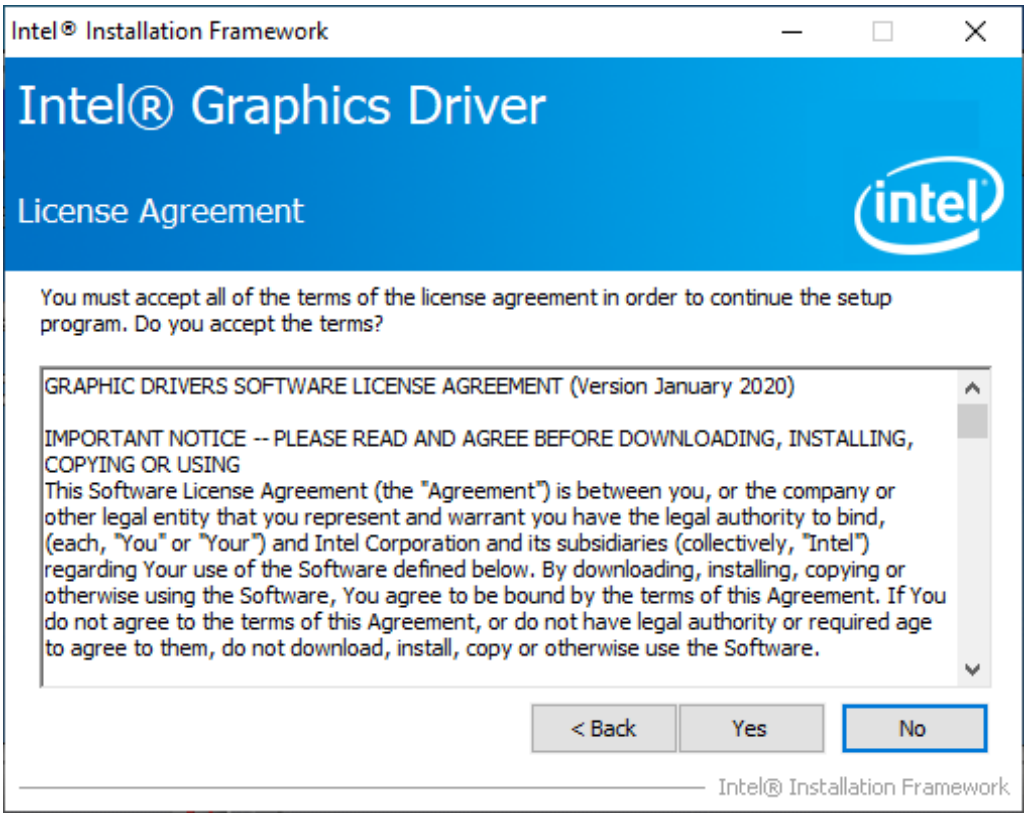
3. When the *Welcome* screen to the Intel® Chipset Device Software appears, click **Next** to continue.
4. Accept the software license agreement and proceed with the installation process.
5. On the *Readme File Information* screen, click **Install** for installation.
6. After the installation, press **Finish** to complete the setup process.



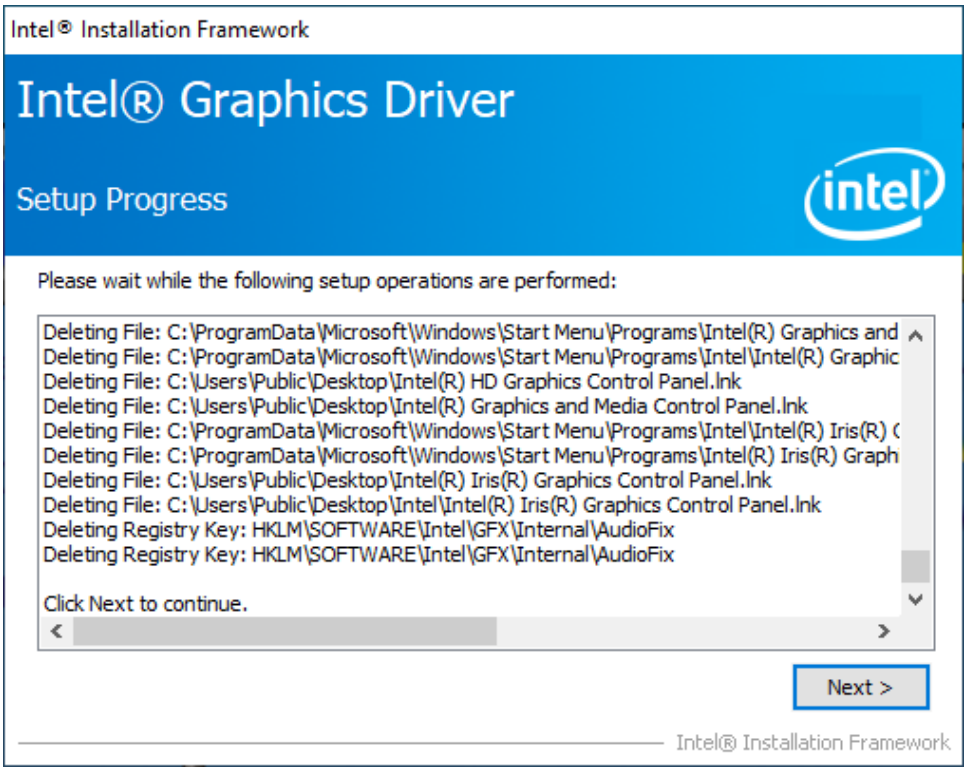
3.3 VGA Driver Installation

1. Click **Intel** on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.
2. Click **Intel(R) Elkhartlake Graphics Driver**.

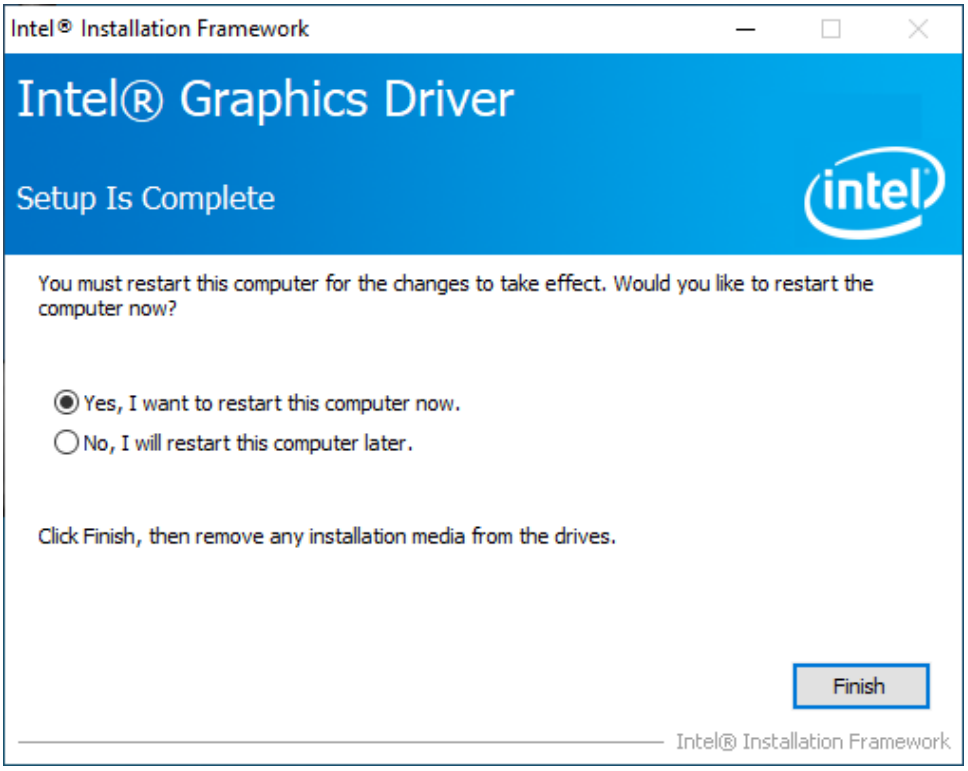
3. When the *Welcome* screen appears, click **Next** to continue.



4. Click **Yes** to accept the license agreement and click **Next** in The Readme File Information screen. Click **Next** in the Setup Progress screen.



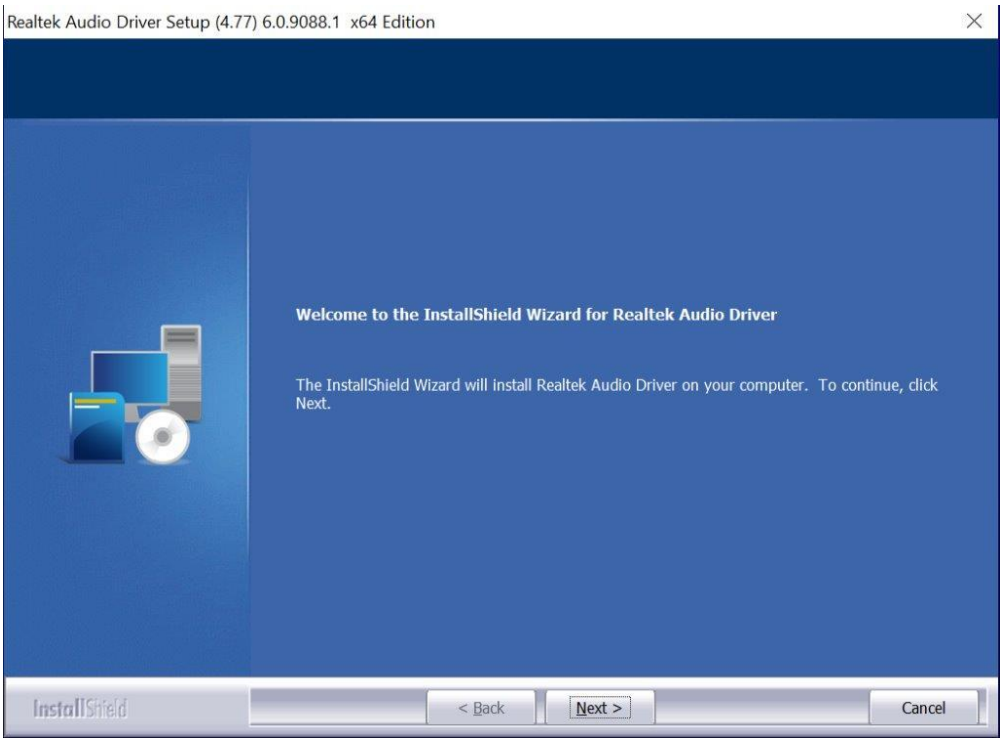
5. Restart the computer when prompted. Click **Finish**, then remove any installation media from the drives.



3.4 HD Audio Driver Installation

1. Click Intel on the left pane and then **Intel(R) Elkhartlake Chipset Drivers** on the right pane.
2. Click **Realtek High Definition Audio Driver**.

3. On the Welcome screen, click **Next** to continue.



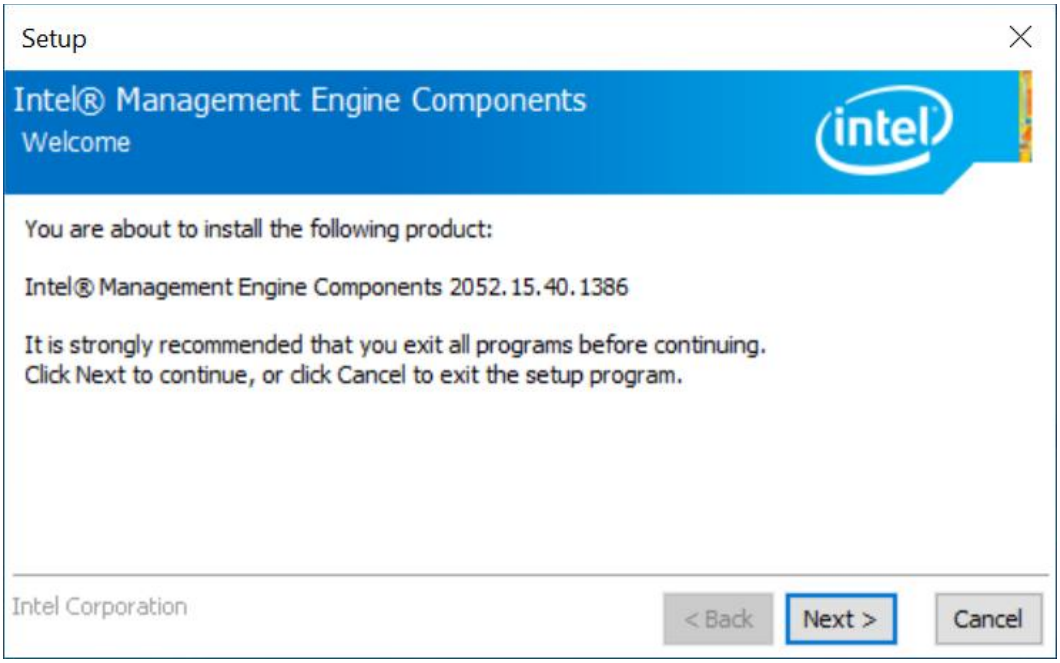
4. When the InstallShield Wizard has successfully installed the Realtek Audio Driver, restart the computer. Click **Finish** to complete the setup.

   Click **Intel** on the left pane and then **Intel(R) Apollolake Chipset Drivers** on the right pane.

3.5 Intel® ME Drivers Installation

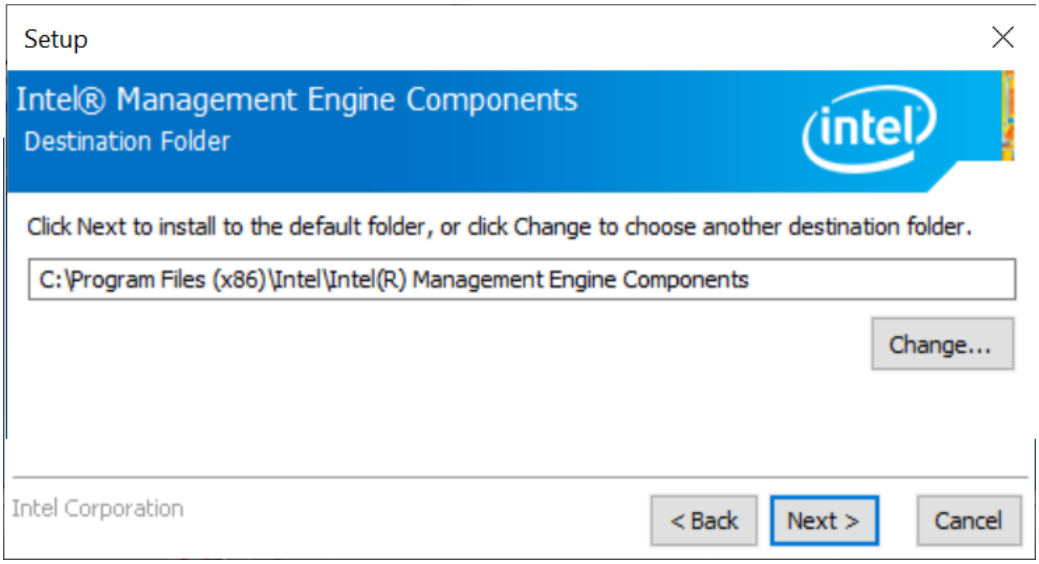
1. Click Intel on the left pane and then **Intel(R) ME Drivers**.

2. The welcome screen to the Intel® Management Engine Components appears. Click **Next** to continue.



3. Accept the license agreement and click **Next**.

4. On the *Setup's Destination Folder* screen, click **Next** to continue.



5. After the Intel® components have been completely installed, click **Finish**.

   Click **Intel** on the left pane and then **Intel(R) Apollolake Chipset Drivers** on the right pane.

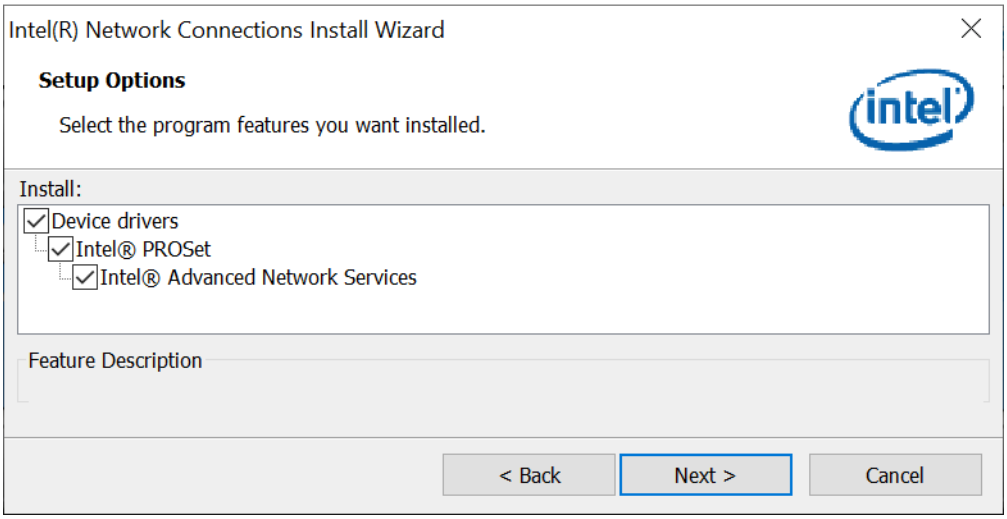
3.6 LAN Driver Installation

1. Click **LAN Card** on the left pane and then **Intel LAN Controller Drivers** on the right pane.

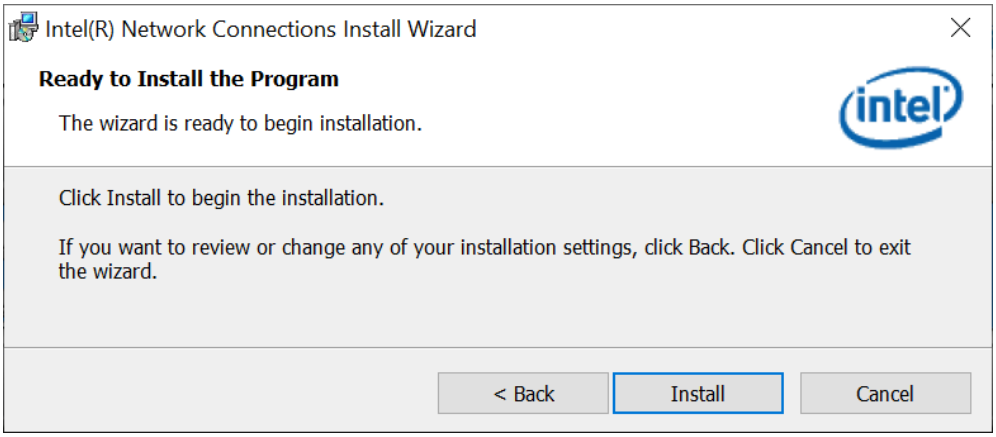
2. Choose **Intel(R) I21x Gigabit Network Drivers**.

3. In the welcome screen to the installation wizard for Intel(R) Network Connections, click **Next**.
4. On the next screen, accept the terms in the license agreement and click **Next**.

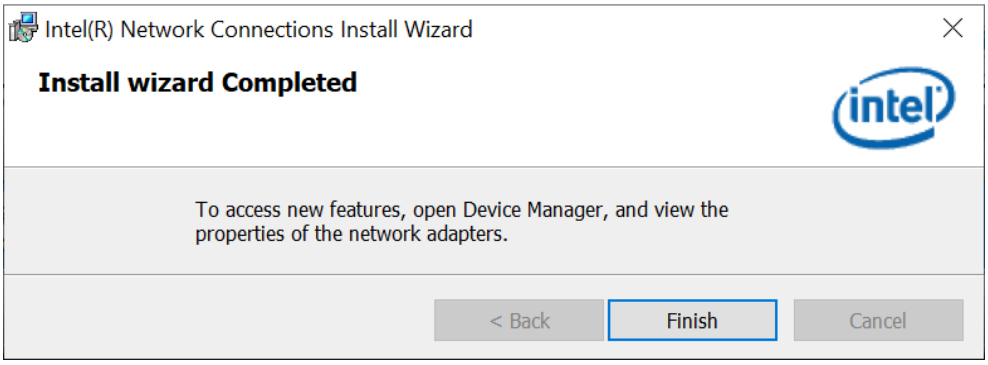
5. In the *Setup Options* screen, click **Next**.



6. Click **install** to begin the installation.



7. Click **Finish** when Install wizard has completed installation.



Chapter 4
BIOS Setup

This chapter describes the different settings available in the AMI BIOS that comes with the board. The topics covered in this chapter are as follows:

- Main Settings
- Advanced Settings
- Chipset Settings
- Security Settings
- Boot Settings
- Save & Exit

4.1 Introduction

The BIOS (Basic Input/Output System) installed in the ROM of your computer system supports Intel® processors. The BIOS provides critical low-level support for standard devices such as disk drives, serial ports and parallel ports. It also provides password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

4.2 BIOS Setup

The BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the BIOS is immediately activated. Press the <Del> key immediately allows you to enter the Setup utility. If you are a little bit late pressing the <Del> key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup.

If you still need to enter Setup, restart the system by pressing the ”Reset” button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again.

The following message will appear on the screen:

```
Press <DEL> to Enter Setup
```

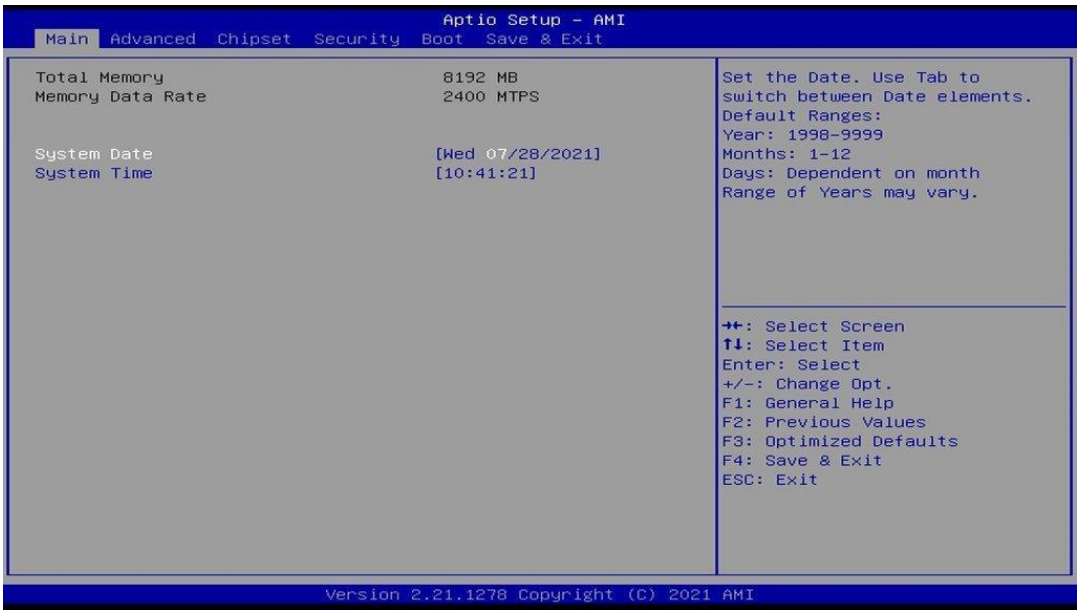
In general, press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help, and <Esc> to quit.

When you enter the BIOS Setup utility, the *Main Menu* screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

Warning: It is strongly recommended that you avoid making any changes to the chipset defaults.

These defaults have been carefully chosen by both AMI and your system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could make the system unstable and crash in some cases.

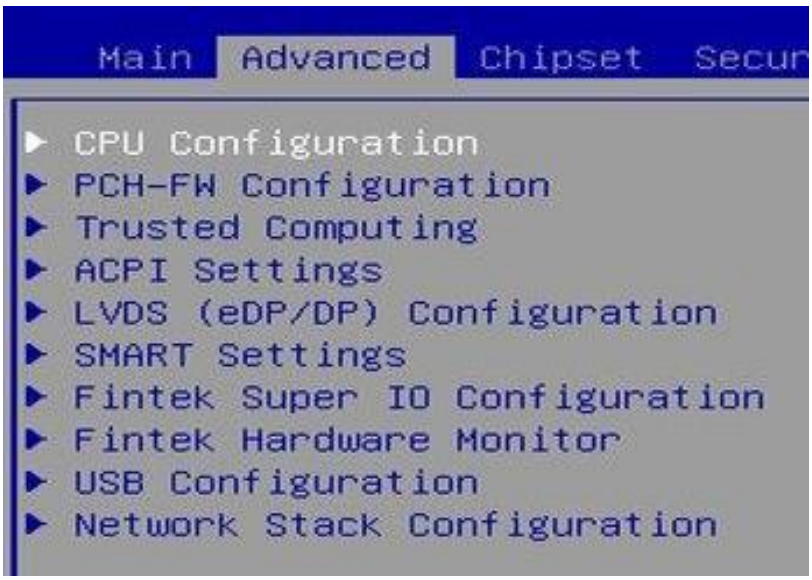
4.3 Main Settings



BIOS Setting	Description
System Date	Sets the date. Use the <Tab> key to switch between the date elements.
System Time	Set the time. Use the <Tab> key to switch between the time elements.

4.4 Advanced Settings

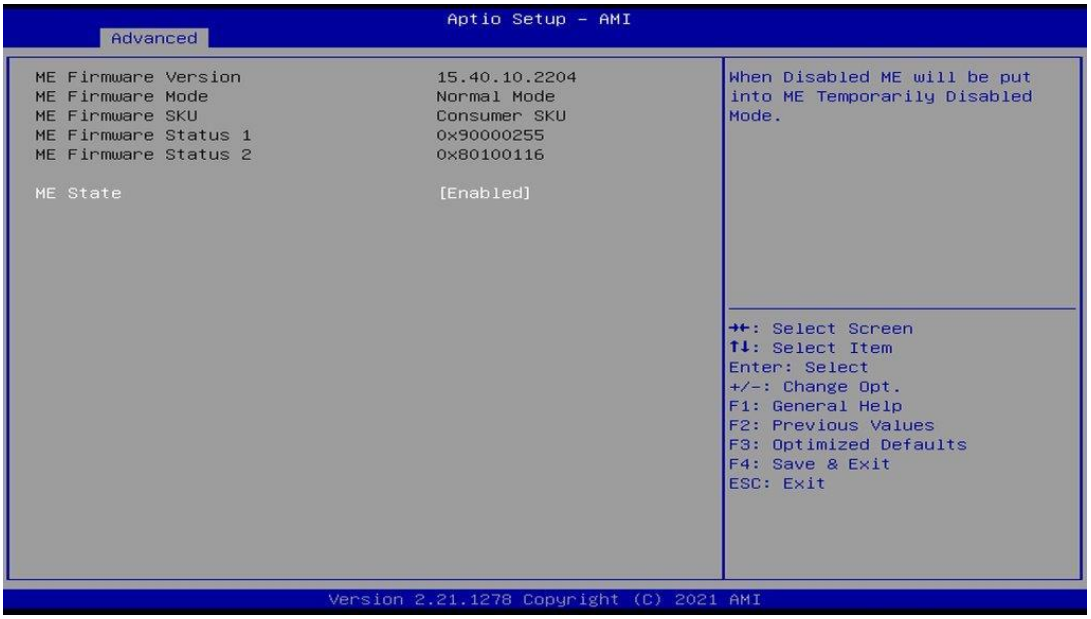
This section allows you to configure, improve your system and allows you to set up some system features according to your preference.



4.4.1 CPU Configuration

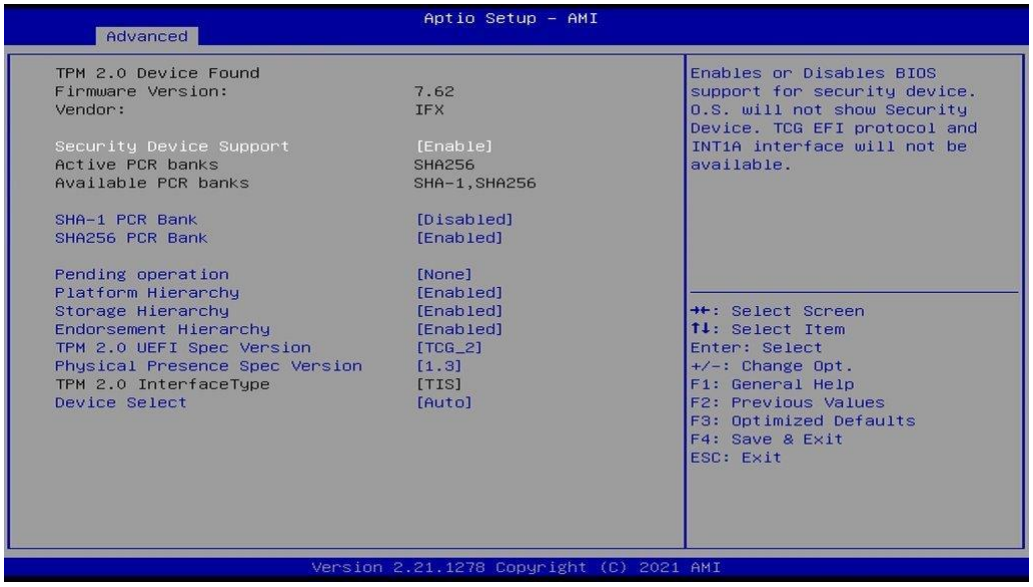
Displays CPU configuration parameters.

4.4.2 PCH-FW Configuration



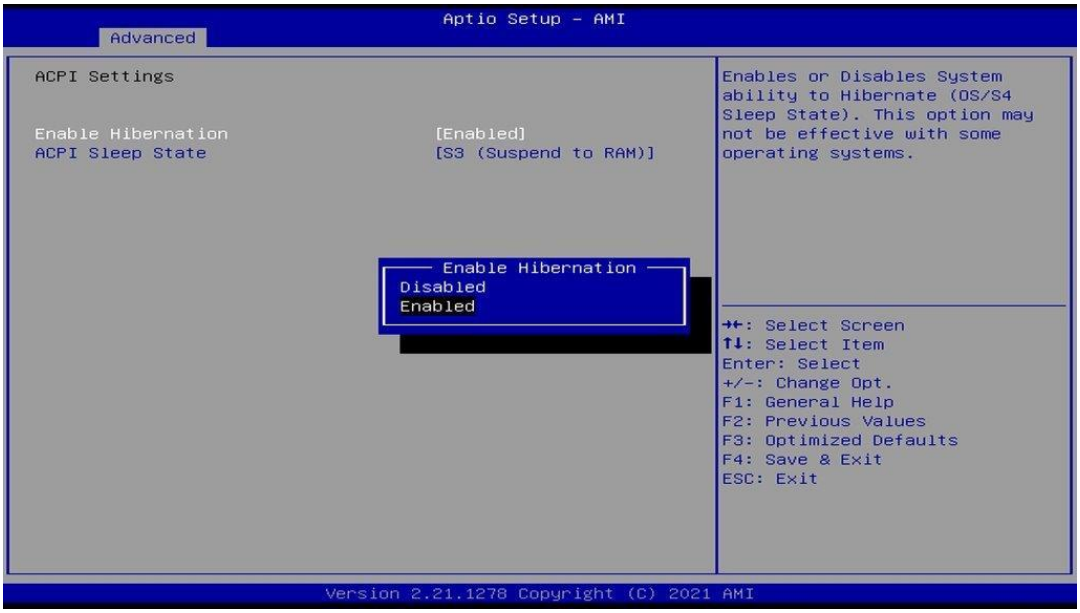
BIOS Setting	Description
ME State	When disabled ME will be put into ME Temporarily Disabled Mode.

4.4.3 Trusted Computing



BIOS Setting	Description
Security Device Support	Enables / Disables BIOS support for security device. OS will not show security device. TCG EFI protocol and INTIA interface will not be available.
SHA-1 PCR Bank	Enables / Disables SHA-1 PCR Bank.
SHA256 PCR Bank	Enables / Disables SHA256 PCR Bank.
Pending operation	Schedule an operation for the security device. Note: Your computer will reboot during restart in order to change state of security device.
Platform Hierarchy	Enables / Disables platform hierarchy.
Storage Hierarchy	Enables / Disables storage hierarchy.
Endorsement Hierarchy	Enables / Disables endorsement hierarchy.
TPM2.0 UEFI Spec Version	Selects the supported TCG version based o your OS. • **TCG_1_2:** supports Windows 8 /10. • **TCG_2:** supports new TCG2 protocol and event format for Windows 10 or later.
Physical Presence Spec Version	Selects to show the PPI Spec Version (1.2 or 1.3) that the OS supports. **Note:** Some HCK tests might not support 1.3.
Device Select	• **TPM 1.2** will restrict support to TPM 1.2 devices only. • **TPM 2.0** will restrict support to TPM 2.0 devices only. • **Auto** will support both with the default being set to TPM 2.0 deices if not found, and TPM 1.2 device will be enumerated.

4.4.4 ACPI Settings



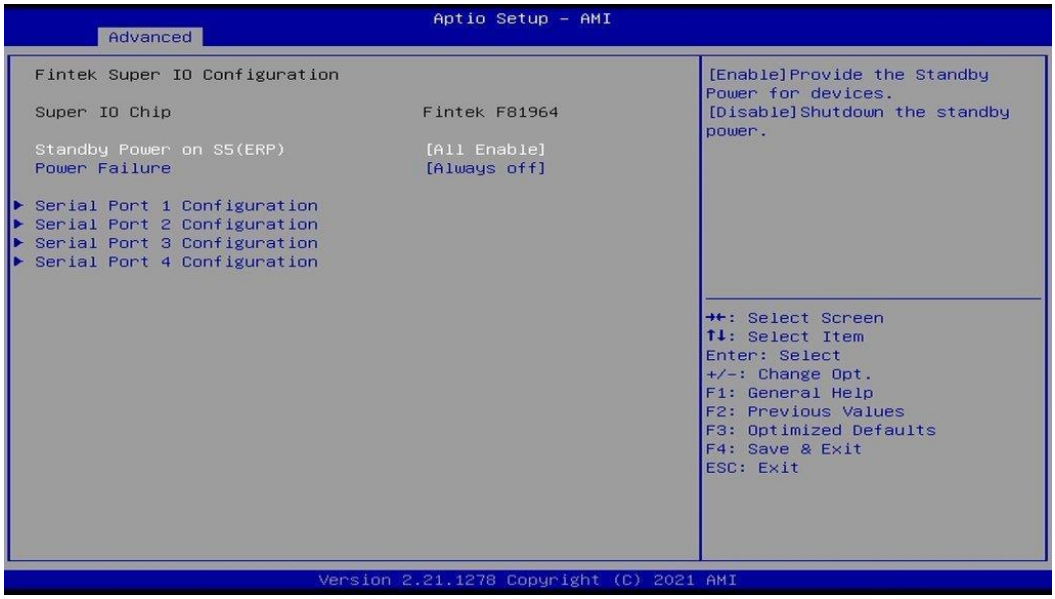
BIOS Setting	Description
Enable Hibernation	Enables / Disables the system ability to hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	Selects an ACPI sleep state (Suspend Disabled or S3) where the system will enter when the Suspend button is pressed.

4.4.5 LVDS (eDP/DP) Configuration

BIOS Setting	Description
LVDS (eDP/DP) Support	Enables / Disables LVDS (eDP/DP).
Panel Color Depth	Selects the panel collor depth. Options: 18 bit, 24 Bit (VESA), 24 bit (JEIDA)
LVDS Channel Type	Chooses the LVDS as single or dual channel.
LCD Panel Type	Selects LCD panel used by Intel Graphics Device by selecting the appropriate setup item. Resolution Options: VBIOS Default, 800 x 480, 800 x 600, 1024 x 768, 1280 x 800, 1280 x 1024, 1366 x 768, 1440 x 900, 1600 x 900, 1680 x 1050, 1920 x 1080, 1920 x 1200
LVDS Brightness Level Control	Options: Level-1 to Level-8

4.4.6 SMART Settings

4.4.7 F81846 Super IO Configuration



BIOS Setting	Description
Standby Power on S5(ERP)	This setting enables or shutdown the standby power for devices.
Power Failure	Options: Always on, Always off
Serial Ports Configuration	Sets parameters of serial ports. Enables / Disables the serial port and select an optimal setting for the Super IO device.

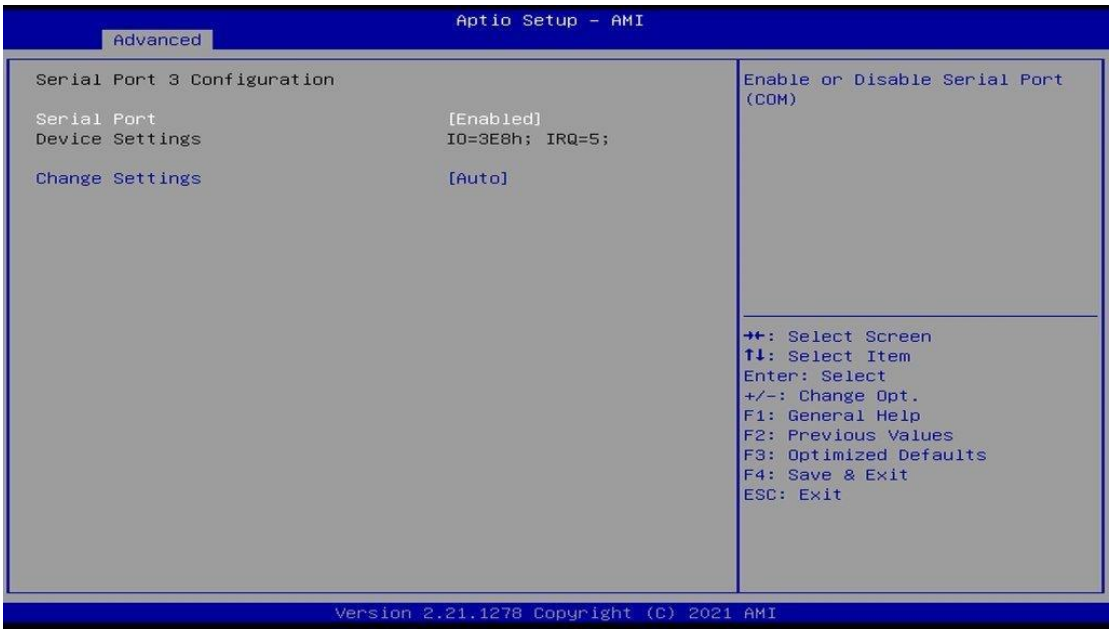
Serial Port 1 Configuration

BIOS Setting	Description
Serial Port	Enables / Disables the serial port.
Change Settings	Selects an optimal settings for Super I/O device. Options: • Auto • IO = 3F8h; IRQ = 4 • IO = 3F8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2F8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 3E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12
Device Mode	Change the serial port mode. Options: RS232 RS485 TX Low Active RS485 with Termination TX Low Active RS422 RES422 with Termination

Serial Port 2 Configuration

BIOS Setting	Description
Serial Port	Enables / Disables the serial port.
Change Settings	Selects an optimal settings for Super I/O device. Options: • Auto • IO = 2F8h; IRQ = 3 • IO = 3F8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2F8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 3E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12

Serial Port 3 Configuration

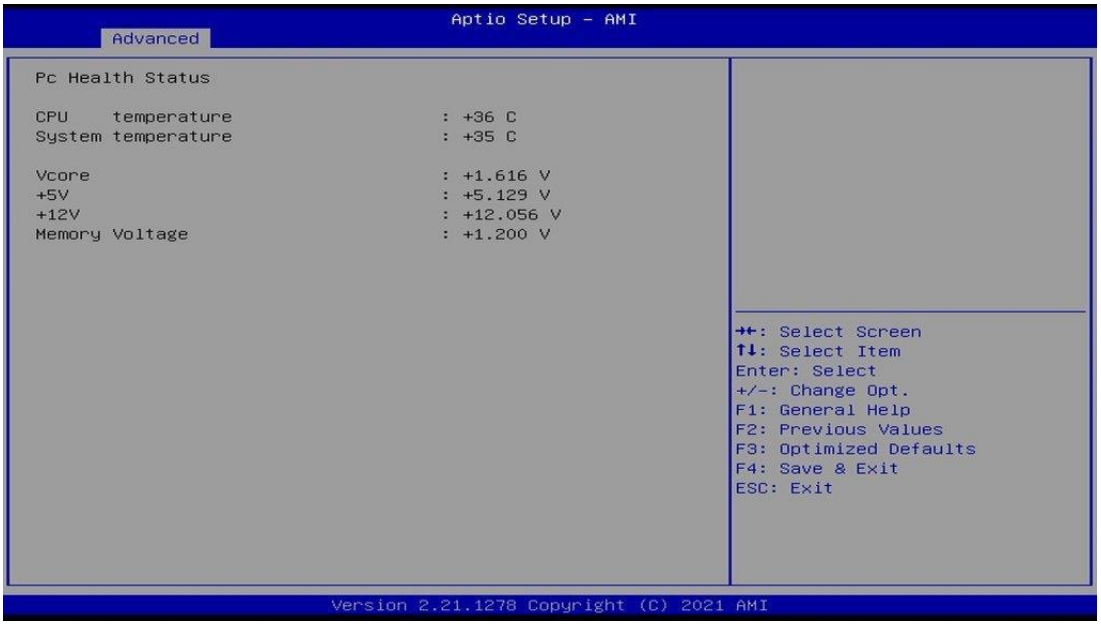


BIOS Setting	Description
Serial Port	Enables / Disables the serial port.
Change Settings	Selects an optimal settings for Super I/O device. Options: • Auto • IO = 3E8h; IRQ = 7 • IO = 3E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2F0h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E0h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12

Serial Port 4 Configuration

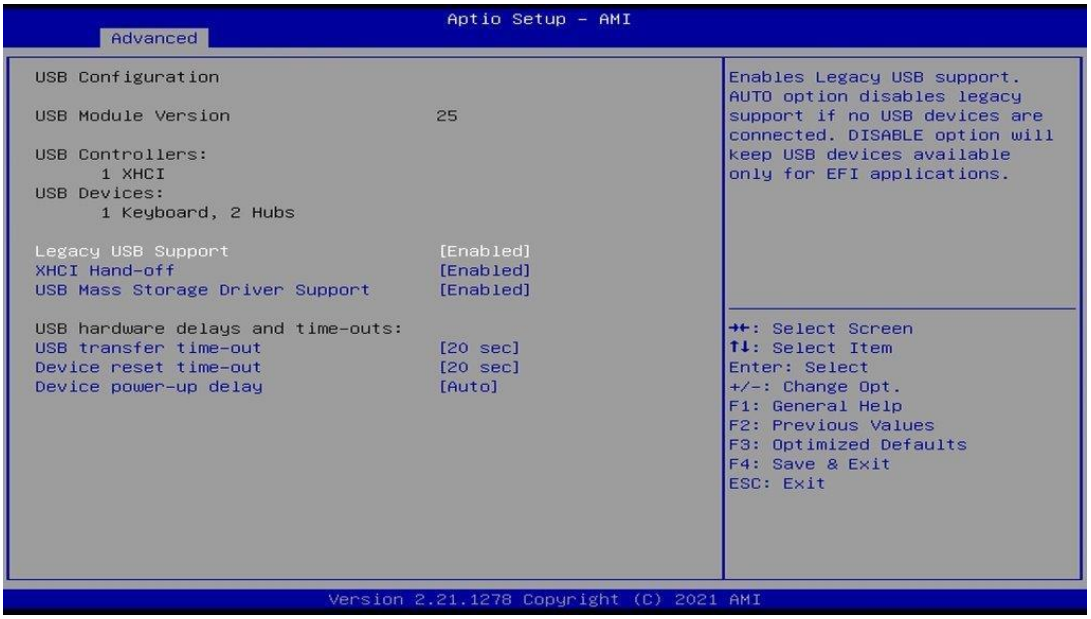
BIOS Setting	Description
Serial Port	Enables / Disables the serial port.
Change Settings	Selects an optimal settings for Super I/O device. Options: • Auto • IO = 2E8h; IRQ = 7 • IO = 3E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E8h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2F0h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12 • IO = 2E0h; IRQ = 3, 4, 5, 6, 7, 9, 10, 11, 12

4.4.8 Fintek Hardware Monitor



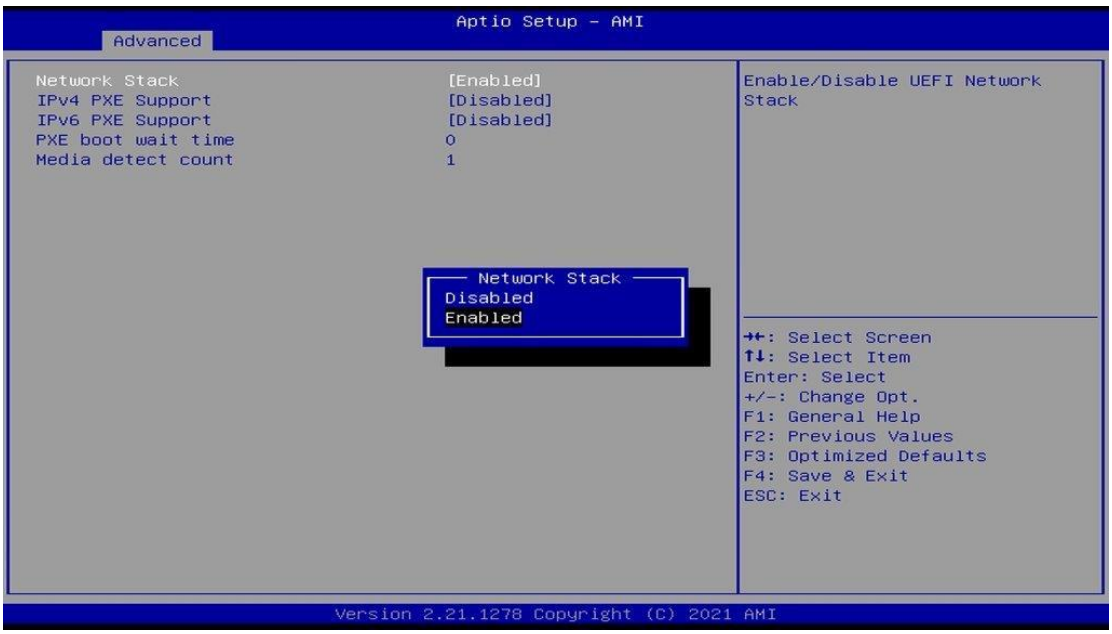
BIOS Setting	Description
Temperatures / Voltages	These fields are the parameters of the hardware monitoring function feature of the motherboard. The values are read-only values as monitored by the system and show the PC health status.

4.4.9 USB Configuration



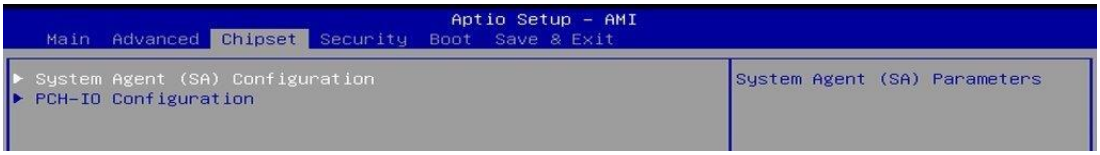
BIOS Setting	Description
Legacy USB Support	• **Enabled** enables Legacy USB support. • **Auto** disables legacy support if there is no USB device connected. • **Disabled** keeps USB devices available only for EFI applications.
XHCI Hand-off	This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	Enables / Disables the support for USB mass storage driver.
USB Transfer time-out	The time-out value (1 / 5 10 / 20 secs) for Control, Bulk, and Interrupt transfers.
Device reset time-out	Gives seconds (10 / 20 / 30 / 40 secs) to delay execution of Start Unit command to USB mass storage device.
Device power-up delay	The maximum time the device will take before it properly reports itself to the Host Controller. **Auto** uses default value for a Root port it is 100ms. But for a Hub port, the delay is taken from Hub descriptor.

4.4.10 Network Stack Configuration



BIOS Setting	Description
Network Stack	Enable/Disable UEFI Network Stack
IPv4 PXE Support	Enable/Disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.
IPv6 PXE Support	Enable/Disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.
Media detect count	Number of times the presence of media will be checked. Use either +/- or numeric keys to set the value.

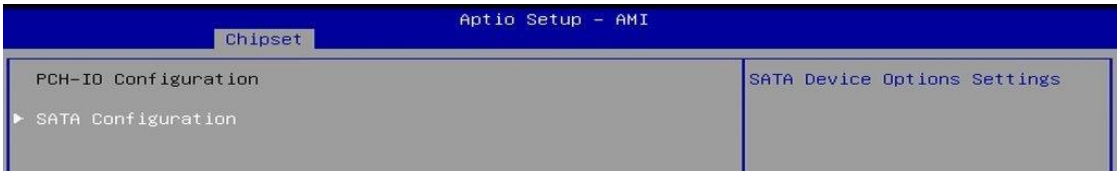
4.5 Chipset Settings

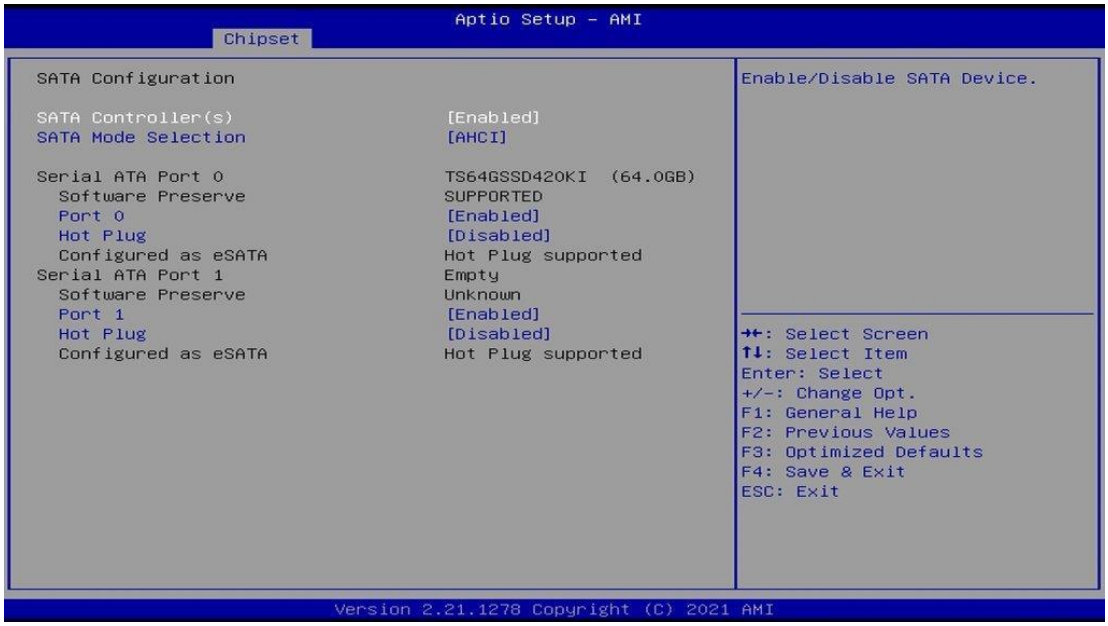


4.5.1 System Agent (SA) Configuration

BIOS Setting	Description
Primary display	Select which of IGFX/PEG/PCI Graphics device should be Primary Display or select HG for Hybrid Gfx
Internal Graphics	Keep IGFX enabled based on the setup options
GTT Size	Options: 2MB, 4MB, 8MB
Aperture Size	Select the Aperture Size. Note: Above 4GB MMIO BIOS assignment is automatically enabled when selecting 2048MB aperture. To use this feature, please disable CSM Support
VT-d	Enable/Disable VT-d capability

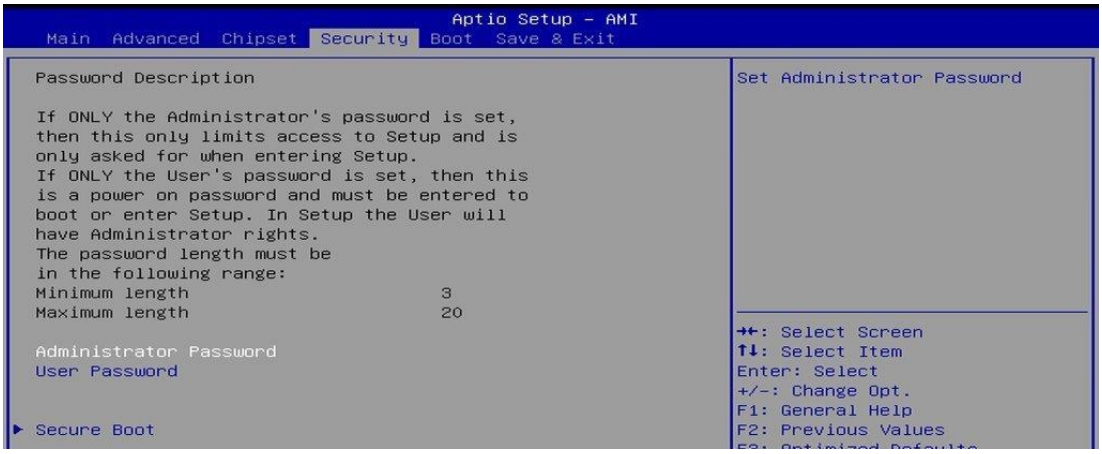
4.5.2 PCH-IO Configuration



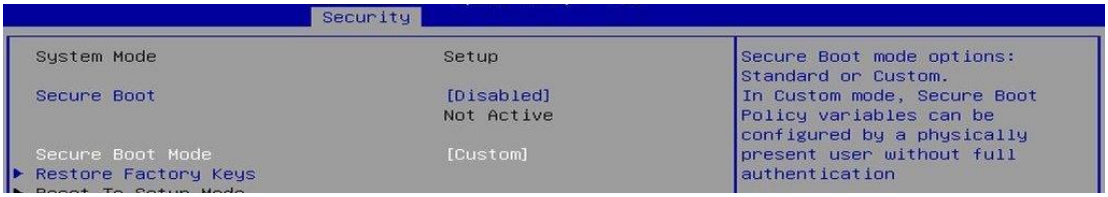


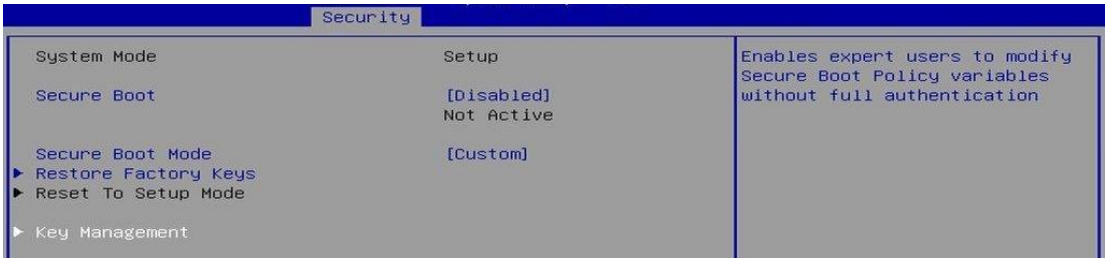
BIOS Setting	Description
SATA Controller(s)	Enables / Disables the Serial ATA.
SATA Mode Selection	Selects IDE or AHCI Mode.
Serial ATA Port 0~1	Enables / Disables Serial Port 0~1.
SATA Ports Hot Plug	Enables / Disables SATA Ports HotPlug.

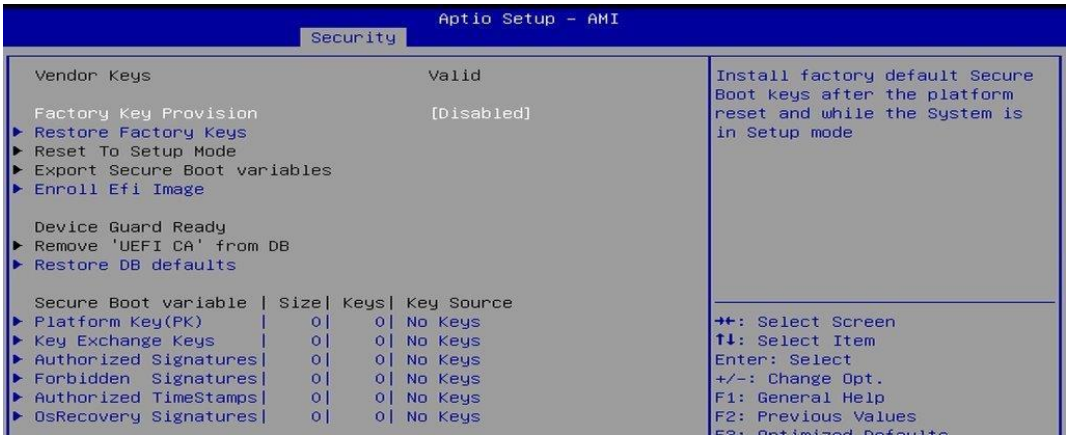
4.6 Security Settings

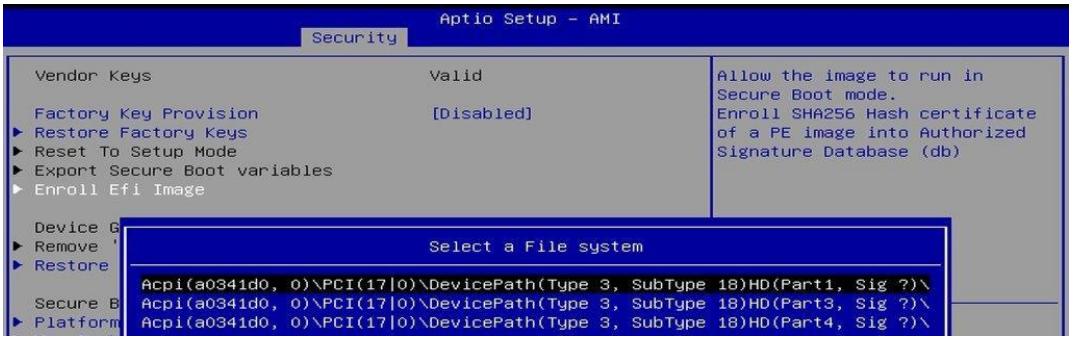


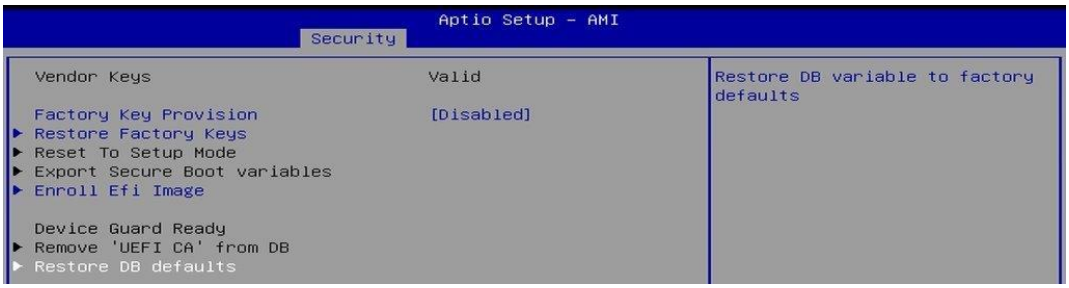
BIOS Setting	Description
Setup Administrator Password	Sets an administrator password for the setup utility.
User Password	Sets a user password.
Secure Boot	Secure Boot configuration

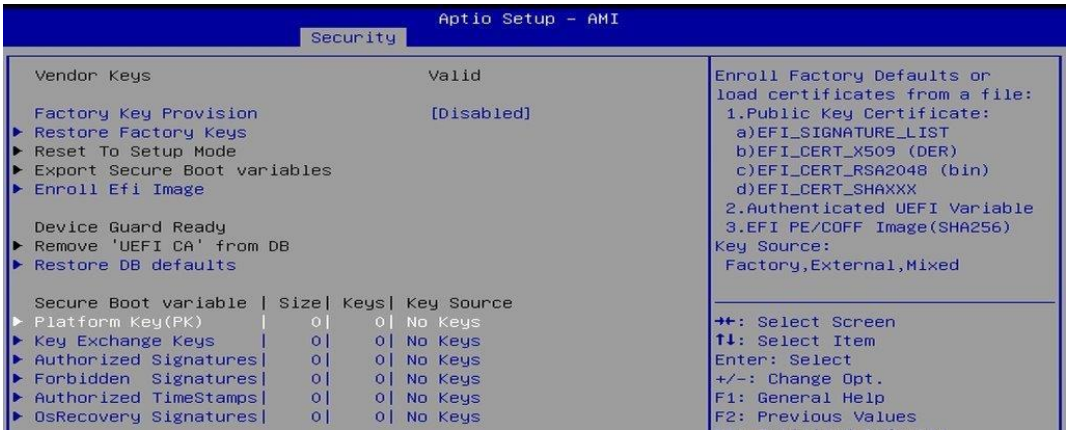




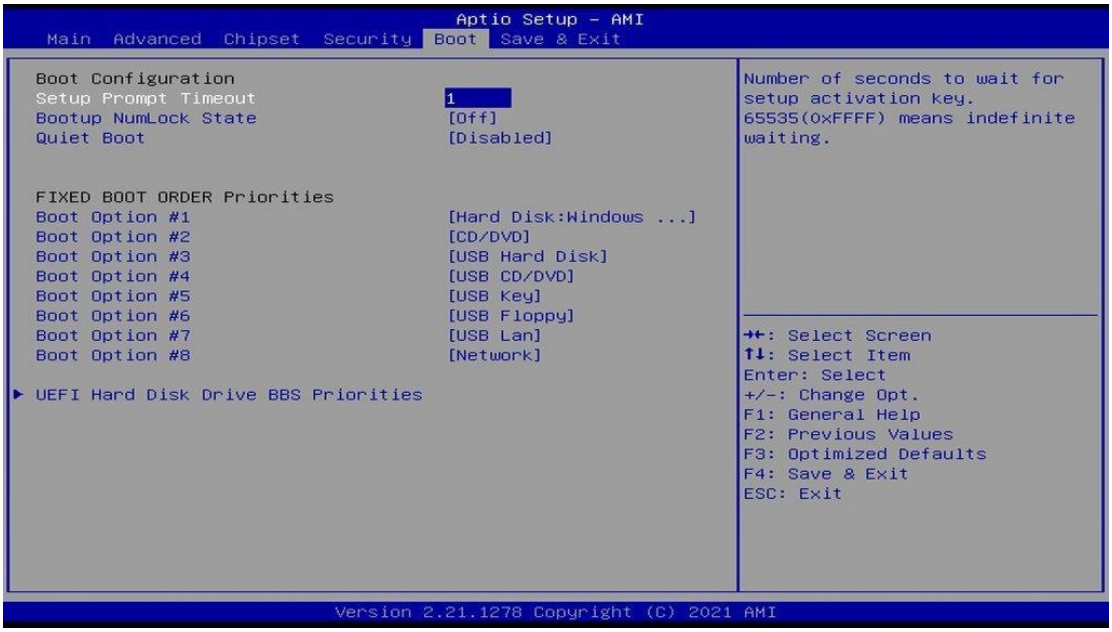

Aptio Setup – AMI
Security
Vendor Keys Valid
Factory Key Provision [Disabled]
Restore Factory Keys
Reset To Setup Mode
Export Secure Boot variables
Enroll Efi Image
Device Guard Ready
Remove 'UEFI CA' from DB
Restore DB defaults
Secure Boot variable | Size| Keys| Key Source
Platform Key(PK) | 0| 0| No Keys
Key Exchange Keys | 0| 0| No Keys
Authorized Signatures| 0| 0| No Keys
Forbidden Signatures| 0| 0| No Keys
Authorized TimeStamps| 0| 0| No Keys
OsRecovery Signatures| 0| 0| No Keys
Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values


Aptio Setup – AMI
Security
Vendor Keys Valid
Factory Key Provision [Disabled]
Restore Factory Keys
Reset To Setup Mode
Export Secure Boot variables
Enroll Efi Image
Allow the image to run in Secure Boot mode.
Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db)
Select a File system
Acpi(a0341d0, 0)\PCI(17|0)\DevicePath(Type 3, SubType 18)HD(Part1, Sig ?)\
Acpi(a0341d0, 0)\PCI(17|0)\DevicePath(Type 3, SubType 18)HD(Part3, Sig ?)\
Acpi(a0341d0, 0)\PCI(17|0)\DevicePath(Type 3, SubType 18)HD(Part4, Sig ?)\


Aptio Setup – AMI
Security
Vendor Keys Valid
Factory Key Provision [Disabled]
Restore Factory Keys
Reset To Setup Mode
Export Secure Boot variables
Enroll Efi Image
Device Guard Ready
Remove 'UEFI CA' from DB
Restore DB defaults
Restore DB variable to factory defaults

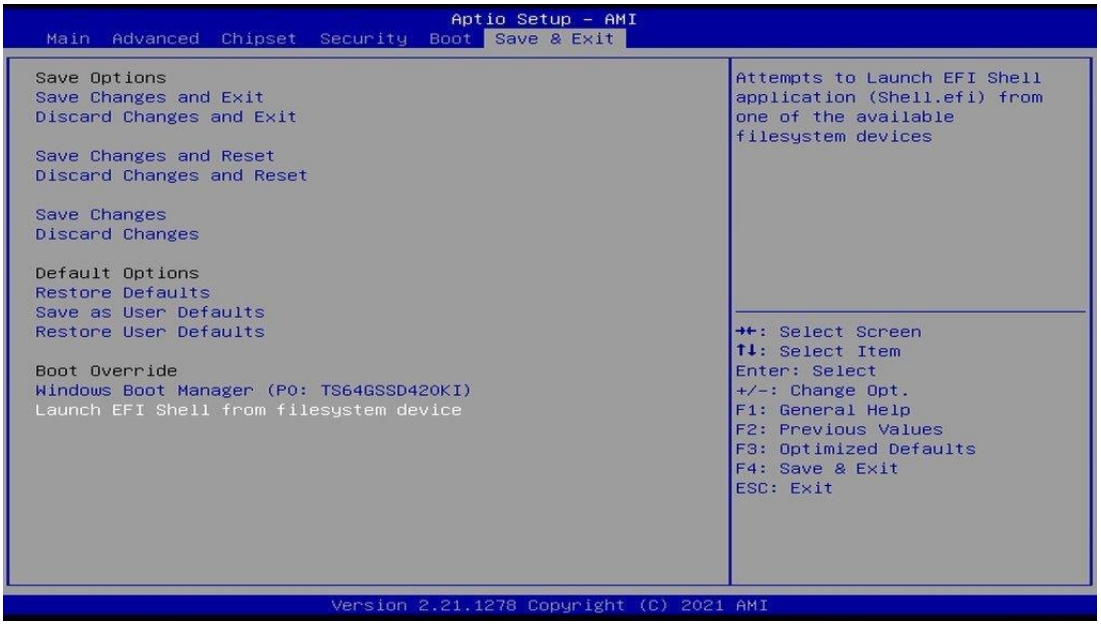

Aptio Setup – AMI
Security
Vendor Keys Valid
Factory Key Provision [Disabled]
Restore Factory Keys
Reset To Setup Mode
Export Secure Boot variables
Enroll Efi Image
Device Guard Ready
Remove 'UEFI CA' from DB
Restore DB defaults
Secure Boot variable | Size| Keys| Key Source
Platform Key(PK) | 0| 0| No Keys
Key Exchange Keys | 0| 0| No Keys
Authorized Signatures| 0| 0| No Keys
Forbidden Signatures| 0| 0| No Keys
Authorized TimeStamps| 0| 0| No Keys
OsRecovery Signatures| 0| 0| No Keys
Enroll Factory Defaults or load certificates from a file:
1.Public Key Certificate:
a)EFI_SIGNATURE_LIST
b)EFI_CERT_X509 (DER)
c)EFI_CERT_RSA2048 (bin)
d)EFI_CERT_SHAXXX
2.Authenticated UEFI Variable
3.EFI PE/COFF Image(SHA256)
Key Source:
Factory,External,Mixed
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values

4.7 Boot Settings



BIOS Setting	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting.
Bootup NumLock State	Selects the keyboard NumLock state.
Quiet Boot	Enables / Disables Quiet Boot option.
Fast Boot	Enables / Disables boot with initialization of a minimal set of devices required to launch the active boot option. Has no effect for BBS boot options.
Boot mode select	Selects a Boot mode, Legacy / UEFI / Dual.
Boot Option Priorities	Sets the system boot order priorities for hard disk, CD/DVD, USB, Network.
UEFI Hard Disk Drives BBS Priorities	Specifies the Boot Device Priority sequence from available UEFI Hark Disk Drives.

4.8 Save & Exit Settings



BIOS Setting	Description
Save Changes and Exit	Exits system setup after saving the changes.
Discard Changes and Exit	Exits system setup without saving any changes.
Save Changes and Reset	Resets the system after saving the changes.
Discard Changes and Reset	Resets system setup without saving any changes.
Save Changes	Saves changes done so far to any of the setup options.
Discard Changes	Discards changes done to any of the setup options.
Restore Defaults	Restores / Loads defaults values for all the setup options.
Save as User Defaults	Saves the changes done so far as User Defaults.
Restore User Defaults	Restores the user defaults to all the setup options.
Launch EFI Shell from filesystem device	Attempts to launch EFI shell application (shell.efi) from one of the available filesystem devices.

Appendix

This section provides the mapping addresses of peripheral devices and the sample code of watchdog timer configuration.

- I/O Port Address Map
- Interrupt Request Lines (IRQ)
- Digital I/O Sample Code
- Watchdog Timer Configuration

A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. The following table lists the I/O port addresses used.

Address	Device Description
0x0000F090-0x0000F097	Standard SATA AHCI Controller
0x0000F080-0x0000F083	Standard SATA AHCI Controller
0x0000F060-0x0000F07F	Standard SATA AHCI Controller
0x00000A00-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x00000A30-0x00000A3F	Motherboard resources
0x00000A40-0x00000A4F	Motherboard resources
0x00000A50-0x00000A5F	Motherboard resources
0x00000A60-0x00000A6F	Motherboard resources
0x0000002E-0x0000002F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000070-0x00000070	System CMOS/real time clock
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000B2-0x000000B3	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x00001800-0x000018FE	Motherboard resources
0x0000164E-0x0000164F	Motherboard resources
0x00000062-0x00000062	Microsoft ACPI-Compliant Embedded Controller

Address	Device Description
0x00000066-0x00000066	Microsoft ACPI-Compliant Embedded Controller
0x0000E000-0x0000EFFF	Mobile 6th/7th Generation Intel(R) Processor Family I/O PCI Express Root Port #1 - 9D10
0x0000F000-0x0000F03F	Intel(R) Iris(R) Plus Graphics 650
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000D00-0x0000FFFF	PCI Express Root Complex
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer
0x00001854-0x00001857	Motherboard resources
0x0000FF00-0x0000FFFE	Motherboard resources
0x0000F040-0x0000F05F	Mobile 6th/7th Generation Intel(R) Processor Family I/O SMBUS - 9D23

B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
IRQ 0	System timer
IRQ 8	System CMOS/real time clock
IRQ 14	Motherboard resources
IRQ 55~204	Microsoft ACPI-Compliant System
IRQ 256~511	Microsoft ACPI-Compliant System
IRQ 4294967294	Standard SATA AHCI Controller
IRQ 4294967290	Intel(R) Ethernet Connection (4) I219-V
IRQ 4294967289	Intel(R) Management Engine Interface
IRQ 4294967288	Intel(R) Dual Band Wireless-AC 8265
IRQ 4294967291	Intel(R) Iris(R) Plus Graphics 650
IRQ 4294967287	Intel(R) Smart Sound Technology (Intel(R) SST) Audio Controller
IRQ 4294967292	Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft)
IRQ 4294967293	Realtek PCIE CardReader

C. Watchdog Timer Configuration

The Watchdog Timer (WDT) is used to generate a variety of output signals after a user programmable count. The WDT is suitable for use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sorts of circumstances, the timer will count to zero and the selected outputs will be driven.

Under normal circumstance, you will need to restart the WDT at regular intervals before the timer counts to zero.

Sample Code:

```
//---------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//---------------------------------------------------------------------------
#include <dos.h>
#include <conio.h>
#include <stdio.h>
#include <stdlib.h>
#include "F81866.H"
//---------------------------------------------------------------------------
int main (int argc, char *argv[]);
void EnableWDT(int);
void DisableWDT(void);
//---------------------------------------------------------------------------
int main (int argc, char *argv[])
{
        unsigned char bBuf;
        unsigned char bTime;
        char **endptr;

        char SIO;

        printf("Fintek 81866 watch dog program\n");
        SIO = Init_F81866();
        if (SIO == 0)
        {
                printf("Can not detect Fintek 81866, program abort.\n");
                return(1);
        }//if (SIO == 0)

        if (argc != 2)
        {
                printf(" Parameter incorrect!!\n");
                return (1);
        }

        bTime = strtol (argv[1], endptr, 10);
        printf("System will reset after %d seconds\n", bTime);
```

```
        if (bTime)
        {  EnableWDT(bTime); }
        else
        {  DisableWDT();    }
        return 0;
}
//---------------------------------------------------------------------------
void EnableWDT(int interval)
{
        unsigned char bBuf;

        bBuf = Get_F81866_Reg(0x2B);
        bBuf &= (~0x20);
        Set_F81866_Reg(0x2B,     bBuf);     //Enable WDTO

        Set_F81866_LD(0x07);          //switch to logic device  7
        Set_F81866_Reg(0x30,     0x01);     //enable timer

        bBuf = Get_F81866_Reg(0xF5);
        bBuf &= (~0x0F);
        bBuf |= 0x52;
        Set_F81866_Reg(0xF5,     bBuf);     //count mode is second

        Set_F81866_Reg(0xF6,     interval);//set timer

        bBuf = Get_F81866_Reg(0xFA);
        bBuf |= 0x01;
        Set_F81866_Reg(0xFA,     bBuf);     //enable WDTO output

        bBuf = Get_F81866_Reg(0xF5);
        bBuf |= 0x20;
        Set_F81866_Reg(0xF5,     bBuf);     //start counting
}
//---------------------------------------------------------------------------
void DisableWDT(void)
{
        unsigned char bBuf;

        Set_F81866_LD(0x07);          //switch to logic device 7

        bBuf = Get_F81866_Reg(0xFA);
        bBuf &= ~0x01;
        Set_F81866_Reg(0xFA,     bBuf);     //disable WDTO output

        bBuf = Get_F81866_Reg(0xF5);
        bBuf &= ~0x20;
        bBuf |= 0x40;
        Set_F81866_Reg(0xF5,     bBuf);     //disable WDT
}
//---------------------------------------------------------------------------
```

```
//---------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//---------------------------------------------------------------------------
#include "F81866.H"
#include <dos.h>
//---------------------------------------------------------------------------
unsigned int F81866_BASE;
void Unlock_F81866 (void);
void Lock_F81866 (void);
//---------------------------------------------------------------------------
unsigned int Init_F81866(void)
{
        unsigned int result;
        unsigned char ucDid;

        F81866_BASE = 0x4E;
        result = F81866_BASE;

        ucDid = Get_F81866_Reg(0x20);
        if   (ucDid == 0x07)            //Fintek 81866
        {  goto     Init_Finish;     }

        F81866_BASE = 0x2E;
        result = F81866_BASE;

        ucDid = Get_F81866_Reg(0x20);
        if   (ucDid == 0x07)            //Fintek 81866
        {  goto     Init_Finish;     }

        F81866_BASE = 0x00;
        result = F81866_BASE;

Init_Finish:
        return (result);
}
//---------------------------------------------------------------------------
void Unlock_F81866 (void)
{
        outportb(F81866_INDEX_PORT, F81866_UNLOCK);
        outportb(F81866_INDEX_PORT, F81866_UNLOCK);
}
//---------------------------------------------------------------------------
void Lock_F81866 (void)
{
        outportb(F81866_INDEX_PORT, F81866_LOCK);
}
//---------------------------------------------------------------------------
void Set_F81866_LD( unsigned char LD)
{
        Unlock_F81866();
        outportb(F81866_INDEX_PORT, F81866_REG_LD);
        outportb(F81866_DATA_PORT, LD);
```

```
        Lock_F81866();
}
//---------------------------------------------------------------------------
void Set_F81866_Reg( unsigned char REG, unsigned char DATA)
{
        Unlock_F81866();
        outportb(F81866_INDEX_PORT, REG);
        outportb(F81866_DATA_PORT, DATA);
        Lock_F81866();
}
//---------------------------------------------------------------------------
unsigned char Get_F81866_Reg(unsigned char REG)
{
        unsigned char Result;
        Unlock_F81866();
        outportb(F81866_INDEX_PORT, REG);
        Result = inportb(F81866_DATA_PORT);
        Lock_F81866();
        return Result;
}
//---------------------------------------------------------------------------
```

```
//---------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//---------------------------------------------------------------------------
#ifndef     F81866_H
#define     F81866_H        1
//---------------------------------------------------------------------------
#define     F81866_INDEX_PORT       (F81866_BASE)
#define     F81866_DATA_PORT (F81866_BASE+1)
//---------------------------------------------------------------------------
#define     F81866_REG_LD       0x07
//---------------------------------------------------------------------------
#define     F81866_UNLOCK       0x87
#define     F81866_LOCK   0xAA
//---------------------------------------------------------------------------
unsigned int Init_F81866(void);
void Set_F81866_LD( unsigned char);
void Set_F81866_Reg( unsigned char,
unsigned char); unsigned char
Get_F81866_Reg( unsigned char);
//---------------------------------------------------------------------------
#endif  //    F81866_H
```

D. Onboard Connector Reference Types

Function	Onboard Type	Compatible Mating Type
Audio Connector	Hao Guo Xing Ye DF11-12S-PA66H	Hirose DF11-12DS-2C
SATA HDD Power Connector	E-call 0110-071-040	JST XHP-4
Front Panel Setting Connector	Dupont 2.54 mm-pitch pin header (Male)	Dupont 2.54 mm-pitch (Female)
USB 2.0 Connector	Hao Guo Xing Ye DF11-8S-PA66H	Hirose DF11-8DS-2C
Battery Connector	Molex 53047-0210	Molex 51021-0200
COM 2, COM3, COM4 RS-232 Ports	Hao Guo Xing Ye DF11-10S-PA66H	Hirose DF11-10DS-2C
DC Power Input Connector	Hao Guo Xing Ye WAFER396-2S-WV	JST VHR-2N
Digital I/O Connector	Dupont 2.00 mm-pitch pin header (Male)	Dupont 2.00 mm-pitch (Female)
LCD Backlight Connector	E-call 0110-161-040	JST PHR-4.
LVDS Connectors	Hirose DF20G-20DP-1V	Hirose DF20A-20DS-1C

```
 ------------------------
void Unlock_F81964 (void)
{
        outportb(F81964_INDEX_PORT, F81964_UNLOCK);
        outportb(F81964_INDEX_PORT, F81964_UNLOCK);
}
//-----------------------------------------------------------------------------
void Lock_F81964 (void)
{
        outportb(F81964_INDEX_PORT, F81964_LOCK);
}
//-----------------------------------------------------------------------------
void Set_F81964_LD( unsigned char LD)
{
        Unlock_F81964();
        outportb(F81964_INDEX_PORT, F81964_REG_LD);
        outportb(F81964_DATA_PORT, LD);
        Lock_F81964();
}
//-----------------------------------------------------------------------------
void Set_F81964_Reg( unsigned char REG, unsigned char DATA)
```

```
{
        Unlock_F81964();
        outportb(F81964_INDEX_PORT, REG);
        outportb(F81964_DATA_PORT, DATA);
        Lock_F81964();
}
//-----------------------------------------------------------------------------
unsigned char Get_F81964_Reg(unsigned char REG)
{
        unsigned char Result;
        Unlock_F81964();
        outportb(F81964_INDEX_PORT, REG);
        Result = inportb(F81964_DATA_PORT);
        Lock_F81964();
        return Result;
}
//-----------------------------------------------------------------------------

//-----------------------------------------------------------------------------
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A
PARTICULAR
// PURPOSE.
//
//-----------------------------------------------------------------------------
#ifndef     F81964_H
#define     F81964_H            1
//-----------------------------------------------------------------------------
#define   F81964_INDEX_PORT     (F81964_BASE)
#define   F81964_DATA_PORT      (F81964_BASE+1)
//-----------------------------------------------------------------------------
#define   F81964_REG_LD     0x07
//-----------------------------------------------------------------------------
#define   F81964_UNLOCK     0x87
#define   F81964_LOCK  0xAA
//-----------------------------------------------------------------------------
unsigned int Init_F81964(void);
void Set_F81964_LD( unsigned char);
void Set_F81964_Reg( unsigned char,
unsigned char); unsigned char
Get_F81964_Reg( unsigned char);
//-----------------------------------------------------------------------------
#endif  //    F81964_H
```