

# ESM-ARL

COM Express Rev. 3.1 COMe Type 6 Basic Module with  
Intel® Core™ Ultra Processor U/H Series (TDP 28W/15W)

## User's Manual

1<sup>st</sup> Ed – 10 December 2025

### Copyright Notice

Copyright © 2025 Avalue Technology Inc., ALL RIGHTS RESERVED.

## Document Amendment History

Revision	Date	By	Comment
1 <sup>st</sup>	December 2025	Avalue	Initial Release

## Declaration of Conformity



This device complies with part 15 FCC rules. Operation is subject to the following two conditions:

(1) This device may not cause harmful interference.

(2) This device must accept any interference received including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a class "a" digital device, pursuant to part 15 of the FCC rules.

These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications.

Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

## CE statement

The product(s) described in this manual complies with all application European Union (CE) directives if it has a CE marking. For computer systems to remain CE compliant, only CE-compliant parts may be used. Maintaining CE compliance also requires proper cable and cabling techniques.

## Notice

This guide is designed for experienced users to setup the system within the shortest time. For detailed information, please always refer to the electronic user's manual.

## Copyright Notice

© 2025 by Avalue Technology Inc. All rights are reserved. No parts of this manual may be copied, modified, or reproduced in any form or by any means for commercial use without the prior written permission of Avalue Technology Inc. All information and specification provided in this manual are for reference only and remain subject to change without prior notice.

## Acknowledgements

Intel and Pentium are trademarks of Intel Corporation.

Microsoft Windows is registered trademark of Microsoft Corp.

All other product names or trademarks are properties of their respective owners.

## Disclaimer

This manual is intended to be used as a practical and informative guide only and is subject to change without notice. It does not represent a commitment on the part of Avalue. This

## **ESM-ARL User's Manual**

product might include unintentional technical or typographical errors. Changes are periodically made to the information herein to correct such errors, and these changes are incorporated into new editions of the publication.

### **A Message to the Customer**

#### ***Avalue Customer Services***

Each and every Avalue's product is built to the most exacting specifications to ensure reliable performance in the harsh and demanding conditions typical of industrial environments. Whether your new Avalue device is destined for the laboratory or the factory floor, you can be assured that your product will provide the reliability and ease of operation for which the name Avalue has come to be known.

Your satisfaction is our primary concern. Here is a guide to Avalue's customer services. To ensure you get the full benefit of our services, please follow the instructions below carefully.

#### ***Technical Support and Assistance***

1. Visit the Avalue website at <https://www.alue.com/> where you can find the latest information about the product.
2. Contact your distributor or our technical support team or sales representative for technical support if you need additional assistance. Please have following information ready before you call:
  - Product name and serial number
  - Description of your peripheral attachments
  - Description of your software (operating system, version, application software, etc.)
  - A complete description of the problem
  - The exact wording of any error messages

To receive the latest version of the user's manual; please visit our Web site at:

[www.alue.com](http://www.alue.com)

## **Product Warranty (Returns & Warranties policy)**

### **1. Purpose**

Avalue establishes the following maintenance specifications and operation procedures for providing the best quality of service and shortened repair time to our customers.

### **2. Warranty**

#### **2.1 Warranty Period**

Avalue endeavors to offer customers the most comprehensive post-sales services and protection; besides offering a 2-year warranty for standard Avalue products, an extended warranty service can also be provided based on additional request from the customer. Within the warranty period, customers are entitled to receive comprehensive and prompt repair and warranty.

Standard products manufactured by Avalue are offered a 2-year warranty, from the date of delivery from Avalue. For ODM/OEM products manufactured by Avalue or PCBA with conformal coating, will follow up the define warranty of the agreement, otherwise will be offered 1-year warranty for ODM/OEM products but non-warranty for PCBA with conformal coating. For outsourcing parts kit by Avalue (ex: Motherboard, LCD touch panel, CPU, RAM, HDD) are offered a 6-month warranty, and Mobile/Tablet PC battery are offered a warranty of the half year, from the date of delivery by Avalue. Products before the mass production stage, i.e. engineering samples are not applied in this warranty or service policy. For extended warranty and cross-territory services, product defects resulting from design, production process or material are covered by the pre-set warranty period after the date of delivery from Avalue. For non-Avalue products, the product warranty and repair time shall be based on the service standards provided by the original manufacturer; in principle Avalue will provide these products a warranty service for no more than one year.

#### **2.2 Maintenance services within the warranty period**

In the case of Avalue product DOA (Defect-on-Arrival) when the customer finds any defect within 1 month after the delivery, Avalue will replace it with a new product in a soonest way. Except for custom products, once the customer is approved of a Cross-Shipment Agreement, which allows for delivery a new product to the customer before receiving the defective one, Avalue will immediately proceed with new product replacement for the said DOA case. On validation of the confirmed defect, Avalue is entitled to reserve the right whether to provide a new product for replacement. For the returned defective new product, it is necessary to verify that there shall be no bruise, alteration, scratch or marking to the appearance, and that none of the delivered accessories missing; otherwise, the customer will be requested to pay a processing fee. On the other hand, if the new product defect is resulting from incorrect configuration or erroneous use by the user instead of any problem of the hardware itself, the customer will also be requested to pay for relevant handling fees.

## **ESM-ARL User's Manual**

As for other conditions, Avalue will handle defects by way of repair. The customer will be requested to send the defective product to an Avalue authorized service center, and Avalue will return the repaired product back to the customer as soon as possible.

### **2.3 Ruling of an out-of-warranty defect**

The following situations are not included in the warranty:

- The warranty period has expired.
- Product has been altered or its label of the serial number has been torn off.
- Product functionality issues resulting from improper use by the user, unauthorized dismantle or alteration, unfit operation environment, improper maintenance, accident or other causes. Avalue reserves the right for the ruling of the aforementioned situations.
- Product damage resulting from lightning, flood, earthquake or other calamities.
- The warranty rules of non-Avalue products and accessories shall be in accordance with standards set up by the original manufacturer. These products and accessories include RAM, HDD, FDD, CD-ROM, CPU, FAN, etc.
- Product upgrade request or test request submitted by the customer after expiration of the warranty.
- PCBA with conformal coating.
- Avalue semi-product and outsourced products without Avalue serial number.
- Products before the mass production stage, i.e. engineering samples.

## **3. Procedure for sending for repair**

### **3.1 Attain a RMA number**

A customer's rejected product returned for repair shall have a RMA (Return Merchandise Authorization) number. Without a RMA number, Avalue will not provide any repair service for the rejected product, and the product will be returned to the customer at customer's cost. Avalue will not issue any notice for the return of the product.

Each returned product for repair shall have a RMA number, which is simply the authorization of the return for repair; it is not a guarantee that the returned goods can be repaired or replaced. For applying for a RMA number, the customer may enter the eRMA webpage of Avalue <https://www.alue.com/en/member> and log-in with an account number and a password authorized by Avalue. The system will then automatically issue a RMA number.

When applying for the RMA number, it is essential to fill in basic information of the customer and the product, together with detailed description of the problem encountered. If possible, avoid using ambiguous words such as "does not work" or "problematic". Without a substantial description of the problem, it is hard to start the repair and will cause prolonged repair time. Lacking detailed statement of fault steps also makes the problem hard to be identified, sometimes resulting in second-time repairs.

In case the customer can't define the cause of problem, please contact Avalue application engineers. Sometimes when the problem can be resolved even before the customer sends back the product.

On the other hand, if the customer only returns the key parts to Avalue for repair, it is necessary that the serial number of the entire unit is given in the "Problem Description" field, so that warranty period can be ruled accordingly; or Avalue will handle the case as an Out-of-warranty case.

### **3.2 Return of faulty product for repair**

It is recommended that the customer not to return the accessories (manual, connection cables, etc.) with the products for repair, devices such as CPU, DRAM, CF memory card, etc., shall also be removed from the faulty goods before return for repair. If these devices are relevant to described repair problems and necessary to be returned with the goods; please clearly indicate the items included in the eRMA application form. Avalue shall not be responsible for any item that is not itemized. Moreover, make sure the problem(s) are detailed in the "Problem Description" field.

In the list of delivery, the customer may fill-in a value which is lower than the actual value, to prevent customs levying a higher tax over the excessive value of the return goods. The customer shall be held responsible for extra fees caused by this. We strongly recommend that "Invoice for customs purpose only with no commercial value" be indicated on the delivery note. Also for the purpose of expedited handling, please printout the RMA number and put it in the carton, also indicate the number outside of the carton, with the recipient addressing to Avalue RMA Department.

When returning the defective product, please use an anti-static bag or ESD material to pack it properly. In case of improper packing resulting in damages in the transportation process, Avalue reserves the right to reject the un-repaired faulty good at the customer's costs.

Furthermore, it is suggested that the faulty goods shall be sent via a door-to-door courier service. The customer shall be held responsible for any customs clearance fee or extra expenses if Air-Cargo is used for the delivery.

In case of a DOA situation of a new product, Avalue will be responsible for the product and the freight. If the faulty goods are within the warranty period, the sender will take responsibility for the freight. For an out-of-warranty case, the customer shall be responsible for the freight of both trips.

### **3.3 Maintenance Charge**

Avalue will charge a moderate repair fee for the following conditions:

- The warranty period has expired.
- Product has been altered or its label of the serial number has been torn off.
- Product functionality issues resulting from improper use by the user, unauthorized dismantle or alteration, unfit operation environment, improper maintenance, accident

## **ESM-ARL User's Manual**

or other causes. Avalue reserves the right for the ruling of the aforementioned situations.

- Product damage resulting from lightning, flood, earthquake or other calamities.
- The warranty rules for non-Avalue products and accessories shall be in accordance with standards set up by the original supplier. These products and accessories include RAM, HDD, FDD, CD-ROM, CPU, FAN, etc.
- Product upgrade request or test request submitted by the customer after expiry of the warranty.
- PCBA with conformal coating.
- Avalue semi-product and outsourced products without Avalue serial number
- Products before the mass production stage, i.e. engineering samples.
- In case the products received are examined as NPF (No Problem Found) within the warranty period, the customer shall be responsible for the freight of both trips.
- Please contact your local distributor to examine in advance to prevent unnecessary freight cost.

For system failure of out-of-warranty products, Avalue will provide a quotation prior to repair service. When the customer applies for the cost, please refer to the Quotation number. In case the customer does not return the DOA product that has already been replaced by a new one, or the customer does not sign back the quotation of the out-of-warranty maintenance, Avalue reserves the right of whether or not to provide the repair service. In case the customer does not reply in 3 months, Avalue shall directly scrap or return the product back to customer at customer's cost without further notice to the customer.

### **3.4 Maintenance service of phased-out products**

For servicing phased-out products, Avalue provides an extended period, starting the date of phase-out, as a guaranteed maintenance period of such products, for continuance of the maintenance service to meet customer's requirements. In case of unexpected factors causing Avalue to be unable to repair/replace a warranted but phased-out product, Avalue will, depending on the availability, upgrade the product (free of charge with continued warranty period as of the original product), or, give partial refund (based on the length of the remaining warranty period) to solve this kind of problem.

### **3.5 Maintenance Report**

On completion of repair of a defective product, a Maintenance Report indicating the maintenance result and part(s) replaced (if any) will be sent to the customer together with the product. If the customer demands an additional maintenance analysis report, a service fee of various level will be charged depending on the warranty status. In case the analysis result shows that the defect attributes to Avalue's faulty design or process, the analysis fee will be exempted.

## **4. Service Products**

Avalue provides service products to manage with different customer needs. Should you have any need, please consult to Avalue Sales Department.

### **Defect Analysis Report (DAR)**

Avalue provides DAR (Defect Analysis Report) services aiming to elevating customer satisfaction. A DAR includes defect cause identification/verification/suggestion and improvement precautions, with instructions on correct usage for the avoidance of any reoccurrence.

### **Upgrade Service**

Avalue is capable to provide system upgrade service for customization requirements. This upgrade service is applicable for main parts, such as CPU, memory, HDD, SSD, storage devices; also replacements motherboards of systems. Please contact Avalue sales for details to evaluate the possibility of system upgrade service and obtain information of lead time and price.

## **Safety Instructions**

### **Safety Precautions**

Before installing and using this device, please note the following precautions.

1. Read these safety instructions carefully.
2. Keep this User's Manual for future reference.
3. Disconnected this equipment from any AC outlet before cleaning.
4. For plug-in equipment, the power outlet socket must be located near the equipment and must be easily accessible.
5. Keep this equipment away from humidity.
6. Put this equipment on a reliable surface during installation. Dropping it or letting it fall may cause damage.
7. Make sure the voltage of the power source is correct before connecting the equipment to the power outlet.
8. Use a power cord that has been approved for using with the product and that it matches the voltage and current marked on the product's electrical range label. The voltage and current rating of the cord must be greater than the voltage and current rating marked on the product.
9. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
10. All cautions and warnings on the equipment should be noted.
11. If the equipment is not used for a long time, disconnect it from the power source to

## **ESM-ARL User's Manual**

avoid damage by transient overvoltage.

12. Never pour any liquid into an opening. This may cause fire or electrical shock.  
13. Never open the equipment. For safety reasons, the equipment should be opened only by qualified service personnel. If one of the following situations arises, get the equipment checked by service personnel:

- The power cord or plug is damaged.
- Liquid has penetrated into the equipment.
- The equipment has been exposed to moisture.
- The equipment does not work well, or you cannot get it work according to the user's manual.
- The equipment has been dropped and damaged.
- The equipment has obvious signs of breakage.

14. CAUTION: Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer.

15. Equipment intended only for use in a RESTRICTED ACCESS AREA.

## Explanation of Graphical Symbols

	Warning	A WARNING statement provides important information about a potentially hazardous situation which, if not avoided, could result in death or serious injury.
	Caution	A CAUTION statement provides important information about a potentially hazardous situation which, if not avoided, may result in minor or moderate injury to the user or patient or in damage to the equipment or other property.
	Note	A NOTE provides additional information intended to avoid inconveniences during operation.
		Direct current.
		Alternating current
		Stand-by, Power on
		FCC Certification
		CE Certification
		Follow the national requirements for disposal of equipment.
		Stacking layer limit
		This side up

## ESM-ARL User's Manual

		Fragile Packaging
		Beware of water damage, moisture-proof
		Carton recyclable
		Handle with care
		Follow operating instructions or consult instructions for use.
		<p><b>WARNING</b></p> <ul style="list-style-type: none"> <li>• <b>INGESTION HAZARD:</b> This product contains a button cell or coin battery.</li> <li>• <b>DEATH</b> or serious injury can occur if ingested.</li> <li>• A swallowed button cell or coin battery can cause <b>Internal Chemical Burns</b> in as little as <b>2 hours</b>.</li> <li>• <b>KEEP</b> new and used batteries <b>OUT OF REACH</b> of <b>CHILDREN</b>.</li> <li>• <b>Seek immediate medical attention</b> if a battery is suspected to be swallowed or inserted inside any part of the body.</li> </ul>

## Disposing of your old product

### **WARNING:**

There is danger of explosion if the battery is mishandled or incorrectly replaced. Replace only with the same type of battery. Do not disassemble it or attempt to recharge it outside the system. Do not crush, puncture, dispose of in fire, short the external contacts, or expose to water or other liquids. Dispose of the battery in accordance with local regulations and instructions from your service provider.

### **CAUTION:**

- Lithium Battery Caution: Danger of explosion if battery is incorrectly replaced. Replace only with same or equivalent type. Dispose batteries according to manufacturer's instructions.
- Disposal of a BATTERY into fire or a hot oven, or mechanically crushing or cutting of a BATTERY, that can result in an EXPLOSION
- Leaving a BATTERY in an extremely high temperature surrounding environment that can result in an EXPLOSION or the leakage of flammable liquid or gas.
- A BATTERY subjected to extremely low air pressure that may result in an EXPLOSION or the leakage of flammable liquid or gas.

### **Mise en garde!**

AVERTISSEMENT : Il existe un risque d'explosion si la batterie est mal manipulée ou remplacée de manière incorrecte. Remplacez uniquement par le même type de batterie. Ne le démontez pas et ne tentez pas de le recharger en dehors du système. Ne pas écraser, percer, jeter au feu, court-circuiter les contacts externes ou exposer à l'eau ou à d'autres liquides. Jetez la batterie conformément aux réglementations locales et aux instructions de votre fournisseur de services.

### **MISE EN GARDE:**

- Pile au lithium Attention : Danger d'explosion si la pile n'est pas remplacée correctement. Remplacer uniquement par un type identique ou équivalent. Jetez les piles conformément aux instructions du fabricant.
- L'élimination d'une BATTERIE dans le feu ou dans un four chaud, ou l'écrasement ou le découpage mécanique d'une BATTERIE, pouvant entraîner une EXPLOSION
- Laisser une BATTERIE dans un environnement à température extrêmement élevée pouvant entraîner une EXPLOSION ou une fuite de liquide ou de gaz inflammable.
- UNE BATTERIE soumise à une pression d'air extrêmement basse pouvant entraîner une EXPLOSION ou une fuite de liquide ou de gaz inflammable.

# Content

<b>1. Getting Started.....</b>	<b>17</b>
1.1 Safety Precautions .....	17
1.2 Packing List.....	17
1.3 Manual Objectives.....	18
1.4 System Specifications .....	19
1.5 Architecture Overview—Block Diagram .....	24
<b>2. Hardware Configuration.....</b>	<b>25</b>
2.1 Product Overview.....	26
2.2 Jumper and Connector List .....	27
2.3 Setting Jumpers & Connectors .....	28
2.3.1 AT/ATX mode selector (SW1).....	28
2.3.1.1 Signal Description –AT/ATX mode selection.....	28
2.3.2 BIOS SPI programming connector (BIOS_SPI1).....	29
2.3.3 Debug connector (DEBUG_1).....	29
2.3.4 COM Express Connector 1 (CN1A) .....	30
2.3.4.1 Signal Description – COM Express Connector 1 (CN1A).....	34
2.3.5 COM Express Connector 2 (CN1B) .....	37
2.3.5.1 Signal Description – COM Express Connector 2 (CN1B).....	41
2.4 Installing Heatsink / Cooler .....	43
<b>3. Drivers Installation.....</b>	<b>45</b>
3.1 Install Chipset Driver .....	46
3.2 Install VGA Driver.....	47
3.3 Install Ethernet Driver.....	48
3.4 Install ME Driver.....	49
3.5 Install Serial IO Driver .....	50
3.6 Install IPMT Driver.....	51
3.7 Install ISST Driver .....	52
3.8 Install IRST Driver .....	53
3.9 Install NPU Driver.....	54
3.10 Install Audio Driver .....	55
<b>4.BIOS Setup .....</b>	<b>56</b>
4.1 Introduction .....	57
4.2 Starting Setup .....	57
4.3 Using Setup .....	58
4.4 Getting Help .....	59
4.5 In Case of Problems.....	59
4.6 BIOS setup.....	60

4.6.1	Main Menu.....	60
4.6.1.1	System Language.....	61
4.6.1.2	System Date .....	61
4.6.1.3	System Time.....	61
4.6.2	Advanced Menu .....	62
4.6.2.1	CPU Configuration.....	62
4.6.2.1.1	Efficient-core Information.....	63
4.6.2.1.2	Performance-core Information.....	64
4.6.2.2	Power & Performance .....	64
4.6.2.2.1	CPU – Power Management Control .....	65
4.6.2.3	System Agent (SA) Configuration.....	66
4.6.2.3.1	Memory Configuration .....	66
4.6.2.3.2	Graphics Configuration.....	67
4.6.2.3.3	TCSS setup menu .....	67
4.6.2.3.4	VMD setup menu .....	70
4.6.2.3.5	VT-d setup menu .....	71
4.6.2.4	PCIE Configuration.....	71
4.6.2.4.1	PCIe Root Port PXPA3(PCIE4_2).....	72
4.6.2.4.2	PCIe Root Port PXPA4(i226).....	73
4.6.2.4.3	PCIe Root Port PXPB1(PCIE4_1).....	74
4.6.2.4.4	PCIe Root Port PXPB2.....	75
4.6.2.4.5	PCIe Root Port PXPB3.....	76
4.6.2.4.6	PCIe Root Port PXPB4 .....	77
4.6.2.4.7	PCIe Root Port PXPC(PCIE16_1.1-4) .....	78
4.6.2.4.8	PCIe Root Port PXPD(PCIE16_1.9-12) .....	79
4.6.2.4.9	PCIe Root Port PXPE(PCIE16_1.13-16) .....	80
4.6.2.5	PCH-IO Configuration.....	81
4.6.2.5.1	SATA Configuration .....	81
4.6.2.5.2	USB Configuration .....	82
4.6.2.5.3	HD Audio Configuration .....	83
4.6.2.5.4	SerialIO Configuration .....	83
4.6.2.6	PCH-FW Configuration.....	84
4.6.2.6.1	Firmware Update Configuration.....	85
4.6.2.7	Trusted Computing .....	85
4.6.2.8	APCI Settings .....	86
4.6.2.9	Board & Panel Configuration .....	86
4.6.2.10	IT5782 Super IO Configuration .....	88
4.6.2.10.1	Serial Port 1 Configuration .....	89
4.6.2.10.2	Serial Port 2 Configuration .....	89
4.6.2.11	EC 5782 HW monitor.....	90

## ESM-ARL User's Manual

4.6.2.12	S5 RTC Wake Settings.....	90
4.6.2.13	Serial Port Console Redirection .....	92
4.6.2.13.1	COM0 .....	93
4.6.2.13.2	Console Redirection EMS .....	94
4.6.2.14	USB Configuration .....	95
4.6.2.15	Network Stack Configuration .....	96
4.6.2.16	NVMe Configuration .....	97
4.6.2.16.1	PCIe SSD .....	98
4.6.3	Security.....	99
4.6.3.1	Secure Boot.....	99
4.6.4	Boot .....	100
4.6.5	Save and Exit .....	101
4.6.5.1	Save Changes and Reset.....	101
4.6.5.2	Discard Changes and Reset.....	101
4.6.5.3	Restore Defaults.....	101
4.6.5.4	Launch EFI Shell from filesystem device .....	101
4.6.5.5	Expert mode [DQV mode] .....	101
<b>5.</b>	<b>Mechanical Drawing .....</b>	<b>102</b>

# 1. Getting Started

## 1.1 Safety Precautions

### Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

### Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

## 1.2 Packing List

Before installation, please ensure all the items listed in the following table are included in the package.

Item	Description	Q'ty
1	ESM-ARL COMe Module	1
2	Desiccant (5g)	1



If any of the above items is damaged or missing, contact your retailer.

### **1.3 Manual Objectives**

This manual describes in details Avalue Technology ESM-ARL Single Board.

We have tried to include as much information as possible but we have not duplicated information that is provided in the standard IBM Technical References, unless it proved to be necessary to aid in the understanding of this board.

We strongly recommend that you study this manual carefully before attempting to set up ESM-ARL or change the standard configurations. Whilst all the necessary information is available in this manual we would recommend that unless you are confident, you contact your supplier for guidance.

Please be aware that it is possible to create configurations within the CMOS RAM that make booting impossible. If this should happen, clear the CMOS settings, (see the description of the Jumper Settings for details).

If you have any suggestions or find any errors regarding this manual and want to inform us of these, please contact our Customer Service department with the relevant details.

## 1.4 System Specifications

System		
<b>CPU</b>	Intel® Core™ Ultra Processor U/H Series (TDP 28W/15W), Code name: Arrow Lake Intel® Core™ Ultra7 Processor 255H Intel® Core™ Ultra5 Processor 225H Intel® Core™ Ultra7 Processor 255U Intel® Core™ Ultra5 Processor 225U	
<b>BIOS</b>	AMI 256Mb UEFI BIOS, 256Mbit SPI Flash ROM	
<b>I/O Chip</b>	EC ITC IT5782VG-I-128/BX	
<b>System Memory</b>	Dual-CH 2 x 262-pin DDR5 6400/5600MHz SO-DIMM up to 96GB	
<b>Watchdog Timer</b>	65536 level, 0 ~ 65535 sec	
<b>H/W Status Monitor</b>	Monitoring CPU Temperature, Voltage with Auto Throttling Control	
<b>VMD (RAID)</b>	Yes Intel® Volume Management Device (VMD)	
<b>TPM</b>	Onboard NuvoTon NPCT760AABYX support TPM 2.0, co-lay Infineon SLB9670VQ2.0 FW7.85	
<b>iAMT</b>	Support iAMT 12.0 (selected by CPU SKU)	
Storage		
<b>NVMe</b>	1x NVMe (Option) NVMe SSD in place of PEG lanes 18-21	
I/O Interface (SOM)		
<b>COM Express Rev.</b>	COM Express Rev. 3.1	
<b>HSIO Multiplexing Details</b>	BOM 1 U-Series	BOM 2 H-Series
<b>USB</b>	8 x USB2.0 4 x USB3.2 Gen 2 x1	8 x USB2.0 4 x USB3.2 Gen 2 x1
<b>SATA</b>	2 x SATA III	2 x SATA III
<b>LAN</b>	1 x i226	1 x i226
<b>PCIe</b>	1 x PCIe x1 Gen4, 1 x PCIe x4 Gen 4 (configurable x1, x2, or x4)	1 x PCIe x1 Gen4, 1 x PCIe x4 Gen 4 (configurable x1, x2, or x4)
<b>PEG</b>	PEG: 3 x PCIe x4 Gen4 Note: 1. NVMe share with 1 x PCIe x4 PEG [3:0] from SoC	PEG: 1 x PCIe x8 Gen4, 2 x PCIe x4 Gen4 Note: NVMe share with 1 x PCIe x4
	It is recommended to use Discrete Graphics or Storage Devices on PEG PCIe	

## ESM-ARL User's Manual

	(came from IOE).																				
<b>UART</b>	2 x UART (2-wire, only TX/RX)																				
<b>USB</b>	USB4: Up to 2 ports, Co-lay with DDI and in place of DDI1 & DDI2 USB3.2 (Gen2): 4 Ports (10 Gbps) USB2.0: 8 Ports (480 Mbps)																				
<b>SATA</b>	2 x SATAIII (6.0Gb/s)																				
<b>System Memory</b>	Dual-CH 2 x 262-pin DDR5 6400/5600MHz SO-DIMM up to 96GB																				
<b>NVMe</b>	1x NVMe (Option) NVMe SSD share with 1 x PCIe x4 and in place of PEG lanes 18-21																				
<b>DIO</b>	8 bit GPIO from EC																				
<b>eSPI/LPC</b>	1 x LPC (via eSPI-to-LPC bridge IC ITE_IT8883FN) (Default LPC)																				
<b>I<sup>2</sup>C Bus</b>	1 x I <sup>2</sup> C																				
<b>SMBus</b>	1 x SMBus																				
<b>TPM</b>	Onboard NuvoTon NPCT760AABYX support TPM 2.0, co-lay Infineon SLB9670VQ2.0 FW7.85																				
<b>SPI Bus</b>	1 x SPI																				
<b>Display</b>																					
<b>Graphic Chipset</b>	Intel® Xe LPG																				
<b>Spec. &amp; Resolution</b>	<u>DDI (HDMI/Display Port):</u> Up to 3 x DDI Port Support Configurable HDMI/DisplayPort -HDMI 2.1: 4096 x 2304 @ 60Hz -DisplayPort 1.4 8K30 <u>LCD (LVDS/eDP):</u> 1 x LVDS (via CH7511B eDP-to-LVDS): 1920x1200 @60Hz (Default); or 1 x eDP 1.4b(By BOM Optional): Max 4K																				
<b>Multiple Display</b>	Quad Simultaneous Independent Display																				
<b>Audio</b>																					
<b>Audio Codec</b>	Intel® HD Audio integrated in SoC																				
<b>Ethernet</b>																					
<b>LAN Chipset</b>	1 x Intel® i226 2.5G Gigabit Controller																				
<b>LAN Spec.</b>	10/100/1000/2500 Base-Tx GbE compatible																				
<b>LED Indicator (Avalue standard design as the reference for customers' carrier board design.)</b>	<p><b>Max. 2.5G LAN Port</b></p> <table border="1"> <thead> <tr> <th colspan="2">ACT/LINK</th> <th colspan="2">SPEED</th> </tr> <tr> <th>LED</th> <th>Definition</th> <th>LED</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>Light Off</td> <td>No Link</td> <td>Solid Orange</td> <td>2.5G</td> </tr> <tr> <td>Solid Yellow</td> <td>Connection</td> <td>Solid Green</td> <td>1G/100M</td> </tr> <tr> <td>Yellow Flashing</td> <td>Activity</td> <td>Light Off</td> <td>10M</td> </tr> </tbody> </table>	ACT/LINK		SPEED		LED	Definition	LED	Definition	Light Off	No Link	Solid Orange	2.5G	Solid Yellow	Connection	Solid Green	1G/100M	Yellow Flashing	Activity	Light Off	10M
ACT/LINK		SPEED																			
LED	Definition	LED	Definition																		
Light Off	No Link	Solid Orange	2.5G																		
Solid Yellow	Connection	Solid Green	1G/100M																		
Yellow Flashing	Activity	Light Off	10M																		

Mechanical & Environmental	
<b>Power Requirement</b>	+9V ~ +19V
<b>ACPI</b>	Single power ATX Support S0, S3, S4, S5 ACPI 6.0 Compliant
<b>Power Mode</b>	AT/ATX
<b>Operating Temp.</b>	Standard 0°C ~ 60°C (32°F ~ 140°F)
<b>Storage Temp.</b>	-40°C ~ 75°C (-40°F ~ 167°F)
<b>Operating Humidity</b>	40°C @ 95% Relative Humidity, Non-condensing
<b>Size (L x W)</b>  (Please consult product engineers for the production feasibility if the size is larger than 410x360mm or smaller than 80x70mm)	4.92" x 3.74" (125*95 mm)
<b>Weight</b>	0.226lbs (0.101Kg) without NVMe 0.227lbs (0.103Kg) with NVMe
<b>Vibration Test</b>	<p>Package Vibration Test Reference IEC60068-2-64 Testing procedures Test Fh: Vibration broadband random Test</p> <ol style="list-style-type: none"> <li>1. PSD: 0.026G<sup>2</sup>/Hz, 2.16 Grms</li> <li>2. Non-operation mode</li> <li>3. Test Frequency: 5-500Hz</li> <li>4. Test Axis: X, Y and Z axis</li> <li>5. 30 min. per each axis</li> <li>6. IEC 60068-2-64 Test: Fh</li> </ol> <p>Random Vibration Operation Reference IEC60068-2-64 Testing procedures Test Fh: Vibration broadband random Test</p> <ol style="list-style-type: none"> <li>1. PSD: 0.00454G<sup>2</sup>/Hz, 1.5 Grms</li> <li>2. Operation mode</li> <li>3. Test Frequency: 5-500Hz</li> <li>4. Test Axis: X, Y and Z axis</li> <li>5. 30 minutes per each axis</li> <li>6. IEC 60068-2-64 Test: Fh</li> </ol> <p>Random Vibration Non Operation Reference IEC60068-2-64 Testing procedures Test Fh : Vibration broadband random Test</p>

## ESM-ARL User's Manual

	<ol style="list-style-type: none"><li>1. PSD: 0.01818G<sup>2</sup>/Hz, 3.0 Grms</li><li>2. Non-Operation mode</li><li>3. Test Frequency: 5-500Hz</li><li>4. Test Axis: X, Y and Z axis</li><li>5. 30 minutes per each axis</li><li>6. IEC 60068-2-64 Test: Fh</li></ol>
<b>Drop Test</b>	<p>Package Drop Reference ISTA 2A, Method : IEC-60068-2-32 Test: Ed</p> <p>Drop Test</p> <p>1 One corner, three edges, six faces</p> <p>2 ISTA 2A, IEC-60068-2-32 Test: Ed</p>
<b>OS Information</b>	Main test: Windows 11, Ubuntu 24.04



**Note:** Specifications are subject to change without notice.

IOE PCIE supports NVMe Graphic Devices  
IOE PCIE supports NVMe Graphic Devices  
IOE PCIE supports NVMe Graphic Devices

→ H:X16 [12:15]  
→ H:X16 [8:11]  
→ H:X16 [0:7]

IOE PCIE supports NVMe Graphic Devices  
IOE PCIE supports NVMe Graphic Devices  
SOC PCIE supports PCIE Devices

→ U:X16 [12:15]  
→ U:X16 [8:11]  
→ U:X16 [0:3]

SOC PCIE supports PCIE Devices

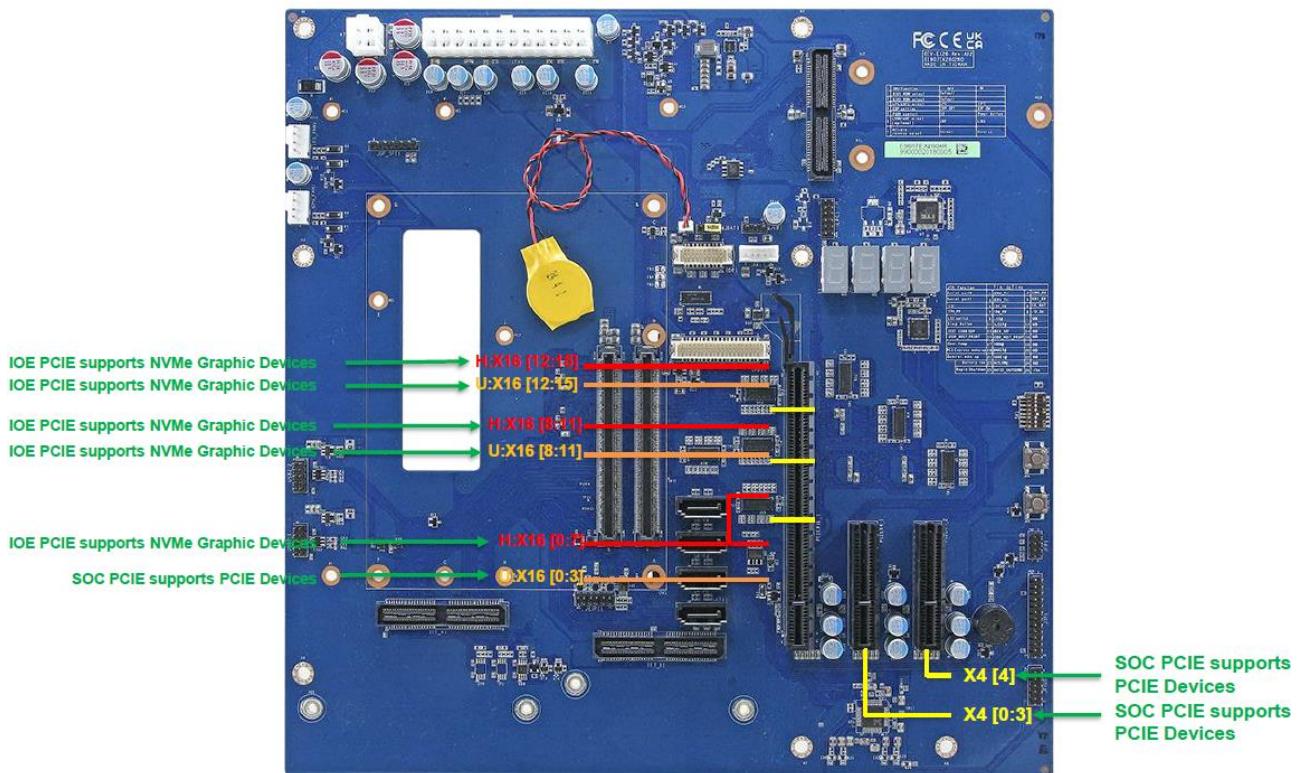
COME  
PEG  
SLOT  
COMIE  
PCIE  
SLOT  
[0:4]

SOC PCIE supports PCIE Devices

COME  
PEG  
SLOT  
COMIE  
PCIE  
SLOT  
[0:4]

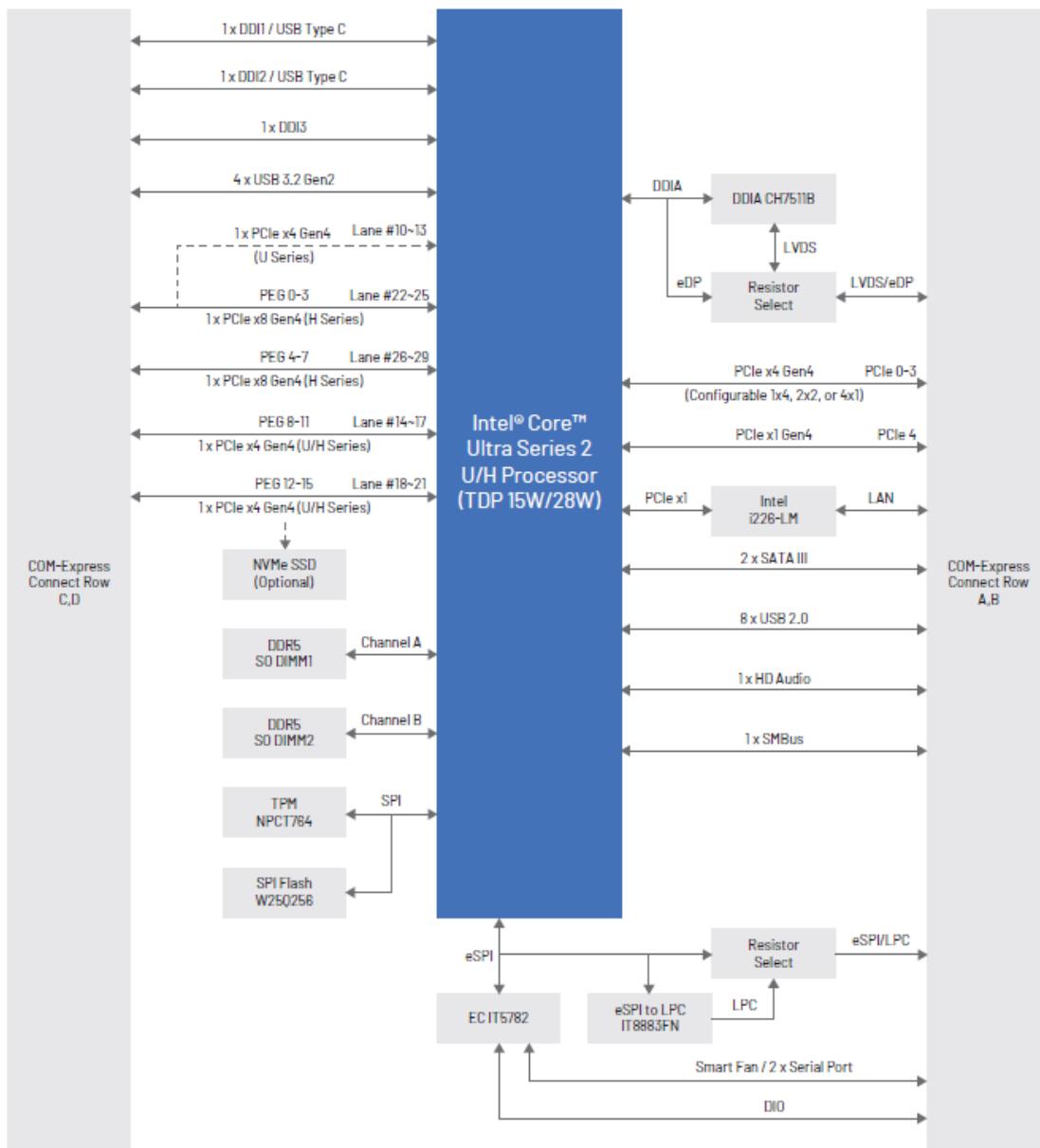
## H-Series CPU

## U-Series CPU

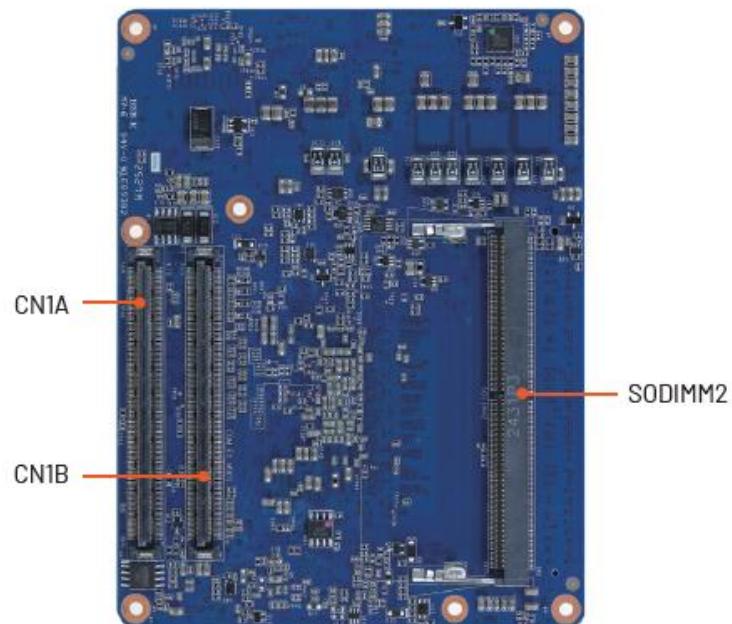
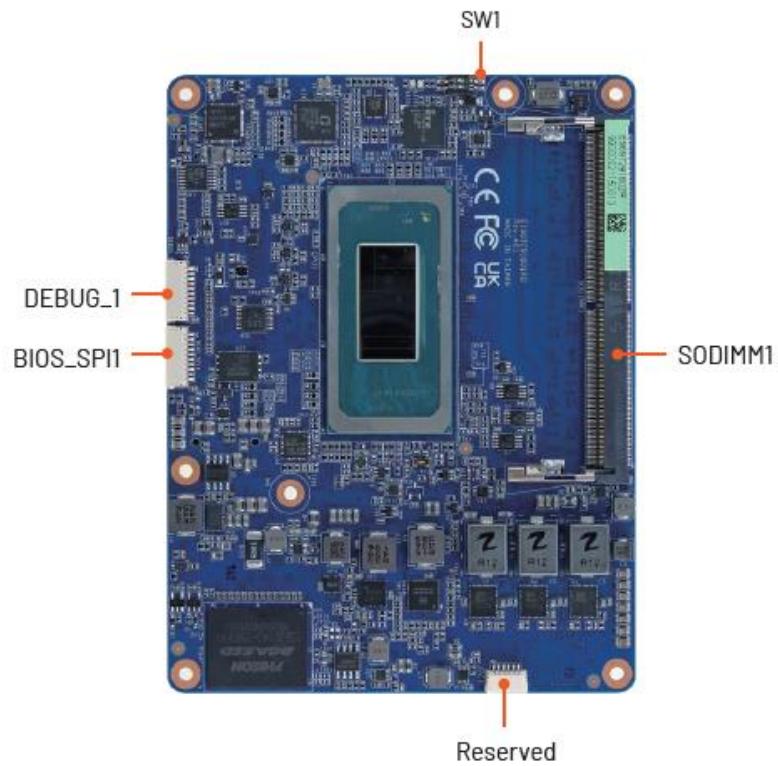


## 1.5 Architecture Overview—Block Diagram

The following block diagram shows the architecture and main components of ESM-ARL.



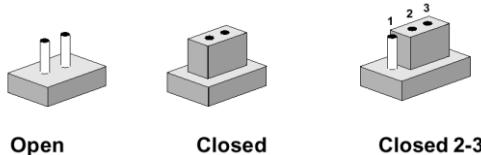
## 2. Hardware Configuration



## 2.2 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

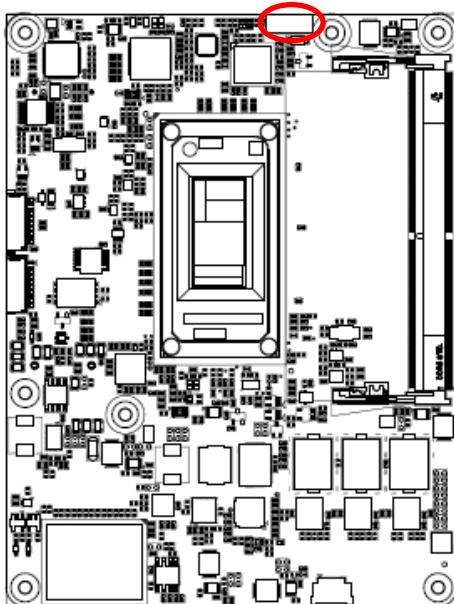
The following tables list the function of each of the board's jumpers and connectors.

### Connectors

Label	Function	Note
<b>BIOS_SPI1</b>	BIOS SPI programming connector	10 x 1 wafer, pitch 1.00mm
<b>DEBUG_1</b>	Debug connector	10 x 1 wafer, pitch 1.00mm
<b>CN1A</b>	COM Express connector 1	
<b>CN1B</b>	COM Express connector 2	
<b>SODIMM1/2</b>	262-pin DDR5 SDRAM DIMM socket	
<b>SW1</b>	AT/ATX mode selector	

## 2.3 Setting Jumpers & Connectors

### 2.3.1 AT/ATX mode selector (SW1)



AT/ATX mode



AT mode

OFF	1		→	ON
	2			

ATX mode\*

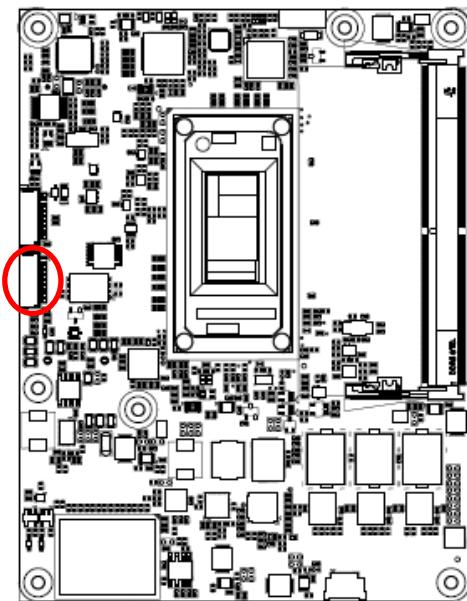
OFF	1	↔		ON
	2			

\*Default

#### 2.3.1.1 Signal Description –AT/ATX mode selection

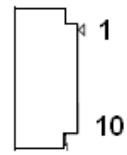
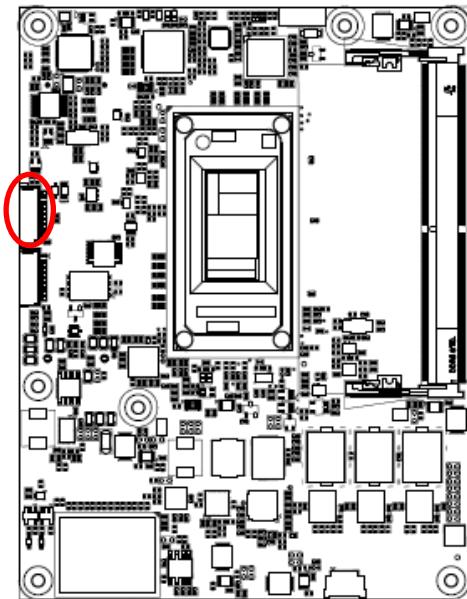
AT/ATX mode	Description
<b>AT mode</b> 	Auto power on, no need to press Power button to enable power on/off
<b>ATX mode</b> 	Press the ATX power button to enable power on/off

### 2.3.2 BIOS SPI programming connector (BIOS\_SPI1)



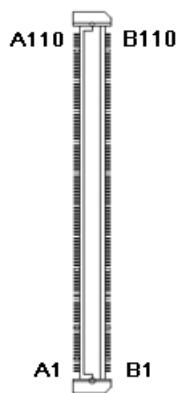
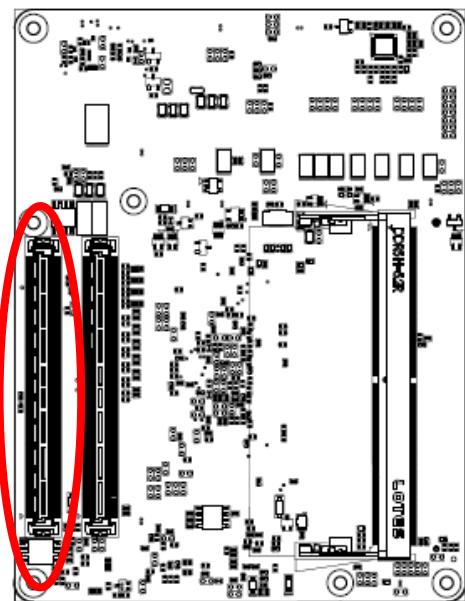
Signal	PIN
EC_SMDAT_DEBUG	1
EC_SMCLK_DEBUG	2
BIOS_WP#	3
BIOS_HOLD#	4
SPI0_BIOS_MOSI	5
SPI0_BIOS_MISO	6
SPI0_BIOS_CLK	7
ROM_CS#	8
GND	9
+1.8VSB	10

### 2.3.3 Debug connector (DEBUG\_1)

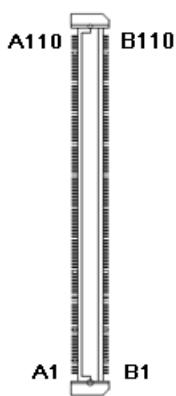
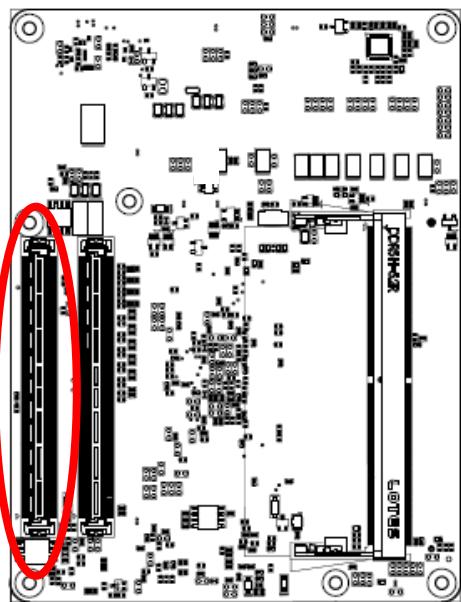


Signal	PIN
ESPI_IO0_COM	1
ESPI_IO1_COM	2
ESPI_IO2_COM	3
ESPI_IO3_COM	4
ESPI_RST#	5
ESPI_CLK_COM	6
ESPI_CS#0/1	7
PLT_BUF_RST#	8
GND	9
+3.3VSB	10

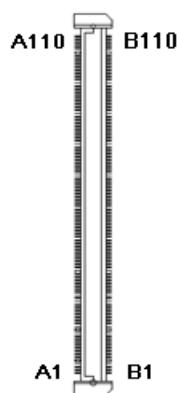
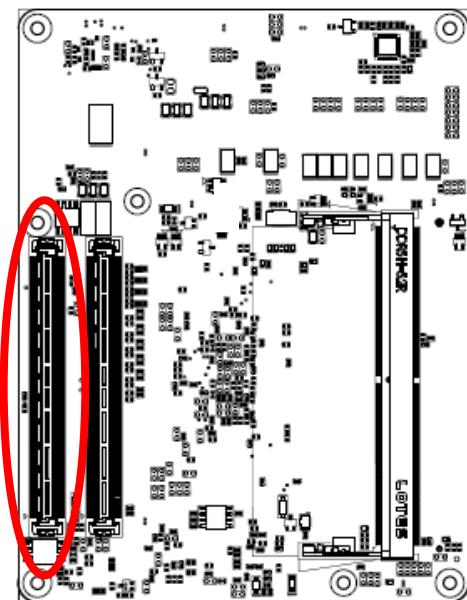
## 2.3.4 COM Express Connector 1 (CN1A)



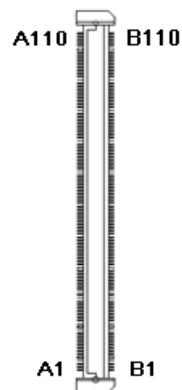
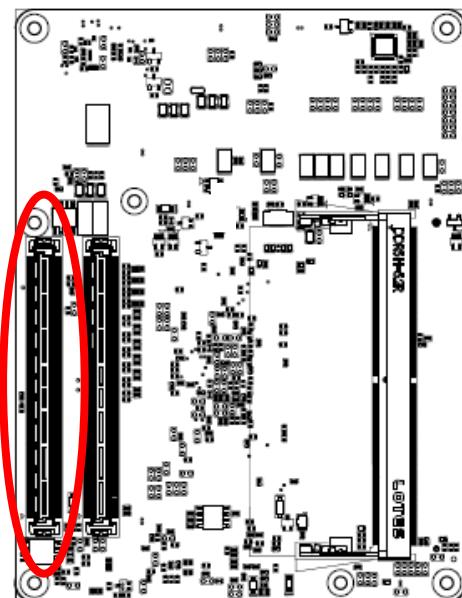
Signal	PIN	PIN	Signal
GND	A110	B110	GND
VCC_12V	A109	B109	VCC_12V
VCC_12V	A108	B108	VCC_12V
VCC_12V	A107	B107	VCC_12V
VCC_12V	A106	B106	VCC_12V
VCC_12V	A105	B105	VCC_12V
VCC_12V	A104	B104	VCC_12V
LID#	A103	B103	SLEEP#
SER1_RX	A102	B102	FAN_TACHIN
SER1_TX	A101	B101	FAN_PWMOUT
GND	A100	B100	GND
SER0_RX	A99	B99	GSPI1_CLK
SER0_TX	A98	B98	GSPI1_MISO
TYPE10#	A97	B97	SPI_CS#
TPM_PP	A96	B96	NC
SPI_MOSI	A95	B95	NC
SPI_CLK	A94	B94	NC
GPO0	A93	B93	NC
SPI_MISO	A92	B92	NC
SPI_POWER	A91	B91	NC
GND	A90	B90	GND
PCIE_CLK_REF-	A89	B89	NC
PCIE_CLK_REF+	A88	B88	BIOS_DIS1#
CB_EDP_HDP	A87	B87	+ATX5VSB
GSPI1_MOSI	A86	B86	+ATX5VSB
GPI3	A85	B85	+ATX5VSB
LVDS_I2C_DAT/EDP_AUX-	A84	B84	+ATX5VSB
LVDS_I2C_CK/EDP_AUX+	A83	B83	LVDS_BKLT_CTRL/ EDP_BKLT_CTRL
LVDS_A_CK-/EDP_TX3-	A82	B82	LVDS_B_CK-
LVDS_A_CK+/EDP_TX3+	A81	B81	LVDS_B_CK+



Signal	PIN	PIN	Signal
GND	A80	B80	GND
LVDS_A3-	A79	B79	LVDS_BKLT_EN/ EDP_BKLT_EN
LVDS_A3+	A78	B78	LVDS_B3-
LVDS_VDD_EN/EDP_VDD_EN	A77	B77	LVDS_B3+
LVDS_A2-/EDP_TX0-	A76	B76	LVDS_B2-
LVDS_A2+/EDP_TX0+	A75	B75	LVDS_B2+
LVDS_A1-/EDP_TX1-	A74	B74	LVDS_B1-
LVDS_A1+/EDP_TX1+	A73	B73	LVDS_B1+
LVDS_A0-/EDP_TX2-	A72	B72	LVDS_B0-
LVDS_A0+/EDP_TX2+	A71	B71	LVDS_B0+
GND	A70	B70	GND
PCIE_TX0-	A69	B69	PCIE_RX0-
PCIE_TX0+	A68	B68	PCIE_RX0+
GPI2	A67	B67	WAKE1#
GND	A66	B66	WAKE0#
PCIE_TX1-	A65	B65	PCIE_RX1-
PCIE_TX1+	A64	B64	PCIE_RX1+
GPI1	A63	B63	GPO3
PCIE_TX2-	A62	B62	PCIE_RX2-
PCIE_TX2+	A61	B61	PCIE_RX2+
GND	A60	B60	GND
PCIE_TX3-	A59	B59	PCIE_RX3-
PCIE_TX3+	A58	B58	PCIE_RX3+
GND	A57	B57	GPO2
PCIE_TX4-	A56	B56	PCIE_RX4-
PCIE_TX4+	A55	B55	PCIE_RX4+
GPI0	A54	B54	GPO1
NC	A53	B53	NC
NC	A52	B52	NC
GND	A51	B51	GND



Signal	PIN	PIN	Signal
LPC_SERIRQ/ ESPI_CS1#	A50	B50	CB_RESET#
GBE0_SD <sub>P</sub>	A49	B49	SYS_RESET#
RSMRST_OUT#	A48	B48	NC
+V3.3A_RTC	A47	B47	NC
USB0+ USB1+	A46	B46	USB1-
USB0- USB1-	A45	B45	USB2+
USB_2_3_OC# USB_0_1_OC#	A44	B44	USB3+
USB2- USB3-	A43	B43	USB2-
GND	A41	B41	USB4+
USB4- USB5+	A40	B40	USB5-
USB_6_7_OC# USB_4_5_OC#	A38	B38	USB6+
USB6- USB7-	A37	B37	USB7+
THRMTRIP#	A35	B35	THRM#
BIOS_DIS0#/ES	A34	B34	I2C_DAT
HDA_SDOUT	A33	B33	I2C_CLK
HDA_BITCLK	A32	B32	SPKR
GND	A31	B31	HDA_SDIN0
HDA_RST#	A30	B30	HDA_SDIN1/ SNDW0_DAT
HDA_SYNC	A29	B29	(S)ATA_ACT#
(S)ATA_ACT#	A28	B28	NC
BATLOW#	A27	B27	NC
NC	A26	B26	WDT
NC	A25	B25	NC
SUS_S5#	A24	B24	NC
NC	A23	B23	NC
NC	A22	B22	NC
GND	A21	B21	GND



Signal	PIN	PIN	Signal
SATA0_RX-	A20	B20	SATA1_RX-
SATA0_RX+	A19	B19	SATA1_RX+
SUS_S4#	A18	B18	ESPI_RST#
SATA0_TX-	A17	B17	SATA1_TX-
SATA0_TX+	A16	B16	SATA1_TX+
SUS_S3#	A15	B15	SMB_ALERT#
GBE0_CTREF	A14	B14	SMB_SDA_S5
GBE0_MDI0+	A13	B13	SMB_SCL_S5
GBE0_MDI0-	A12	B12	PWRBTN#
GND	A11	B11	GND
GBE0_MDI1+	A10	B10	LPC_CLK/ ESPI_CK
GBE0_MDI1-	A9	B9	NC
GBE0_LINK#	A8	B8	LPC_DRQ0#/ ESPI_ALERT0#
GBE0_MDI2+	A7	B7	LPC_AD3/ ESPI_IO_3
GBE0_MDI2-	A6	B6	LPC_AD2/ ESPI_IO_2
GBE0_LINK2500#	A5	B5	LPC_AD1/ ESPI_IO_1
GBE0_LINK1000#	A4	B4	LPC_AD0/ ESPI_IO_0
GBE0_MDI3+	A3	B3	LPC_FRAME#/ ESPI_CS0#
GBE0_MDI3-	A2	B2	GBE0_ACT#
GND	A1	B1	GND

### 2.3.4.1 Signal Description – COM Express Connector 1 (CN1A)

#### 2.3.4.1.1 Audio Signals

Signal	Signal Description
HDA_SYNC	HD Audio Sync
HDA_RST#	HD Audio Reset

#### 2.3.4.1.2 Gigabit Ethernet Signals

Signal	Signal Description			
GBE0_MD[0:3] +/-	Gigabit Ethernet Controller 0: Media Dependent Interface Differential Pairs 0,1,2,3. The MDI can operate in 2500, 1000, 100 and 10 Mbit / sec modes. Some pairs are unused in some modes, per the following:			
	MDI[0]+/-	B1_DA+/-	TX+/-	TX+/-
	MDI[1]+/	B1_DB+/-	RX+/-	RX+/-
	MDI[2]+/	B1_DC+/-	X	X
	MDI[3]+/	B1_DD+/-	X	X
GBE0_ACT#	Gigabit Ethernet Controller 0 activity indicator, active low.			
GBE0_LINK#	Gigabit Ethernet Controller 0 link indicator, active low.			
GBE0_LINK100_1000#	Gigabit Ethernet Controller 100 1000 Mbit / sec link indicator, active low.			
GBE0_LINK2500#	Gigabit Ethernet Controller 2500 Mbit / sec link indicator, active low.			

#### 2.3.4.1.3 PCI Express Signals

Signal	Signal Description
PCIE_TX[0:4] +/-	PCI Express Differential Transmit Pair 0-4
PCIE_RX[0:4] +/-	PCI Express Differential Receive Pair 0-4

#### 2.3.4.1.4 Flat Panel LVDS Signals

Signal	Signal Description
LVDS_BKLT_CTRL	Controls panel digital power.
LVDS_I2C_CK	I2C clock output for LVDS display use.
LVDS_I2C_DAT	I2C data line for LVDS display use.
LVDS_VDD_EN	LVDS panel power enables.

## 2.3.4.1.5 LPC/eSPI Signals

Signal	Signal Description
LPC_FRAME#/ ESPI_CS0#	LPC frame indicates the start of an LPC cycle  ESPI Mode: eSPI Master Chip Select Outputs Driving Chip Select0#. A low selects a particular eSPI slave for the transaction. Each of the eSPI slaves is connected to a dedicated Chip Selectn# pin
LPC_AD[0:3]/ ESPI_IO_[0:3]	LPC multiplexed address, command and data bus  ESPI Mode: eSPI Master Data Input / Outputs These are bi-directional input/output pins used to transfer data between master and slaves.  Multiplexed with LPC_AD[0:3]
LPC_CLK/ ESPI_CK	LPC clock output - 33MHz nominal  ESPI Mode: eSPI Master Clock Output This pin provides the reference timing for all the serial input and output operations
LPC_SERIRQ/ ESPI_CS1#	LPC serial interrupt  ESPI Mode: eSPI Master Chip Select Outputs Driving Chip Select# A low selects a particular eSPI slave for the transaction. Each of the eSPI slaves is connected to a dedicated Chip Selectn# pin
LPC_DRQ0#/ ESPI_ALERT0#	LPC serial DMA request.  <b>ESPI Mode:</b> eSPI pins used by eSPI slave to request service from the eSPI master.
LPC_DRQ1#/ ESPI_ALERT1#	LPC serial DMA request.  <b>ESPI Mode:</b> eSPI pins used by eSPI slave to request service from the eSPI master.

## 2.3.4.1.6 GPIO Signals

Signal	Signal Description
GPI[0:4]	General purpose input pins.
GPO[0:4]	General purpose output pins.

## 2.3.4.1.7 Power &amp; System Management Signals

Signal	Signal Description
SUS_S3#	Indicates system is in Suspend to RAM state. Active low output.
BATLOW#	Indicates that external battery is low
PWRBTN#	Power button to bring system out of S5 (soft off), active on rising edge.

## ESM-ARL User's Manual

SMB_SCL_S5	System Management Bus bidirectional clock line.
SMB_SDA_S5	System Management Bus bidirectional data line.
SMB_ALERT#	System Management Bus Alert - input can be used to generate an SMI# (System Management Interrupt) or to wake the system.
ESPI_RST#	ESPI Mode: eSPI Reset Reset the eSPI interface for both master and slaves. eSPI Reset# is typically driven from eSPI master to eSPI slaves
PWR_OK	Power OK from main power supply
SYS_RESET#	Reset button input. Active low input.
WAKE0#	PCI Express wake up signal.
WAKE1#	General purpose wake up signal.

### 2.3.4.1.8 SATA Signals

Signal	Signal Description
SATA[0:1]_TX +/-	Serial ATA Channel 0-1 transmit differential pair.
SATA[0:1]_RX +/-	Serial ATA Channel 0-1 receive differential pair.
ATA_ACT#	ATA (parallel and serial) activity indicator, active low.

### 2.3.4.1.9 USB Signals

Signal	Signal Description
USB[0:7] +/-	USB differential pairs, channels 0 through 7
USB_0_1_OC#	USB over-current sense, USB channels 0 and 1
USB_2_3_OC#	USB over-current sense, USB channels 2 and 3
USB_4_5_OC#	USB over-current sense, USB channels 4 and 5
USB_6_7_OC#	USB over-current sense, USB channels 6 and 7

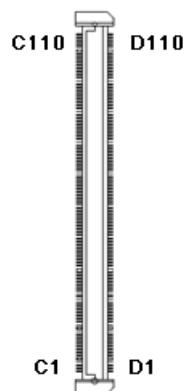
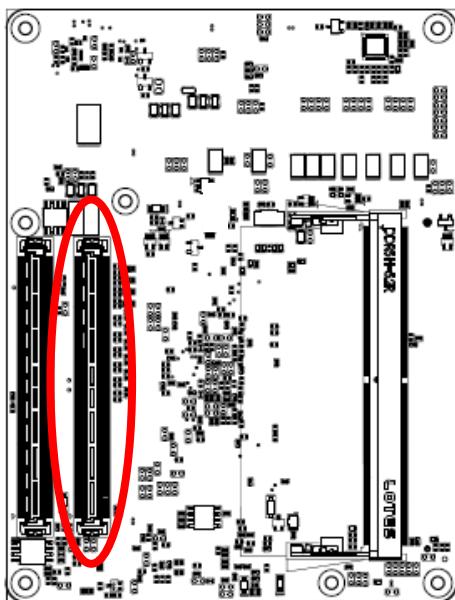
### 2.3.4.1.10 I2C Signals

Signal	Signal Description
I2C_CLK	General purpose I2C port clock output.
I2C_DATA	General purpose I2C port data I/O line.

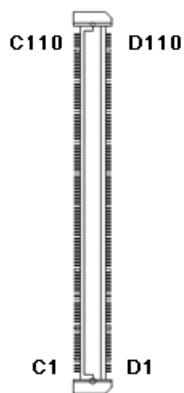
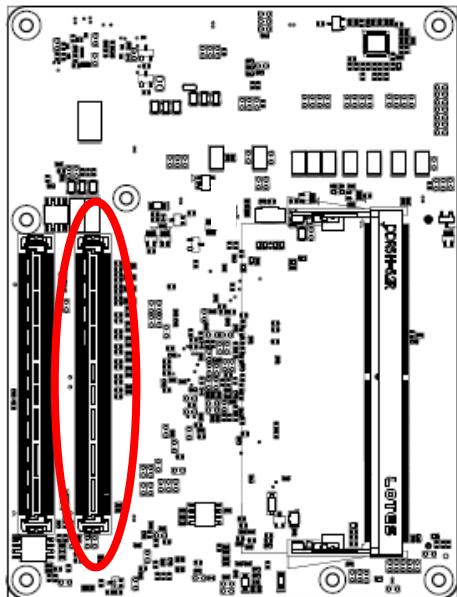
### 2.3.4.1.11 USB3.0 Signals

Signal	Signal Description
USB_SSTX[0:1]+	Additional transmit signal differential pairs for the SuperSpeed USB data path.
USB_SSTX[0:1]-	
USB_SSRX[0:1]+	Additional receive signal differential pairs for the SuperSpeed USB data path.
USB_SSRX[0:1]-	

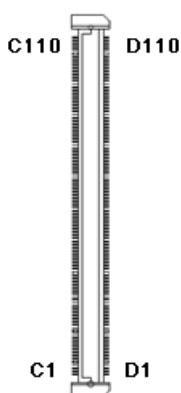
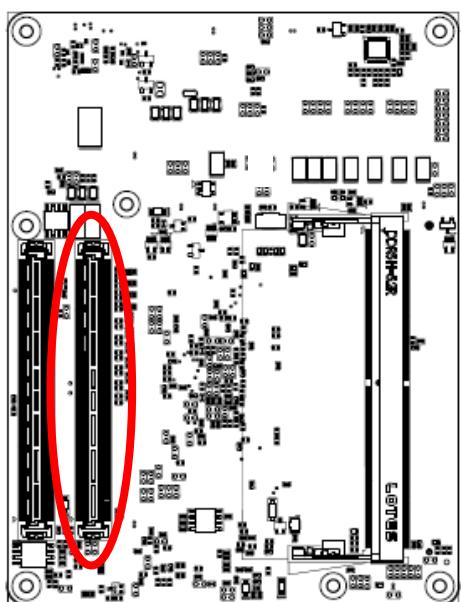
### 2.3.5 COM Express Connector 2 (CN1B)



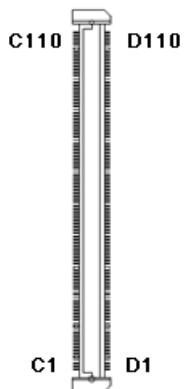
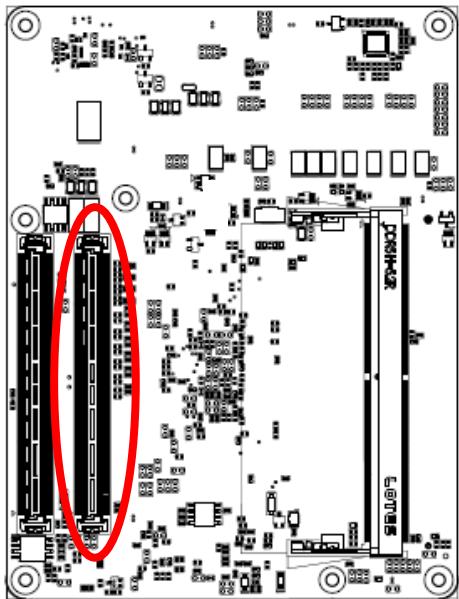
Signal	PIN	PIN	Signal
GND	C110	D110	GND
VCC_12V	C109	D109	VCC_12V
VCC_12V	C108	D108	VCC_12V
VCC_12V	C107	D107	VCC_12V
VCC_12V	C106	D106	VCC_12V
VCC_12V	C105	D105	VCC_12V
VCC_12V	C104	D104	VCC_12V
GND	C103	D103	GND
PCIEX4_B_RX3-	C102	D102	PCIEX4_B_TX3-
PCIEX4_B_RX3+	C101	D101	PCIEX4_B_TX3+
GND	C100	D100	GND
PCIEX4_B_RX2-	C99	D99	PCIEX4_B_TX2-
PCIEX4_B_RX2+	C98	D98	PCIEX4_B_TX2+
GND	C97	D97	GND
GND	C96	D96	GND
PCIEX4_B_RX1-	C95	D95	PCIEX4_B_TX1-
PCIEX4_B_RX1+	C94	D94	PCIEX4_B_TX1+
GND	C93	D93	GND
PCIEX4_B_RX0-	C92	D92	PCIEX4_B_TX0-
PCIEX4_B_RX0+	C91	D91	PCIEX4_B_TX0+
GND	C90	D90	GND
PCIEX4_A_RX3-	C89	D89	PCIEX4_A_TX3-
PCIEX4_A_RX3+	C88	D88	PCIEX4_A_TX3+
GND	C87	D87	GND
PCIEX4_A_RX2-	C86	D86	PCIEX4_A_TX2-
PCIEX4_A_RX2+	C85	D85	PCIEX4_A_TX2+
GND	C84	D84	GND
GND	C83	D83	GND
PCIEX4_A_RX1-	C82	D82	PCIEX4_A_TX1-
PCIEX4_A_RX1+	C81	D81	PCIEX4_A_TX1+



Signal	PIN	PIN	Signal
GND	C80	D80	GND
PCIEX4_A_RX0-	C79	D79	PCIEX4_A_TX0-
PCIEX4_A_RX0+	C78	D78	PCIEX4_A_TX0+
GND	C77	D77	GND
GND	C76	D76	GND
PCIE_X8_RX7-	C75	D75	PCIE_X8_TX0-
PCIE_X8_RX7+	C74	D74	PCIE_X8_TX0+
GND	C73	D73	GND
PCIE_X8_RX6-	C72	D72	PCIE_X8_TX7-
PCIE_X8_RX6+	C71	D71	PCIE_X8_TX7+
GND	C70	D70	GND
PCIE_X8_RX5-	C69	D69	PCIE_X8_TX5-
PCIE_X8_RX5+	C68	D68	PCIE_X8_TX5+
RAPID_SHUTDOWN	C67	D67	GND
PCIE_X8_RX4-	C66	D66	PCIE_X8_TX4-
PCIE_X8_RX4+	C65	D65	PCIE_X8_TX4+
GND	C64	D64	GND
GND	C63	D63	GND
PCIE_X8_RX3-	C62	D62	PCIE_X8_TX3-
PCIE_X8_RX3+	C61	D61	PCIE_X8_TX3+
GND	C60	D60	GND
PCIE_X8_RX2-	C59	D59	PCIE_X8_TX2-
PCIE_X8_RX2+	C58	D58	PCIE_X8_TX2+
TYPE1#	C57	D57	TYPE2#
PCIE_X8_RX1-	C56	D56	PCIE_X8_TX1-
PCIE_X8_RX1+	C55	D55	PCIE_X8_TX1+
TYPE0#	C54	D54	PEG_LAN_RV#
PCIE_X8_RX0-	C53	D53	PCIE_X8_TX0-
PCIE_X8_RX0+	C52	D52	PCIE_X8_TX0+
GND	C51	D51	GND



Signal	PIN	PIN	Signal
DDI3_PAIR3-	C50	D50	DDI2_PAIR3-/USB4_2_SS RX1-
DDI3_PAIR3+	C49	D49	DDI2_PAIR3+/USB4_2_SS RX1+
PEG_SLOT_RESTE#	C48	D48	GND
DDI3_PAIR2-	C47	D47	DDI2_PAIR2-/USB4_2_SS TX1-
DDI3_PAIR2+	C46	D46	DDI2_PAIR2+/USB4_2_SS TX1+
GSPI1_CS0#	C45	D45	GND
DDI3_HPD	C44	D44	DDI2_HPD_CB
DDI3_PAIR1-	C43	D43	DDI2_PAIR1-/USB4_2_SS RX0-
DDI3_PAIR1+	C42	D42	DDI2_PAIR1+/USB4_2_SS RX0+
GND	C41	D41	GND
DDI3_PAIR0-	C40	D40	DDI2_PAIR0-/USB4_2_SS TX0-
DDI3_PAIR0+	C39	D39	DDI2_PAIR0+/USB4_2_SS TX0+
DDI3_DDC_AUX_SEL	C38	D38	GND
DDI3_CTRLDATA_AUX-	C37	D37	DDI1_PAIR3-/USB4_1_SS RX1-
DDI3_CTRLCLK_AUX+	C36	D36	DDI1_PAIR3+/USB4_1_SS RX1+
USB4_2_LSTX	C35	D35	LSX1_RXD
DDI2_DDC_AUX_SEL	C34	D34	DDI1_DDC_AUX_SEL
DDI2_CTRLDATA_AUX-	C33	D33	DDI1_PAIR2-/USB4_1_SS TX1-
DDI2_CTRLCLK_AUX+	C32	D32	DDI1_PAIR2+/USB4_1_SS TX1+
GND	C31	D31	GND
USB4_PD_I2C_DAT	C30	D30	DDI1_PAIR1-/USB4_1_SS RX0-
USB4_PD_I2C_CLK	C29	D29	DDI1_PAIR1+/USB4_1_SS RX0+
SML1_DAT	C28	D28	GND
SML1_CLK	C27	D27	DDI1_PAIR0-/USB4_1_SS TX0-
SML0_DAT	C26	D26	DDI1_PAIR0+/USB4_1_SS TX0+
SML0_CLK	C25	D25	GND
DDI1_HPD_CB	C24	D24	GND
NC	C23	D23	NC
NC	C22	D22	NC
GND	C21	D21	GND



Signal	PIN	PIN	Signal
NC	C20	D20	NC
NC	C19	D19	NC
GND	C18	D18	PMCALERT#
USB4_RT_ENA	C17	D17	EC_I2C_IRQ#
USB4_1_LSRX	C16	D16	DDI1_CTRLDATA_AUX-
USB4_1_LSTX	C15	D15	DDI1_CTRLCLK_AUX+
GND	C14	D14	GND
USB_SSRX3+	C13	D13	USB_SSTX3+
USB_SSRX3-	C12	D12	USB_SSTX3-
GND	C11	D11	GND
USB_SSRX2+	C10	D10	USB_SSTX2+
USB_SSRX2-	C9	D9	USB_SSTX2-
GND	C8	D8	GND
USB_SSRX1+	C7	D7	USB_SSTX1+
USB_SSRX1-	C6	D6	USB_SSTX1-
GND	C5	D5	GND
USB_SSRX0+	C4	D4	USB_SSTX0+
USB_SSRX0-	C3	D3	USB_SSTX0-
GND	C2	D2	GND
GND	C1	D1	GND

### 2.3.5.1 Signal Description – COM Express Connector 2 (CN1B)

#### 2.3.5.1.1 USB3.0 Signals

Signal	Signal Description
USB_SSTX[0:3]+ USB_SSTX[0:3]-	Additional transmit signal differential pairs for the SuperSpeed USB data path.
USB_SSRX[0:3]+ USB_SSRX[0:3]-	Additional receive signal differential pairs for the SuperSpeed USB data path.

#### 2.3.5.1.2 USB4.0 Signals

Signal	Signal Description
USB4_1_SSTX[0:1]+ USB4_1_SSTX[0:1]-	High speed USB4 data transmit pairs, pin shared with DDI[1:2].
USB4_1_SSRX[0:1]+ USB4_1_SSRX[0:1]-	High speed USB4 data receive pairs, pin shared with DDI[1:2].
USB4_2_SSTX[0:1]+ USB4_2_SSTX[0:1]-	High speed USB4 data transmit pairs, pin shared with DDI[1:2].
USB4_2_SSRX[0:1]+ USB4_2_SSRX[0:1]-	High speed USB4 data receive pairs, pin shared with DDI[1:2].
USB4_RT_ENA	Power Enable for Carrier based USB Retimers. Sourced from chipset GPO. “USB Retimer Enable”.
USB4_1_LSRX	Side-band RX interface for USB4 Alternate modes. “Low Speed” asynchronous serial RX line
USB4_1_LSTX	Side-band TX interface for USB4 Alternate modes. “Low Speed” asynchronous serial TX line

#### 2.3.5.1.3 DDI Signals

Signal	Signal Description
DDI[1:3]_PAIR[0:3]+ DDI[1:3]_PAIR[0:3]-	Digital Display Interface 1 to 3Pair[0:3] differential pairs
DDI[1:3]_DDC_AUX_SEL	Selects the function of DDI[1:3]_CTRLCLK_AUX+ and DDI[1:3]_CTRLDATA_AUX-. If this input is floating the AUX pair is used for the DP AUX+/- signals. If pulled-high the AUX pair contains the CRTLCLK and CTRLDATA signals.
DDI[1:3]_CTRLCLK_AUX+	DP AUX+function if DDI[1:3]_DDC_AUX_SEL is no connect HDMI/DVI 12C CTRLCLK if DDI[1:3]_DDC_AUX_SEL is pulled high
DDI[1:3]_CTRLDATA_AUX-	DP AUX-function if DDI[1:3]_DDC_AUX_SEL is no connect HDMI/DVI 12C CTRLDATA if DDI[1:3]_DDC_AUX_SEL is pulled high
DDI[1:3]_HPD	Digital Display Interface Hot-Plug Detect

## ESM-ARL User's Manual

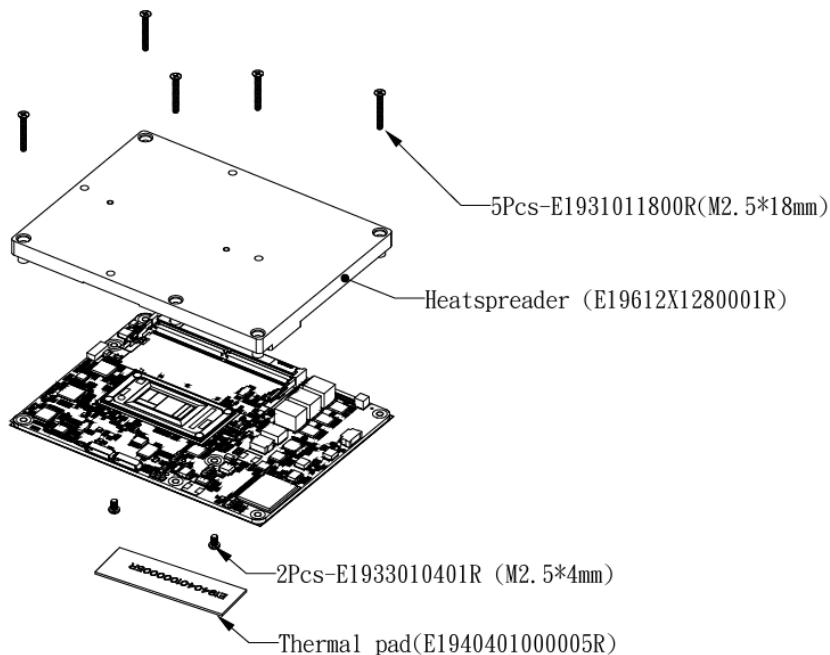
### 2.3.5.1.4 PEG PCI Express Lanes Signals

Signal	Signal Description
PEG_TX[0:15]+ PEG_TX[0:15]-	PCI Express Graphics transmit differential pairs.
PEG_RX[0:15]+ PEG_RX[0:15]-	PCI Express Graphics receive differential pairs.

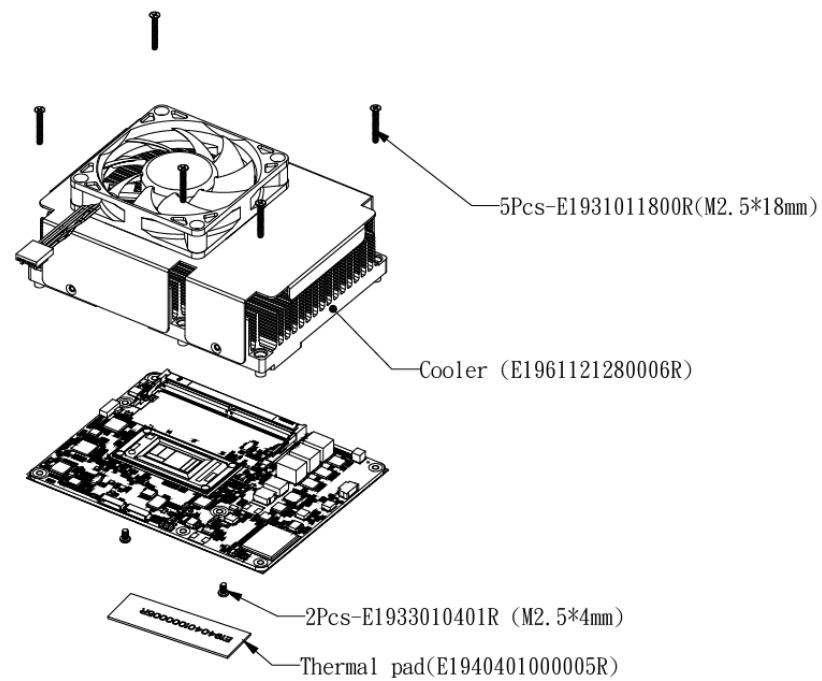
## 2.4 Installing Heatsink / Cooler

### Standard Temperature

**Heat spreader (ACC-ESMARL-SD-1R)** \_For ESM-ARL 15W, 0~60°C, 0.5S Airflow

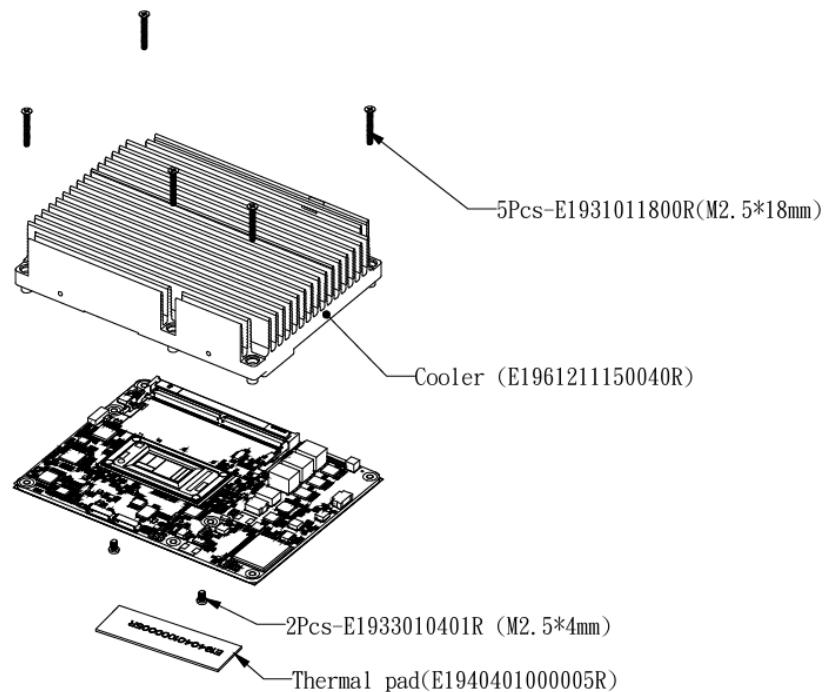


**Cooler (ACC-ESMARL-CL-1R)** \_For ESM-ARL 28W, 0~60°C, 0.5S Airflow



## ESM-ARL User's Manual

**Heatsink (ACC-ESMARL-SK-1R) \_For ESM-ARL 15W, 0~60°C, 0.5S Airflow**



## 3. Drivers Installation

All the drivers are available on Avalue Downloads Area (<https://www.alue.com/en/support/download>). Type the model name and press Enter to find all the relevant software, utilities, and documentation.

### Note:

The panel PC with projected capacitive type touchscreen and Windows 7 (or later) OS does not require touch driver installation. This is because there is a HID touch digitizer built-in driver in Windows 7 or later.

Category	Count
Chipset	1
Audio	1
Graphics	1
LAN	1
Other	1

## Chipset

Total 1 Files

No.	Release Date	Title	Description	Download
01	2023-09-20	Intel Chipset Driver for Win10 x64	Windows 10 64bit	

Audio				Total 1 Files
No.	Release Date	Title	Description	Download
01	2023-09-20	Realtek Audio Driver for Win10 x64	Windows 10 64bit	

(For reference only)



**Note:** Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

# ESM-ARL User's Manual

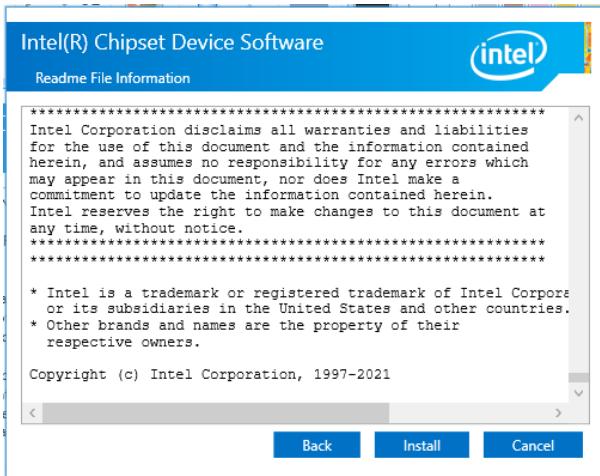
## 3.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

[www.alue.com](http://www.alue.com)



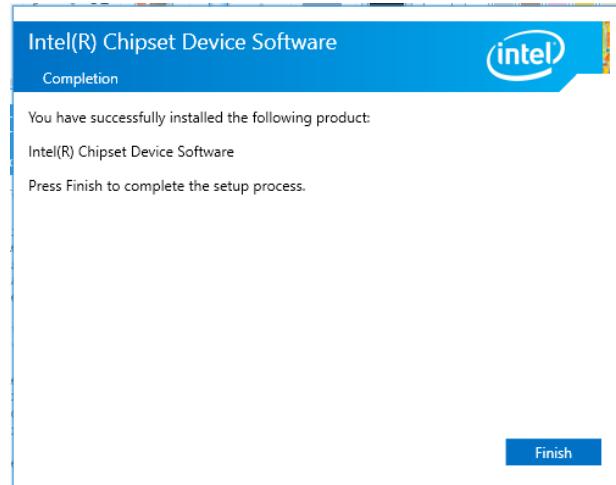
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



### Step 3. Click Install.



### Step1. Click Next.



### Step 4. Click Finish to complete setup.



### Step 2. Click Accept.

## 3.2 Install VGA Driver

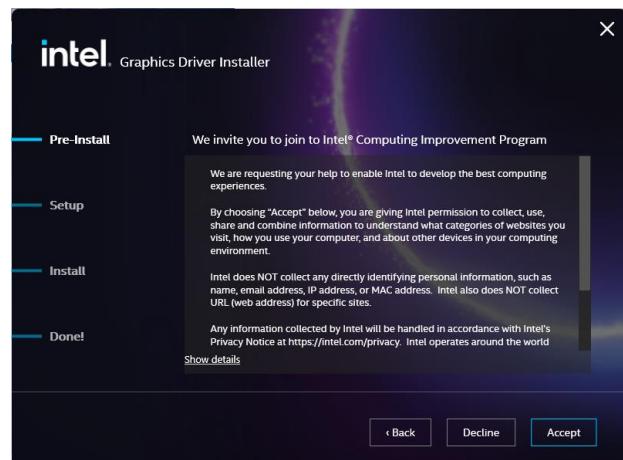
All drivers can be found on the Avalue

Official Website:

[www.avalue.com](http://www.avalue.com).



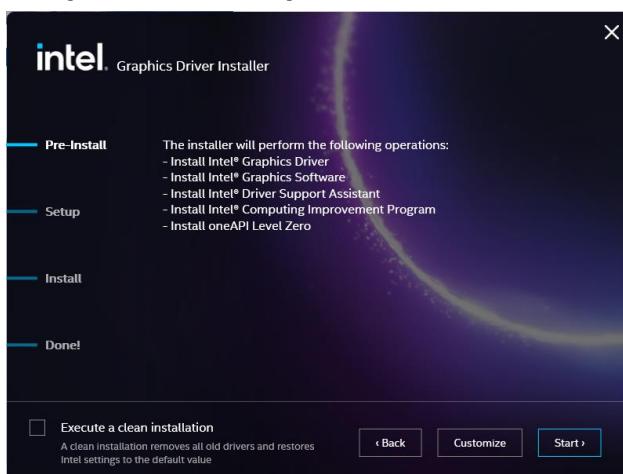
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



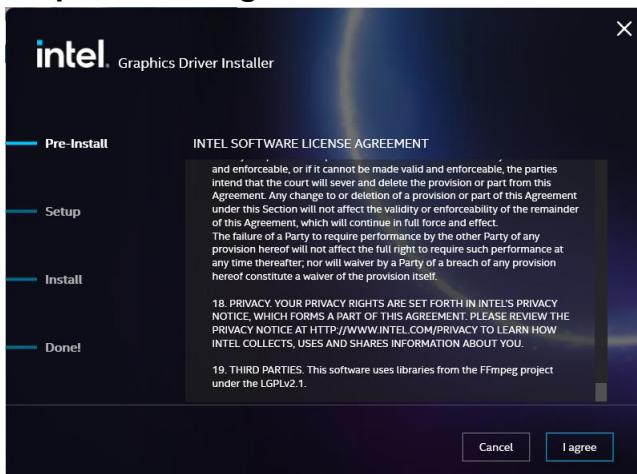
**Step 3. Click Accept.**



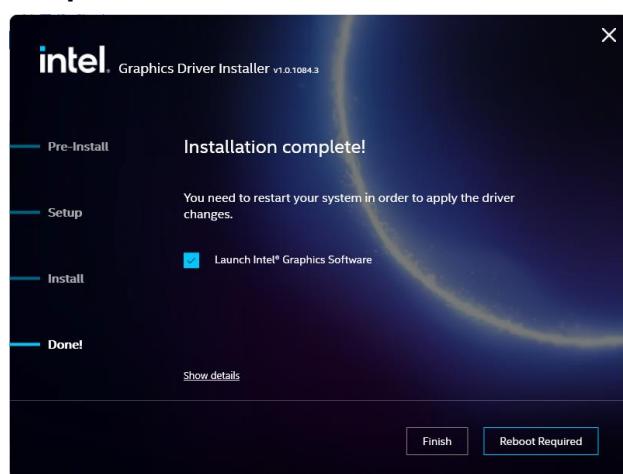
**Step 1. Click Begin installation.**



**Step 4. Click Start.**



**Step 2. Click I agree.**



**Step 5. Click Finish to complete setup.**

### 3.3 Install Ethernet Driver

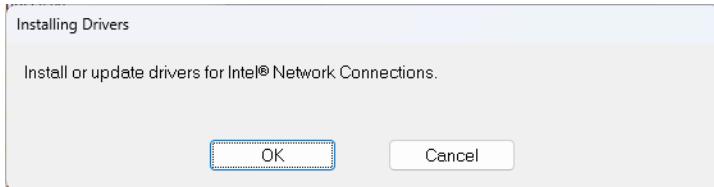
All drivers can be found on the Avalue Official

Website:

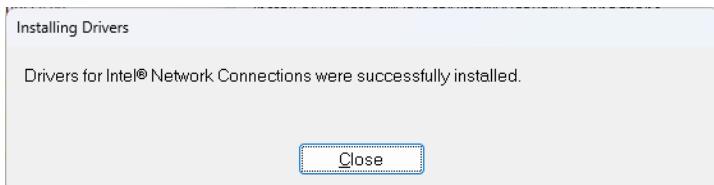
[www.alue.com](http://www.alue.com).



**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



**Step 1.** Click **OK** to continue installation.



**Step 2.** Setup completed.

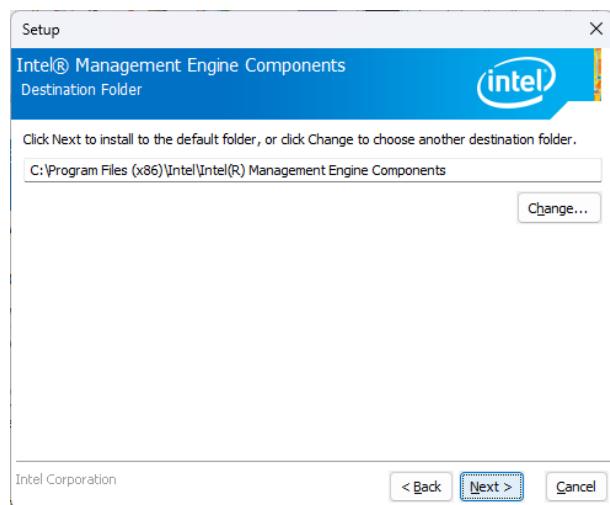
## 3.4 Install ME Driver

All drivers can be found on the Avalue Official Website:

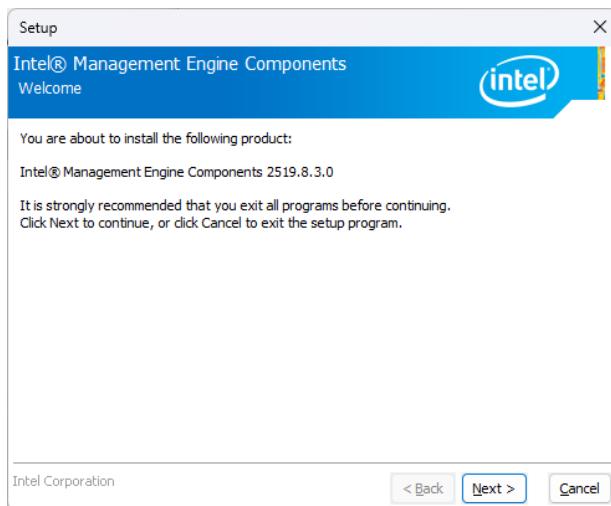
[www.alue.com](http://www.alue.com)



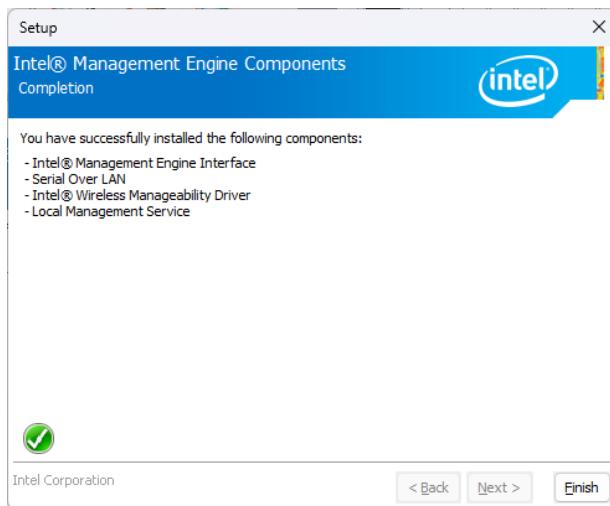
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



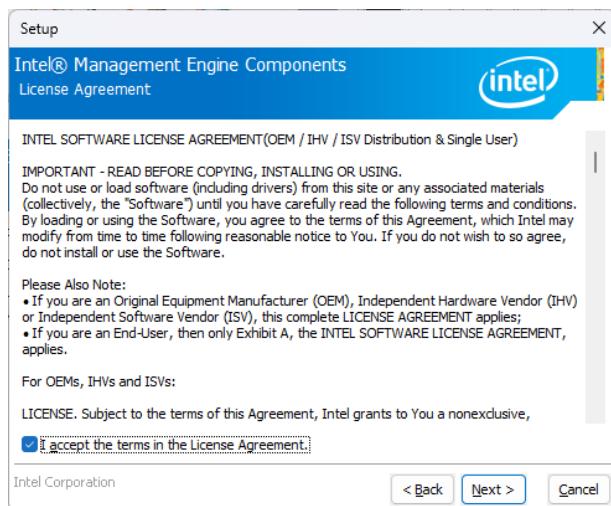
### Step 3. Click Next.



### Step 1. Click Next to continue installation.



### Step 4. Click Finish to complete setup.



### Step 2. Click Next.

## 3.5 Install Serial IO Driver

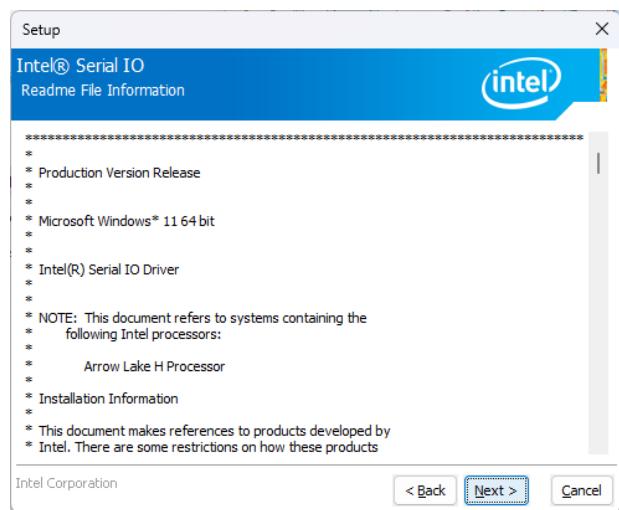
All drivers can be found on the Avalue Official

Website:

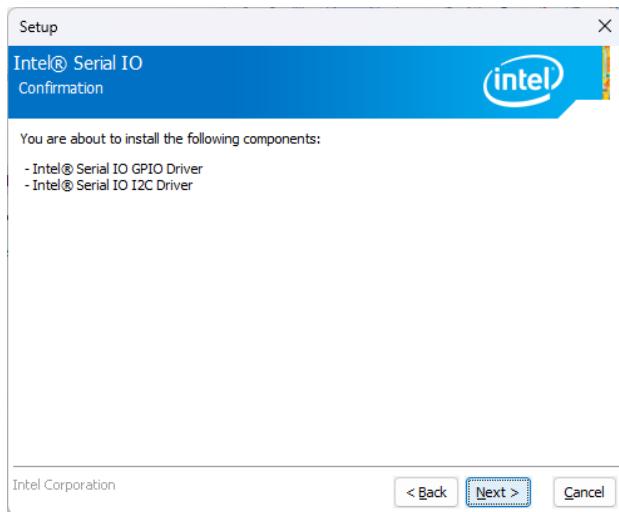
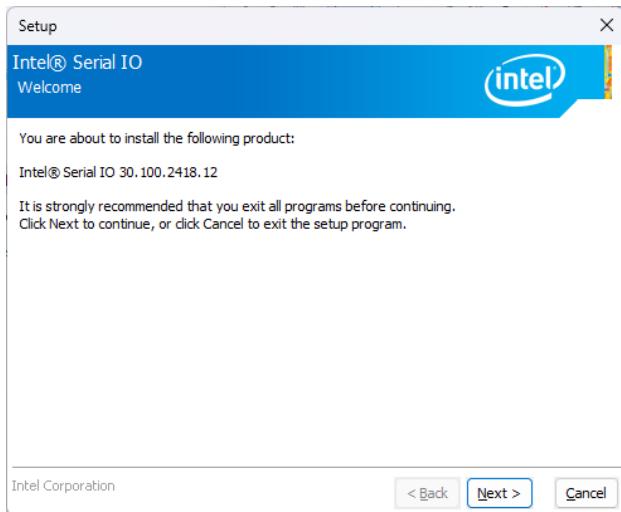
[www.alue.com](http://www.alue.com)



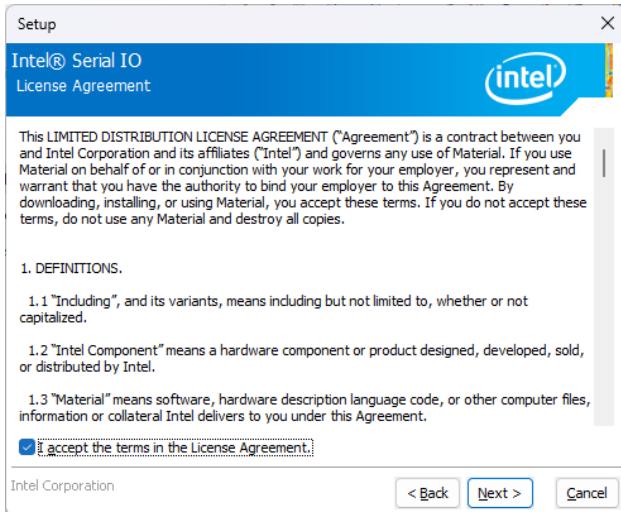
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



### Step 3. Click Next.

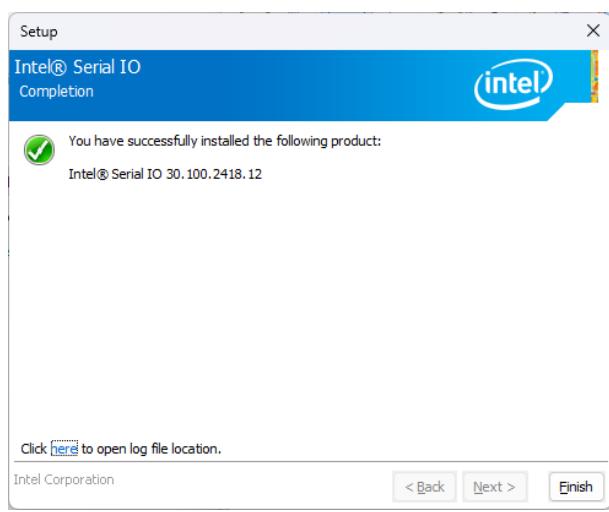


### Step 1. Click Next to continue installation.



### Step 2. Click Next.

### Step 4. Click Next.



### Step 5. Click Finish to complete setup.

## 3.6 Install IPMT Driver

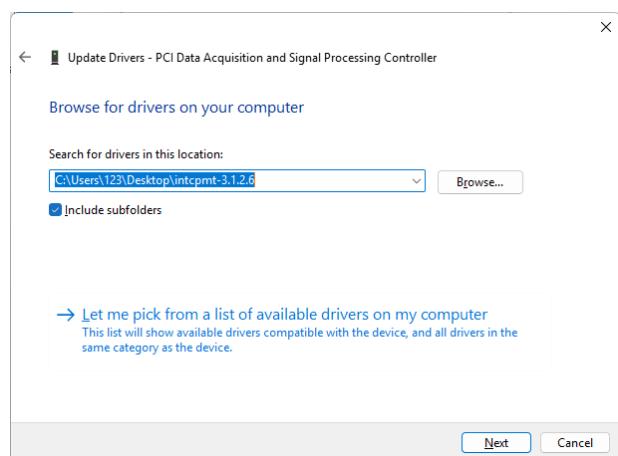
All drivers can be found on the Avalue Official

Website:

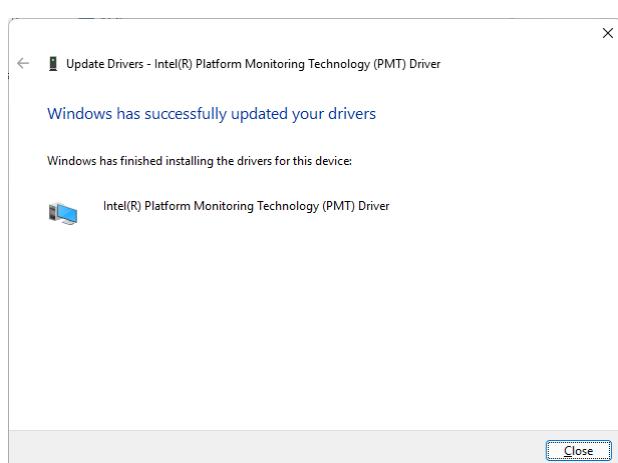
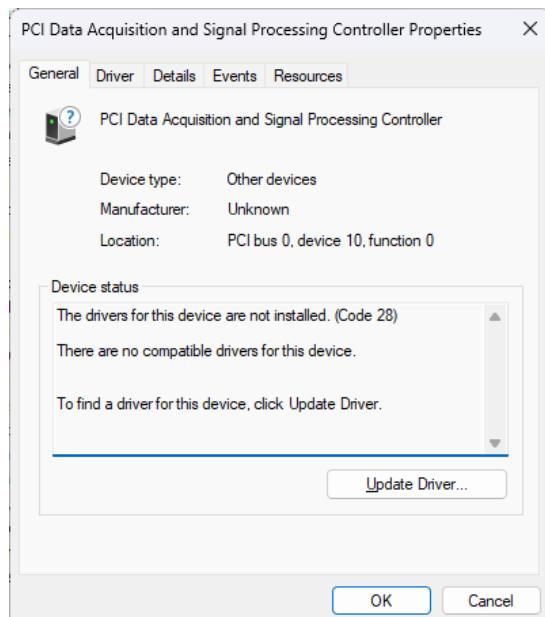
[www.alue.com](http://www.alue.com)



**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.

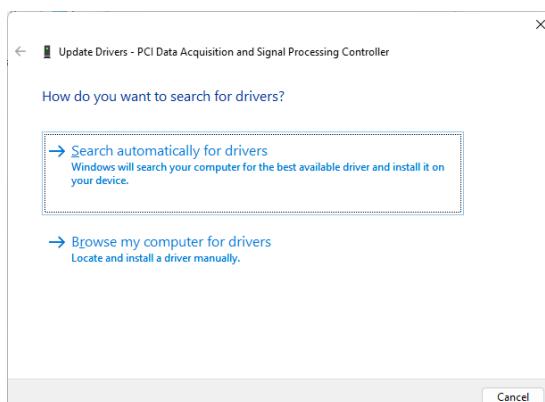


### Step 3. Click Next.



### Step 4. Setup completed.

### Step 1. Click OK.



### Step 2. Click Search automatically for drivers.

## 3.7 Install ISST Driver

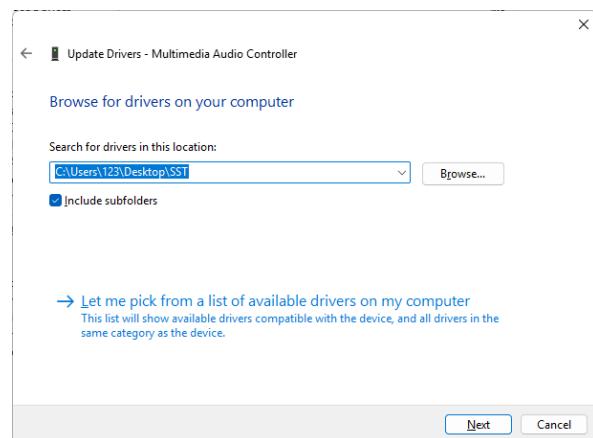
All drivers can be found on the Avalue Official

Website:

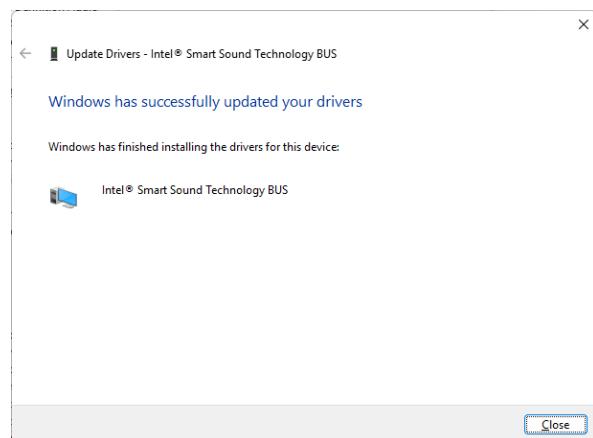
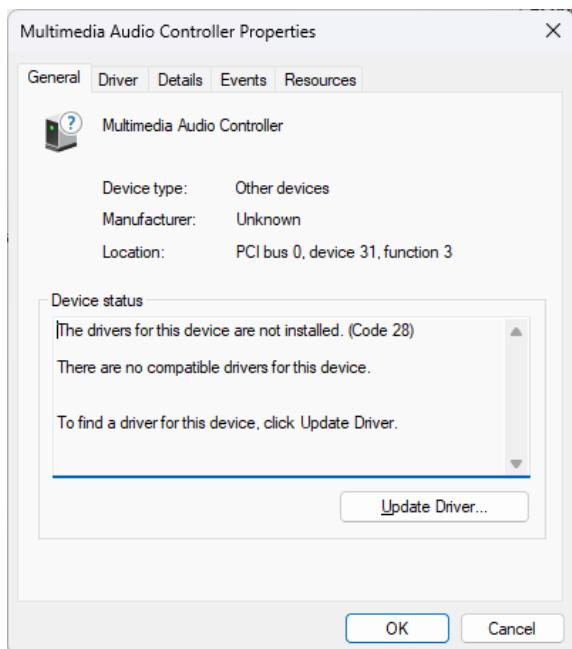
[www.alue.com](http://www.alue.com)



**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.

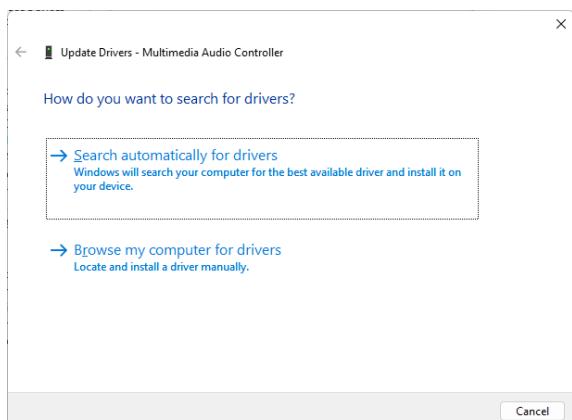


### Step 3. Click Next.



### Step 4. Setup completed.

### Step 1. Click OK.



### Step 2. Click Search automatically for drivers.

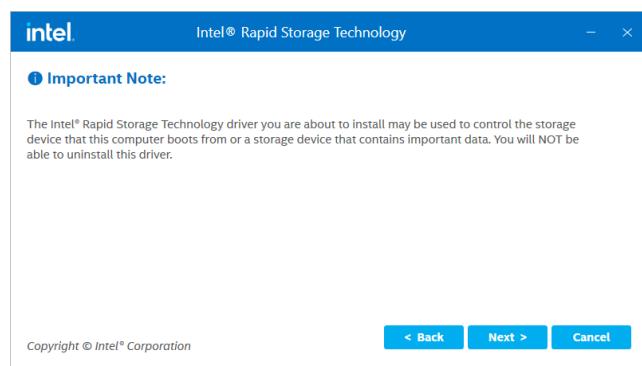
## 3.8 Install IRST Driver

All drivers can be found on the Avalue Official Website:

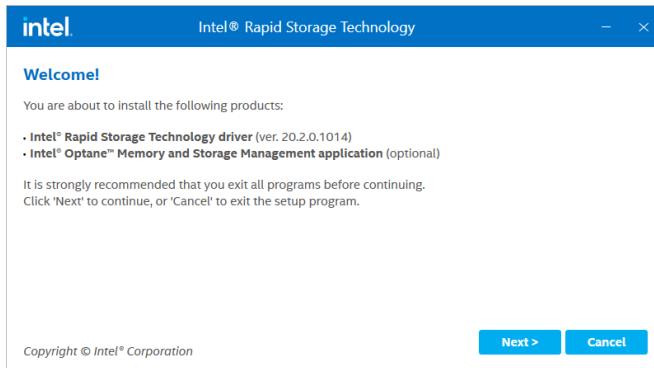
[www.alue.com](http://www.alue.com)



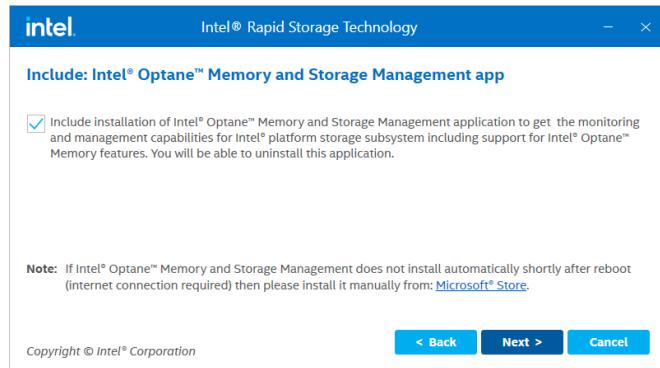
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



### Step 3. Click Next.



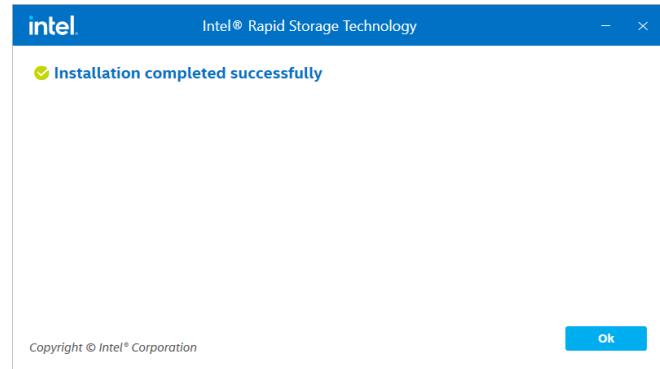
### Step 1. Click Next to continue installation.



### Step 4. Click Next.



### Step 2. Click Next.



### Step 5. Setup completed.

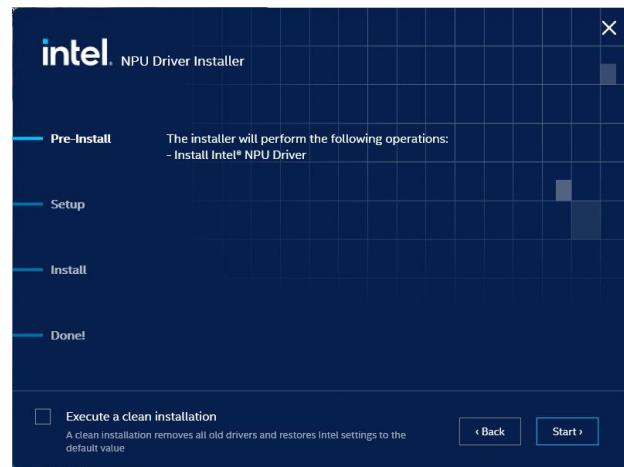
### 3.9 Install NPU Driver

All drivers can be found on the Avalue Official Website:

[www.alue.com](http://www.alue.com)



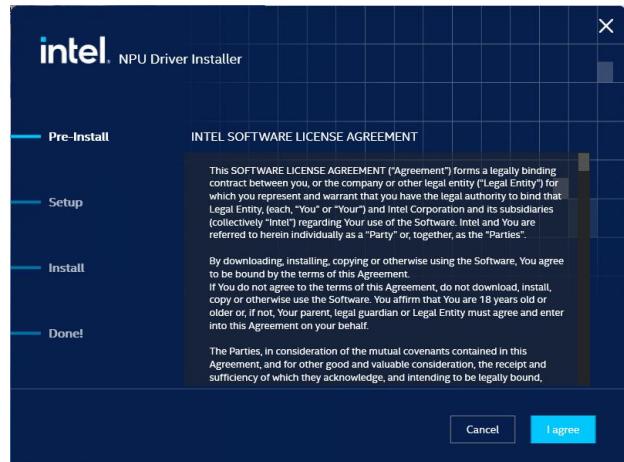
**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



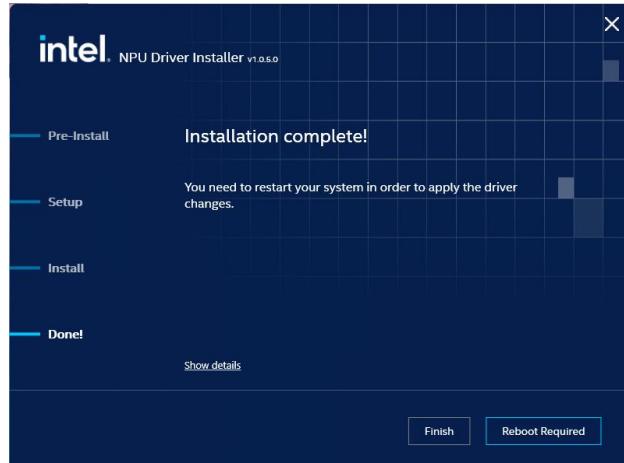
#### Step 3. Click Start.



#### Step 1. Click Begin installation.



#### Step 2. Click I agree.



#### Step 4. Click Finish to complete setup.

## 3.10 Install Audio Driver

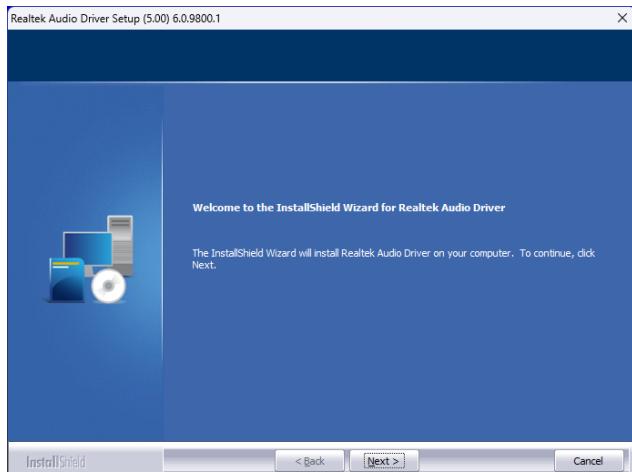
All drivers can be found on the Avalue Official

Website:

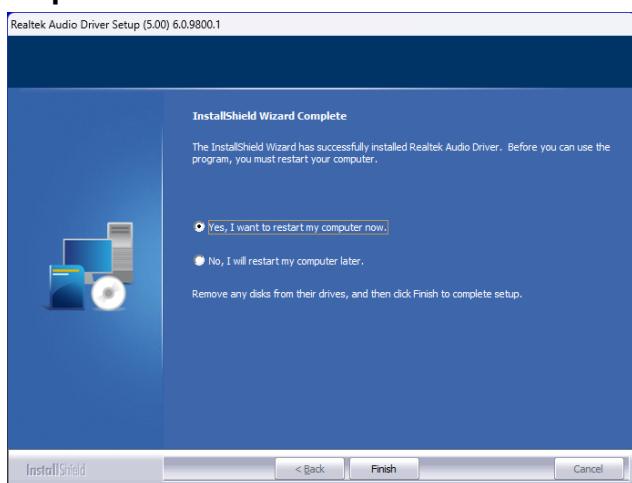
[www.alue.com](http://www.alue.com).



**Note:** The installation procedures and screen shots in this section are based on Windows 11 operation system.



**Step 1. Click Next.**



**Step 2. Click Finish to complete setup.**

## 4.BIOS Setup

## **4.1 Introduction**

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

## **4.2 Starting Setup**

AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing <ESC> or <Del> immediately after switching the system on, or

By pressing the < ESC> or <Del> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

**Press <ESC> or <Del> to enter SETUP**

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys.

### 4.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



**Note:** Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

## **4.4 Getting Help**

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the <Enter> key again.

## **4.5 In Case of Problems**

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

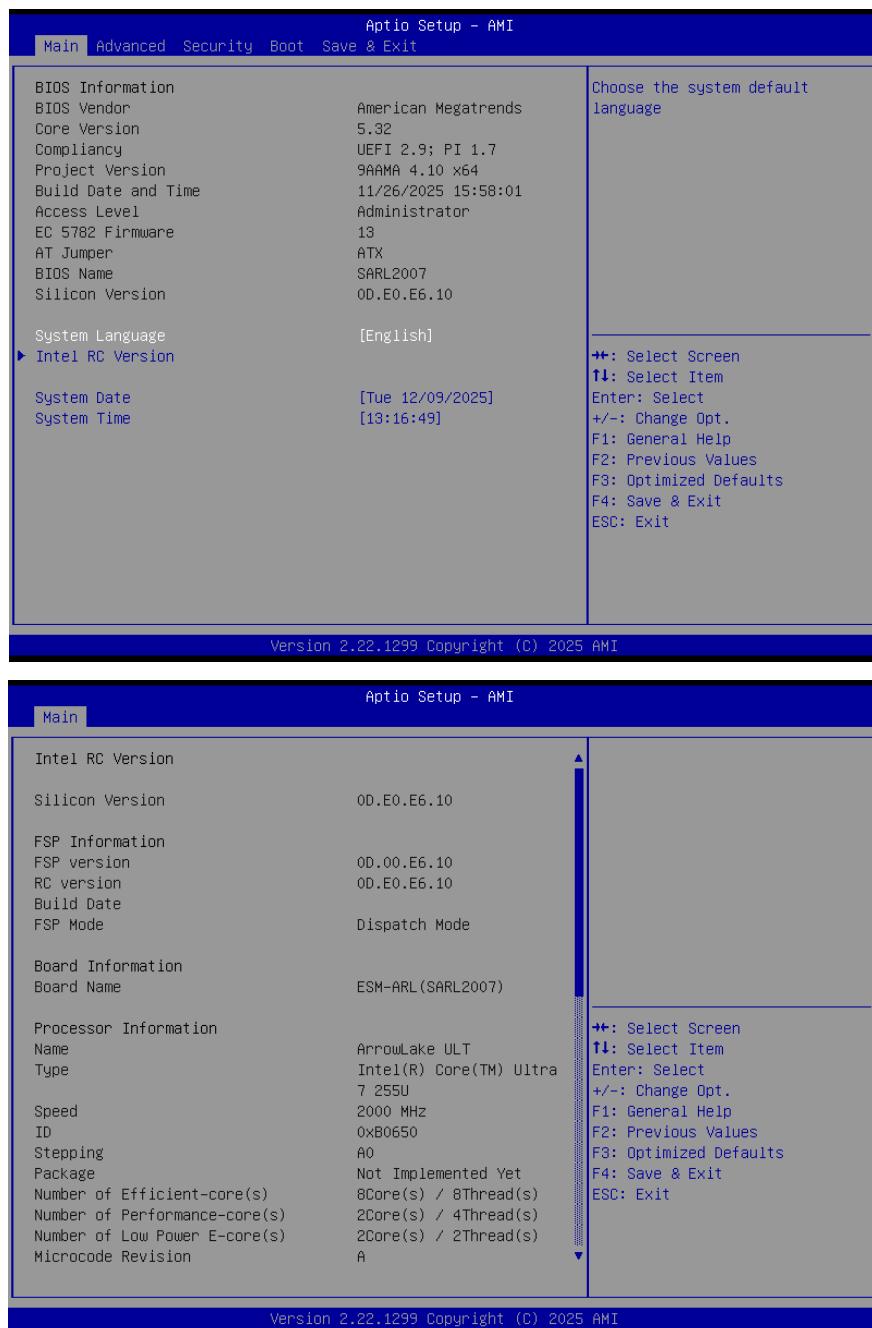
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

### 4.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

#### 4.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.





#### 4.6.1.1 System Language

This option allows choosing the system default language.

#### 4.6.1.2 System Date

Use the system date option to set the system date. Manually enter the Month, day and year.

#### 4.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

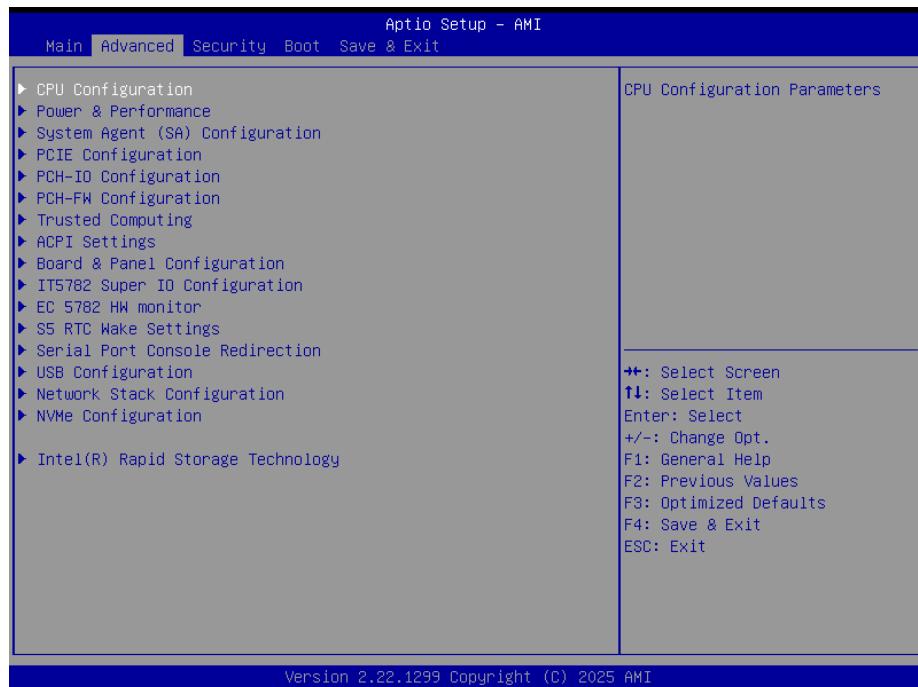


**Note:** The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.  
Visit the Avalue website ([www.alue.com](http://www.alue.com)) to download the latest product and BIOS information.

## ESM-ARL User's Manual

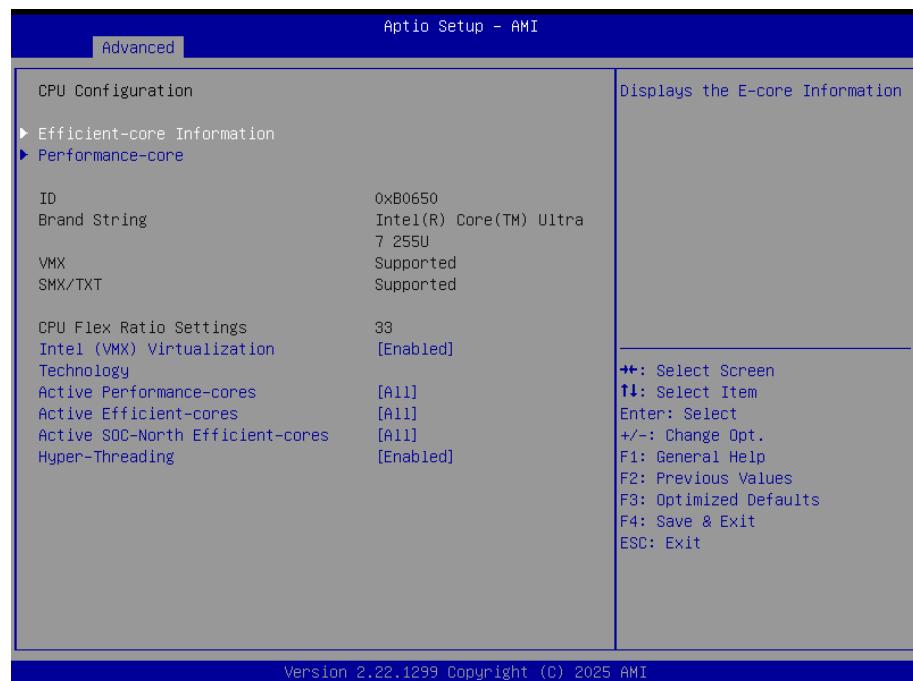
### 4.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



#### 4.6.2.1 CPU Configuration

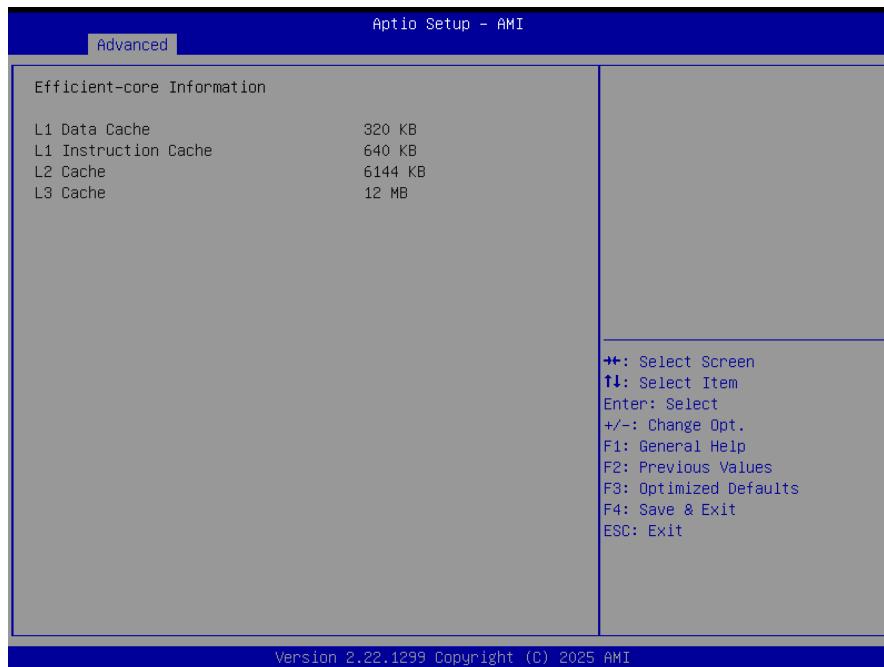
Use the CPU configuration menu to view detailed CPU specification and configure the CPU.



Item	Options	Description
<b>Intel (VMX) Virtualization Technology</b>	Disabled Enabled <b>[Default]</b>	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool

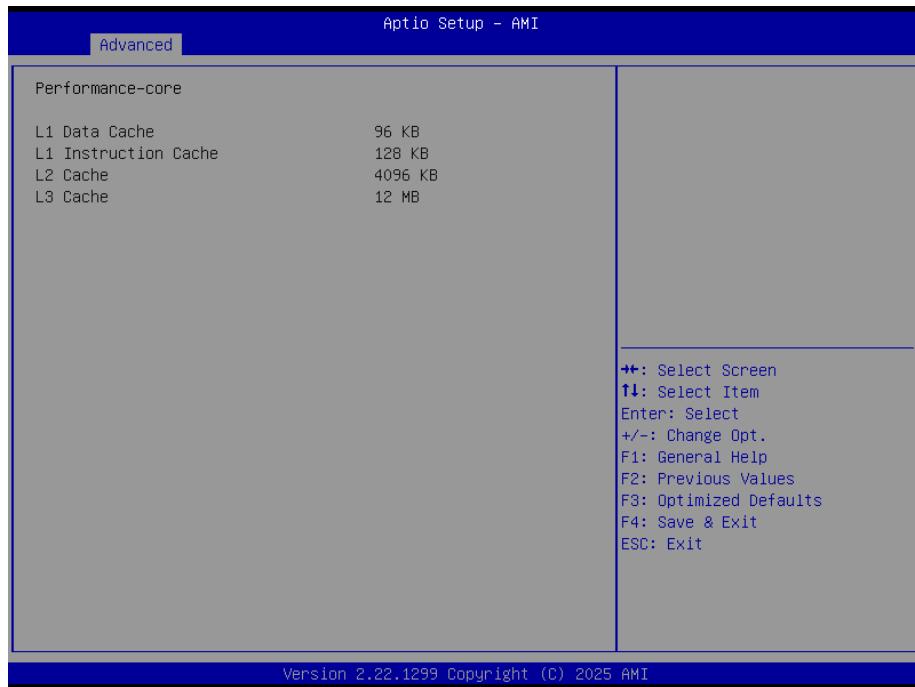
		Technology.
<b>Active Performance-cores</b>	All[Default] 7 6 5 4 3 2 1	Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.
<b>Active Efficient-cores</b>	All[Default] 15 14 13 12 11 10 9 8	Number of E-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.
<b>Active SOC-North Efficient-cores</b>	All[Default] 1 0	Number of SOC-North Efficient-cores to enable in SOC North.
<b>Hyper-Threading</b>	Disabled Enabled[Default]	Enable or Disable Hyper-Threading Technology.

#### 4.6.2.1.1 Efficient-core Information



## ESM-ARL User's Manual

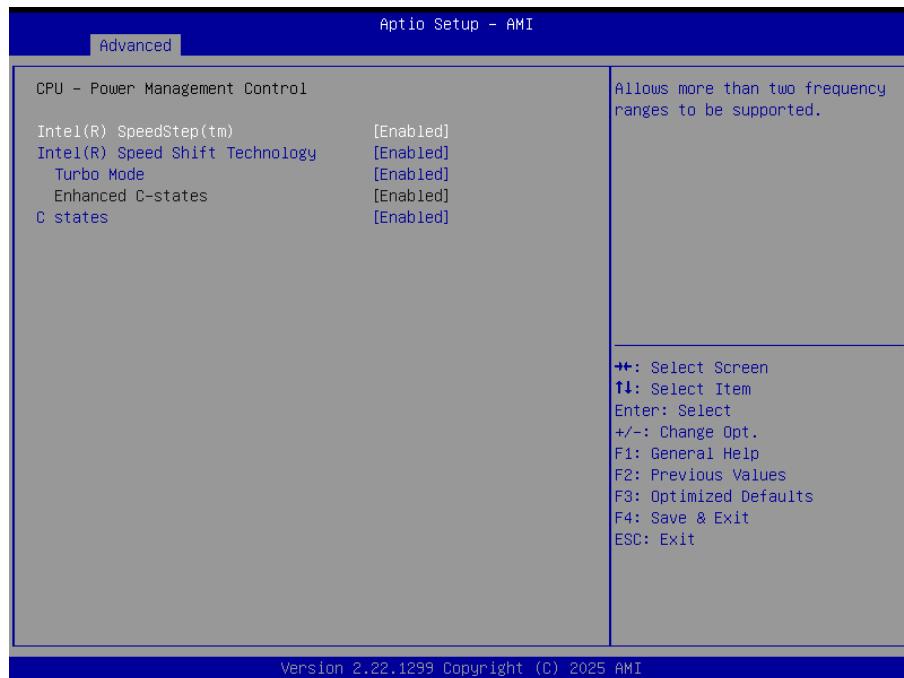
### 4.6.2.1.2 Performance-core Information



### 4.6.2.2 Power & Performance

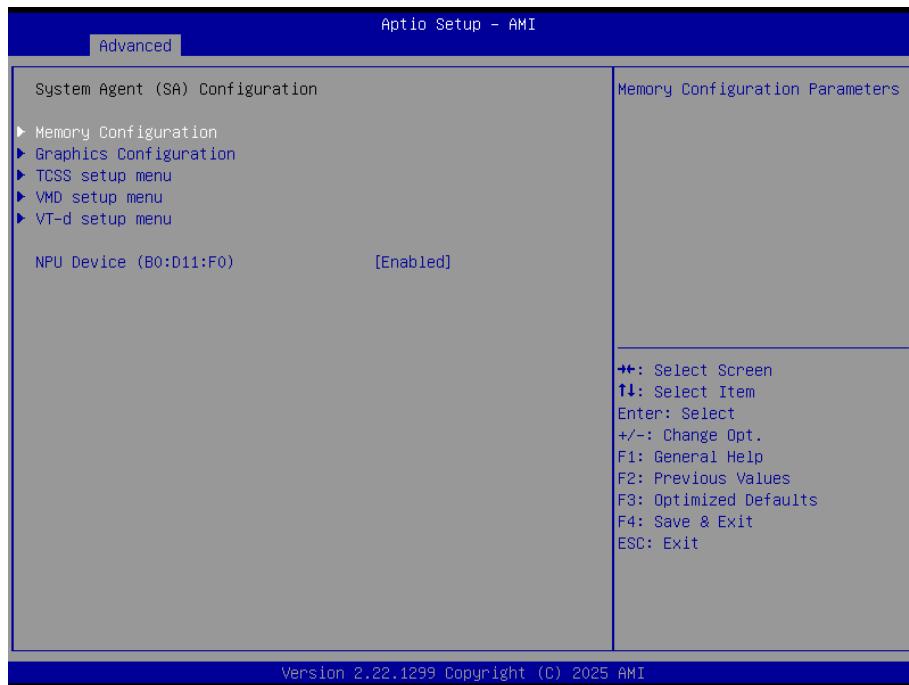


#### 4.6.2.2.1 CPU – Power Management Control



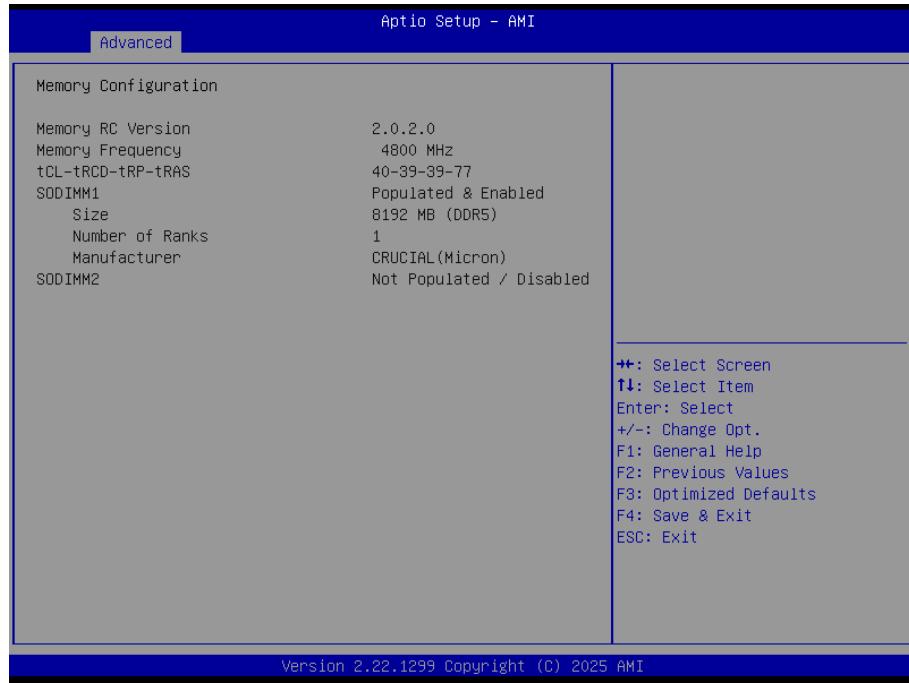
Item	Option	Description
<b>Intel® SpeedStep™</b>	Enabled[ <b>Default</b> ], Disabled	Allows more than two frequency ranges to be supported.
<b>Intel® Speed Shift Technology</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.
<b>Turbo Mode</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled).
<b>C States</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable CPU Power Management.

## 4.6.2.3 System Agent (SA) Configuration



Item	Option	Description
<b>NPU Device (B0:D11:F0)</b>	Enabled[ <b>Default</b> ] Disabled	Enable/Disable NPU(Neural Processing Unit) Device.

### 4.6.2.3.1 Memory Configuration

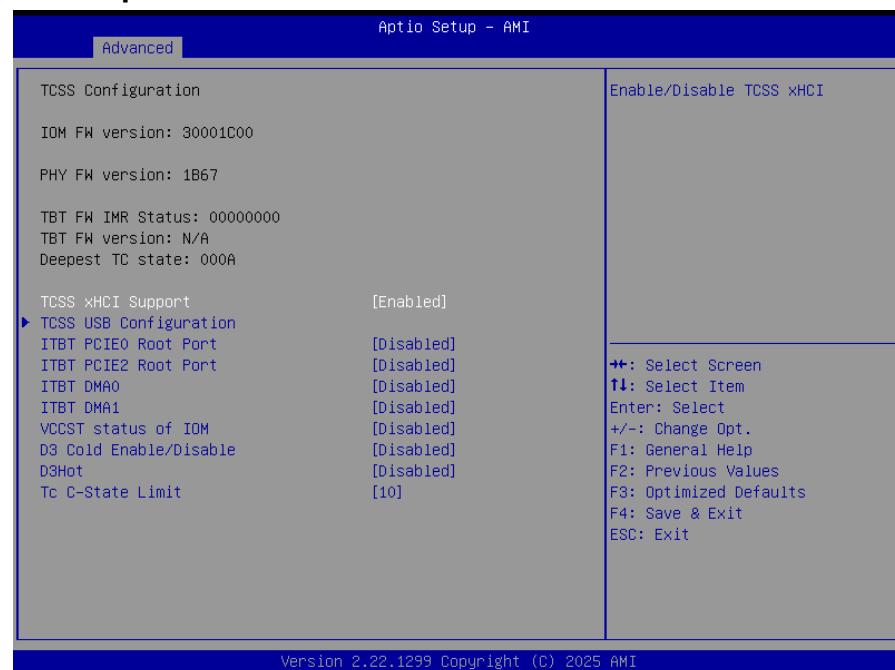


### 4.6.2.3.2 Graphics Configuration



Item	Option	Description
Primary Display	Auto [Default] IGFX HG	Select AUTO set IGD to be Primary Display if no external Graphics Device connected otherwise external Graphics Device detected on first PCIe port will be Primary Display or Select IGFX for IGD to be Primary Display Or Select HG for Hybrid Gfx.

### 4.6.2.3.3 TCSS setup menu



## ESM-ARL User's Manual

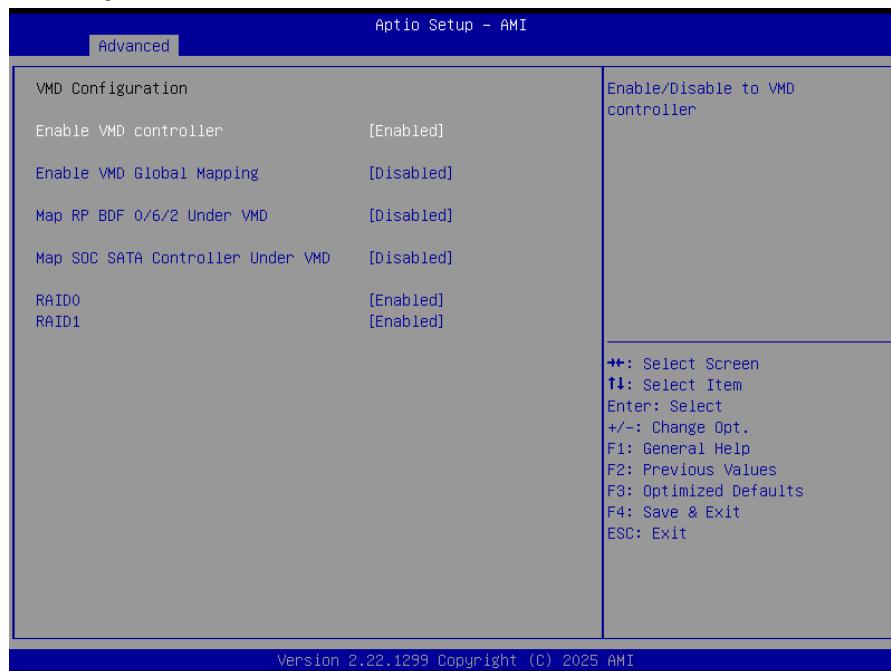
Item	Option	Description
<b>TCSS xHCI Support</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable TCSS xHCI.
<b>ITBT PCIE0 Root Port</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable ITBT PCIE Root.
<b>ITBT PCIE2 Root Port</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable ITBT PCIE Root.
<b>ITBT DMA0</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable ITBT DMA0.
<b>ITBT DMA1</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable ITBT DMA1.
<b>VCCST status of IOM</b>	Disabled Enabled[ <b>Default</b> ]	Enables/Disables VCCST. Enable: Sends VCCST ON message to EC or PMC Disable: Sends VCCST OFF message to EC or PMC.
<b>D3 Cold Enable/Disable</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable D3 Cold. Enable:D3 cold support for IOM is enabled Disable:D3 cold support for IOM is Disabled.
<b>D3Hot</b>	Disabled Enabled[ <b>Default</b> ]	Enable/Disable D3 Hot. Enable:D3 Hot support for IOM is enabled Disable:D3 Hot support for IOM is Disabled.
<b>Tc C-State Limit</b>	Disable 1 2 4 5 6 7 10[ <b>Default</b> ]	BIOS mailbox to limit deepest TCx state.

#### 4.6.2.3.3.1 TCSS USB Configuration



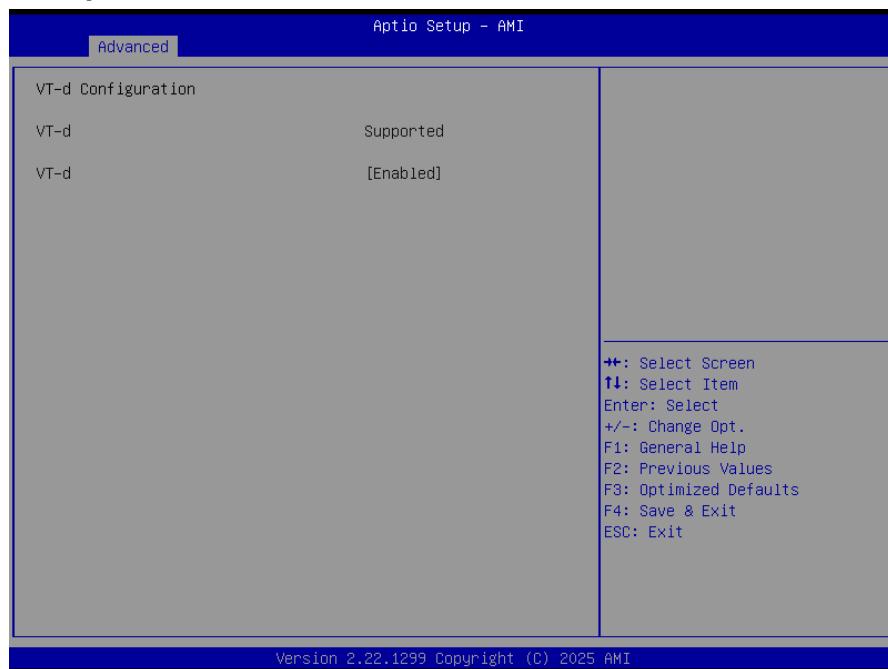
Item	Option	Description
<b>USB CONNECT OVERRIDE</b>	Disabled[ <b>Default</b> ], Enabled	Option will allow VCCSTTPC to turn off even when there is a connection for a USB3 port.
<b>TCSS xDCI Support</b>	Disabled[ <b>Default</b> ], Enabled	Enable/Disable TCSS xDCI.
<b>TCSS CPU USB PDO Programming</b>	Disabled Enabled[ <b>Default</b> ],	Select 'Enabled' if Port Disable Override functionality is used.
<b>TCSS CPU USB Port Disable Override</b>	Disabled[ <b>Default</b> ], Select Per-Pin	Selectively Enable/Disable the corresponding USB port from reporting a Device Connection to the controller.

## 4.6.2.3.4 VMD setup menu

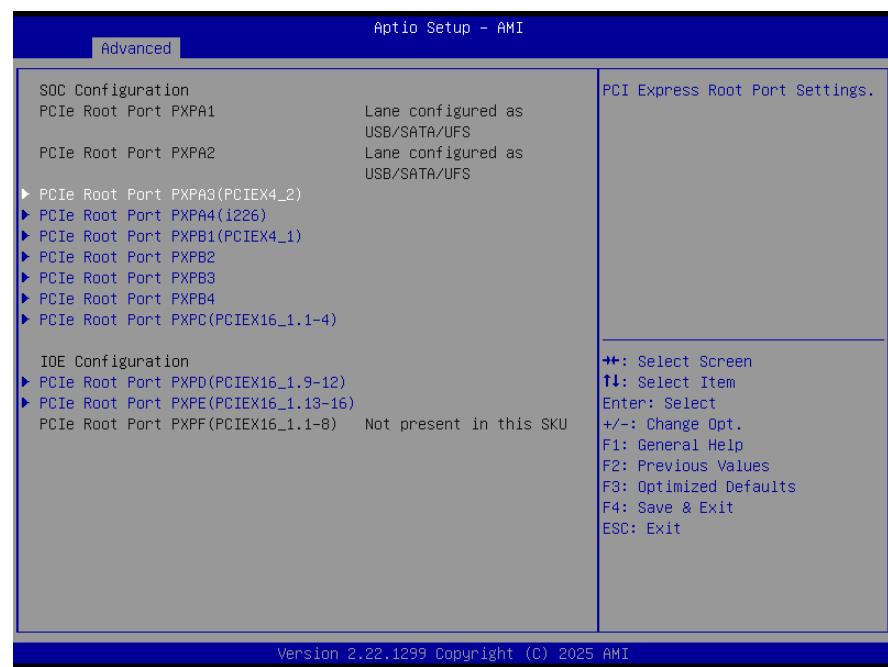


Item	Option	Description
<b>Enable VMD controller</b>	Disabled Enabled <b>[Default]</b>	Enable/Disable to VMD controller.
<b>Enable VMD Global Mapping</b>	Disabled <b>[Default]</b> Enabled	Enable/Disable to VMD Global Mapping.
<b>Map RP BDF 0/6/2 Under VMD</b>	Disabled <b>[Default]</b> Enabled	Map RP BDF 0/6/2 Under VMD
<b>Map SOC SATA Controller Under VMD</b>	Disabled <b>[Default]</b> Enabled	Map SOC SATA Controller Under VMD
<b>RAID0</b>	Disabled Enabled <b>[Default]</b>	Enable/Disable RAID0 support.
<b>RAID1</b>	Disabled Enabled <b>[Default]</b>	Enable/Disable RAID1 support.

#### 4.6.2.3.5 VT-d setup menu

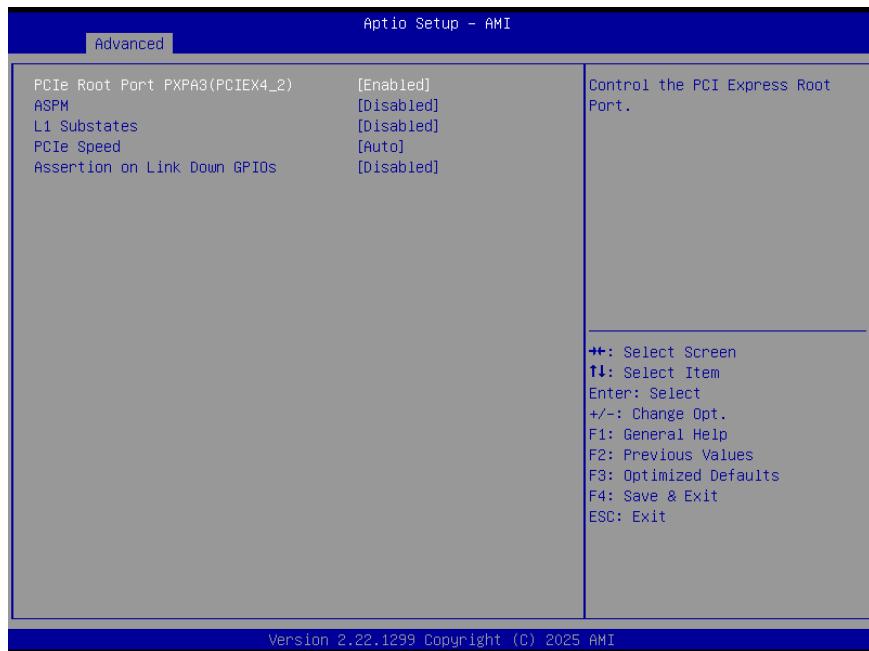


#### 4.6.2.4 PCIE Configuration



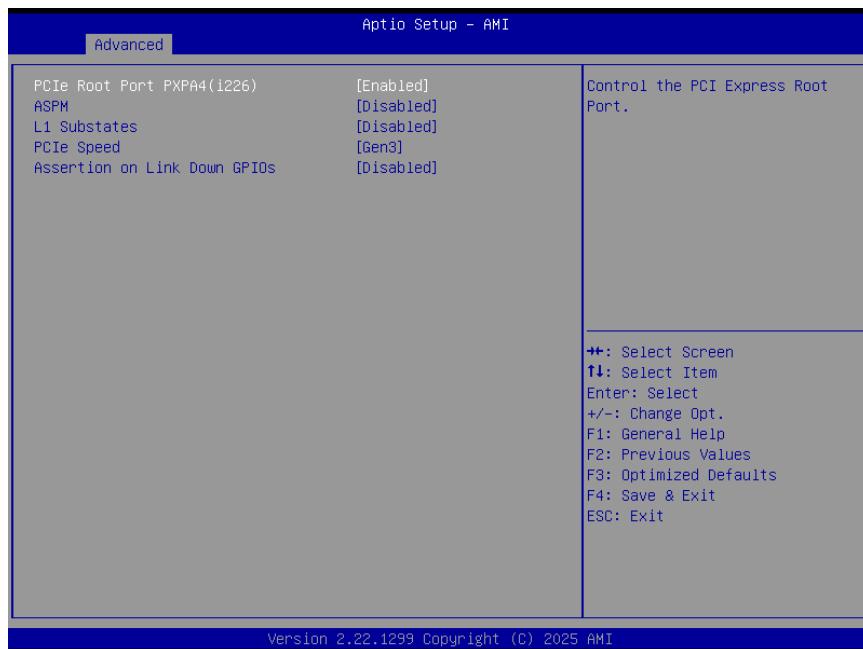
## ESM-ARL User's Manual

### 4.6.2.4.1 PCIe Root Port PXPA3(PCIEX4\_2)



Item	Option	Description
<b>PCIe Root Port PXPA3(PCIEX4_2)</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

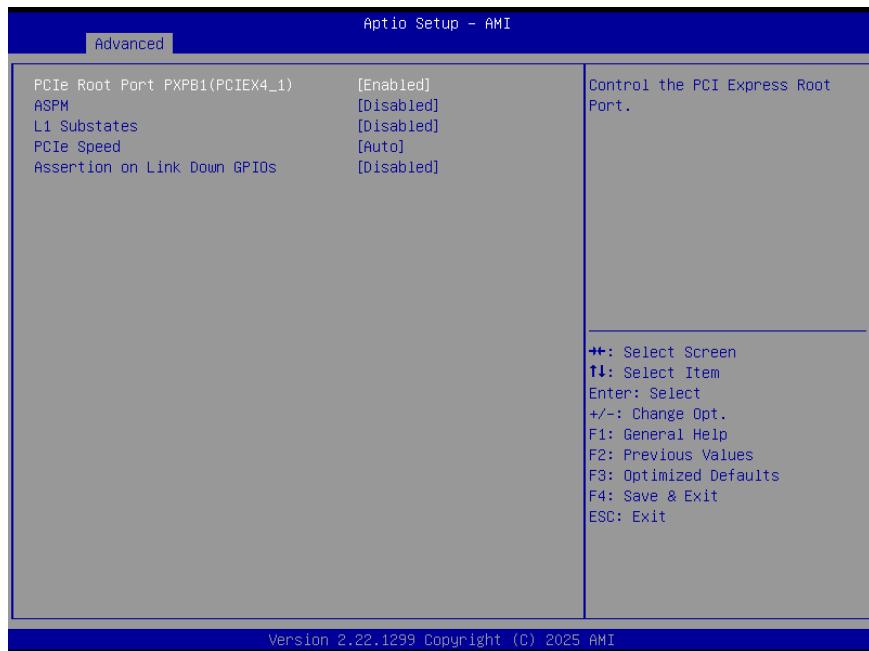
#### 4.6.2.4.2 PCIe Root Port PXPA4(i226)



Item	Option	Description
PCIe Root Port PXPA4(i226)	Disabled Enabled[ <b>Default</b> ],	Control the PCI Express Root Port.
ASPM	Disabled[ <b>Default</b> ], L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled[ <b>Default</b> ] L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
PCIe Speed	Auto Gen1 Gen2 Gen3[ <b>Default</b> ] Gen4 Gen5	Configure PCIe Speed.
Assertion on Link Down GPIOs	Disabled[ <b>Default</b> ], Enabled	GPIO Assertion on Link Down.

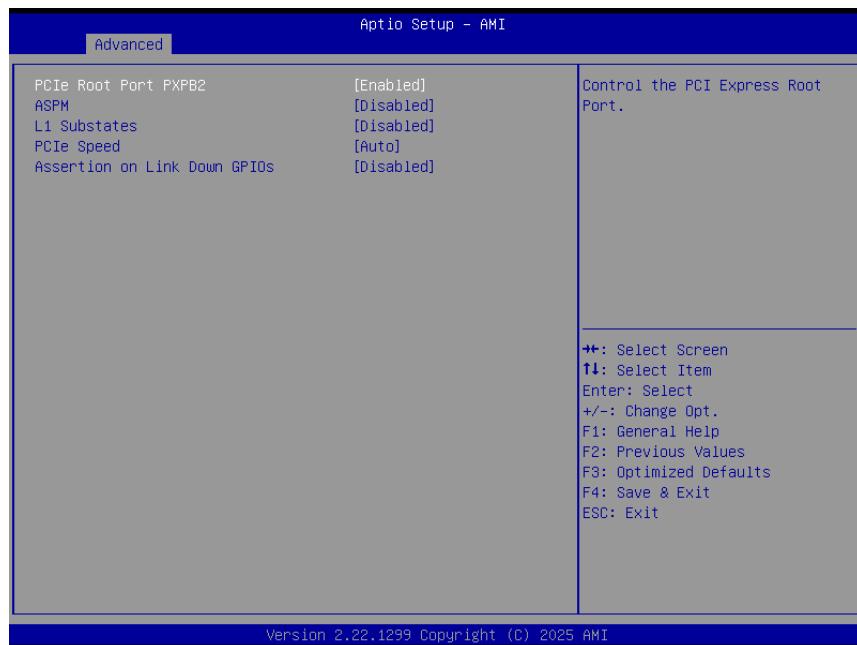
## ESM-ARL User's Manual

### 4.6.2.4.3 PCIe Root Port PXPB1(PCIEX4\_1)



Item	Option	Description
<b>PCIe Root Port PXPB1(PCIEX4_1)</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

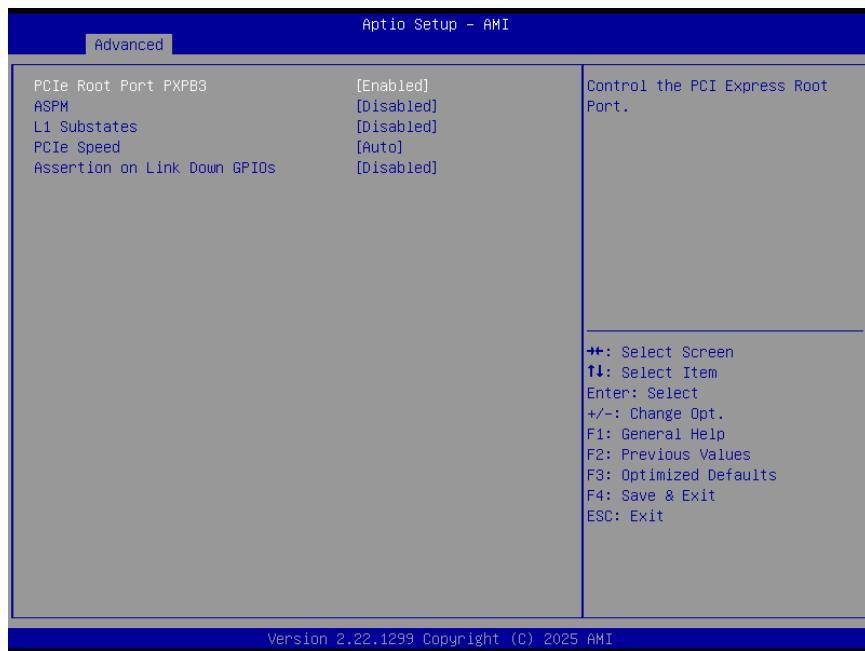
#### 4.6.2.4.4 PCIe Root Port PXPB2



Item	Option	Description
<b>PCIe Root Port PXPB2</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

## ESM-ARL User's Manual

### 4.6.2.4.5 PCIe Root Port PXPB3



Item	Option	Description
<b>PCIe Root Port PXPB3</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

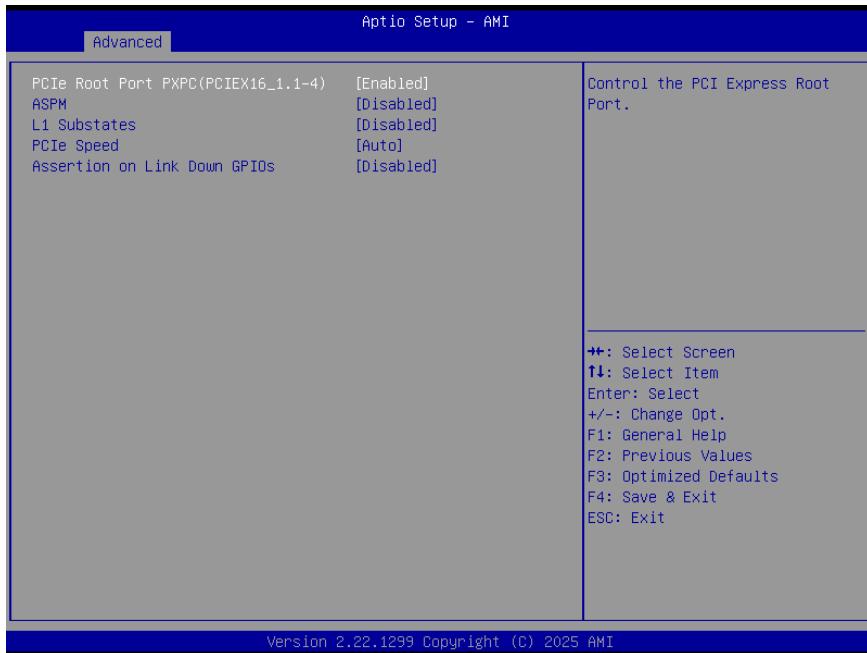
#### 4.6.2.4.6 PCIe Root Port PXPB4



Item	Option	Description
<b>PCIe Root Port PXPB4</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

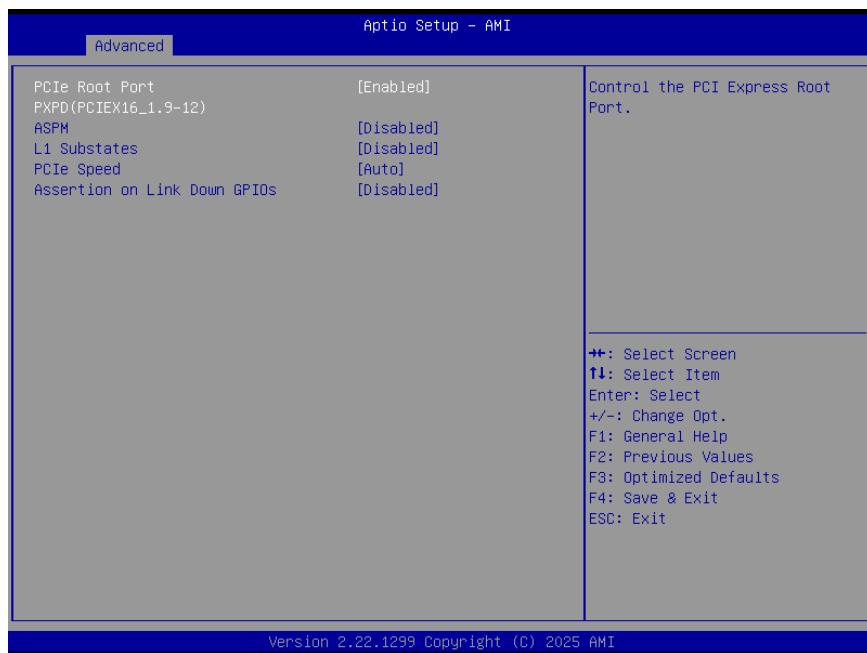
## ESM-ARL User's Manual

### 4.6.2.4.7 PCIe Root Port PXPC(PCIEX16\_1.1-4)



Item	Option	Description
<b>PCIe Root Port PXP C(PCIEX16_1.1-4)</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

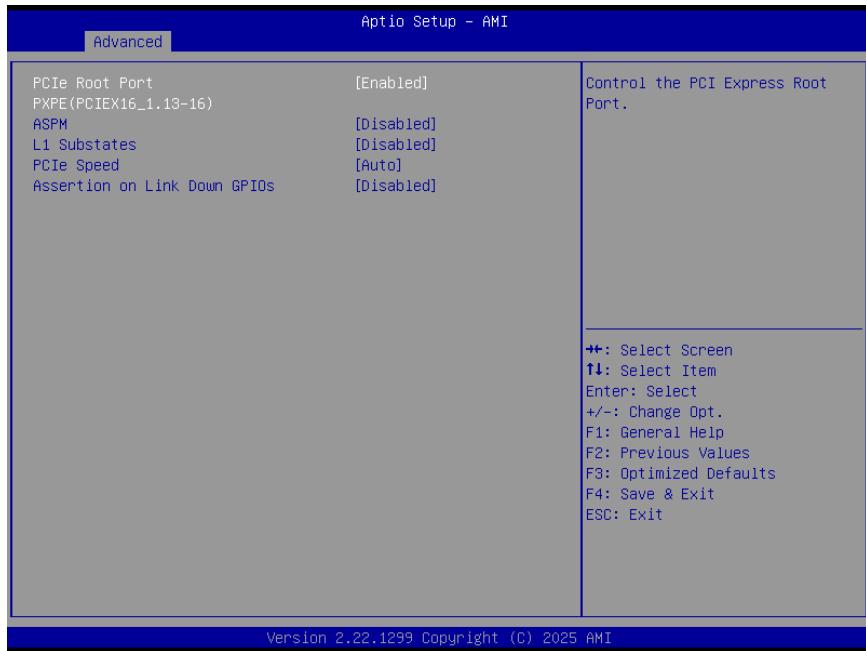
#### 4.6.2.4.8 PCIe Root Port PXPD(PCIE16\_1.9-12)



Item	Option	Description
<b>PCIe Root Port PXP D(PCIE16_1.9-12)</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

## ESM-ARL User's Manual

### 4.6.2.4.9 PCIe Root Port PXPE(PCIE16\_1.13-16)

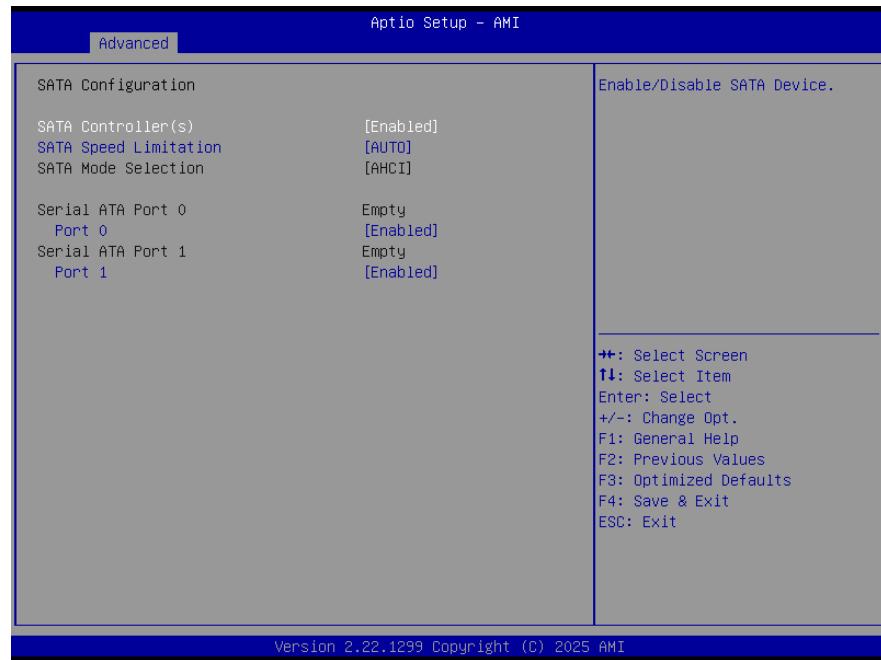


Item	Option	Description
<b>PCIe Root Port PXP E(PCIE16_1.13-16)</b>	Disabled Enabled <b>[Default]</b> ,	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled <b>[Default]</b> , L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled <b>[Default]</b> L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto <b>[Default]</b> Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed.
<b>Assertion on Link Down GPIOs</b>	Disabled <b>[Default]</b> , Enabled	GPIO Assertion on Link Down.

#### 4.6.2.5 PCH-IO Configuration



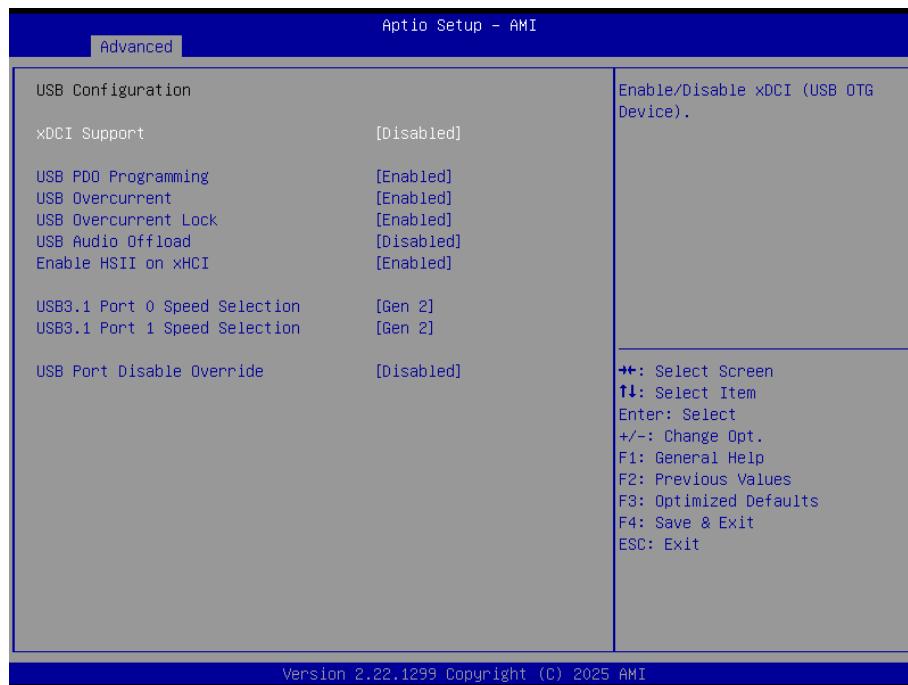
##### 4.6.2.5.1 SATA Configuration



Item	Options	Description
<b>SATA Controller(s)</b>	Enabled[ <b>Default</b> ] Disabled,	Enable/Disable SATA Device.
<b>SATA Speed Limitation</b>	AUTO[ <b>Default</b> ] Gen1 1.5 Gb/s Gen2 3.0 Gb/s Gen3 6.0 Gb/s	Set the maximum speed of SATA.
<b>Port 0</b>	Enabled[ <b>Default</b> ] Disabled	Enable or Disable SATA Port.

Port 1	Enabled[ <b>Default</b> ] Disabled	Enable or Disable SATA Port.
--------	---------------------------------------	------------------------------

### 4.6.2.5.2 USB Configuration



Item	Options	Description
<b>xCXI Support</b>	Disabled[ <b>Default</b> ], Enabled	Enable/Disable xDCI (USB OTG Device).
<b>USB PDO Programming</b>	Disabled, Enabled[ <b>Default</b> ]	Select 'Enabled' if Port Disable Override functionality is used.
<b>USB Overcurrent</b>	Disabled, Enabled[ <b>Default</b> ]	Select 'Disabled' for pin-based debug. If pin-based debug is enabled but USB overcurrent is not disabled, USB DbC does not work.
<b>USB Overcurrent Lock</b>	Disabled, Enabled[ <b>Default</b> ]	Select 'Enabled' if Overcurrent functionality is used. Enabling this will make xHCI controller consume the Overcurrent mapping data.
<b>USB Audio Offload</b>	Disabled, Enabled[ <b>Default</b> ]	Enable/Disable USB Audio Offload functionality.
<b>Enable HSII on xHCI</b>	Disabled, Enabled[ <b>Default</b> ]	Enable/Disable HSII feature. It may lead to increased power consumption.
<b>USB3.1 Port 0 Speed Selection</b>	Gen2[ <b>Default</b> ] Gen1	USB3.1 Speed selection; Gen1 or Gen2.
<b>USB3.1 Port 1 Speed Selection</b>	Gen2[ <b>Default</b> ] Gen1	USB3.1 Speed selection; Gen1 or Gen2.
<b>USB Port Disable Override</b>	Disabled[ <b>Default</b> ], Select Per-Pin	Selectively Enable/Disable the corresponding USB port from reporting a Device Connection to the controller.

#### 4.6.2.5.3 HD Audio Configuration



Item	Option	Description
<b>HD Audio</b>	Disabled Enabled <b>[Default]</b>	Control Detection of the HD-Audio device. Disable = HDA will be unconditionally disabled Enabled = HDA will be unconditionally enabled.

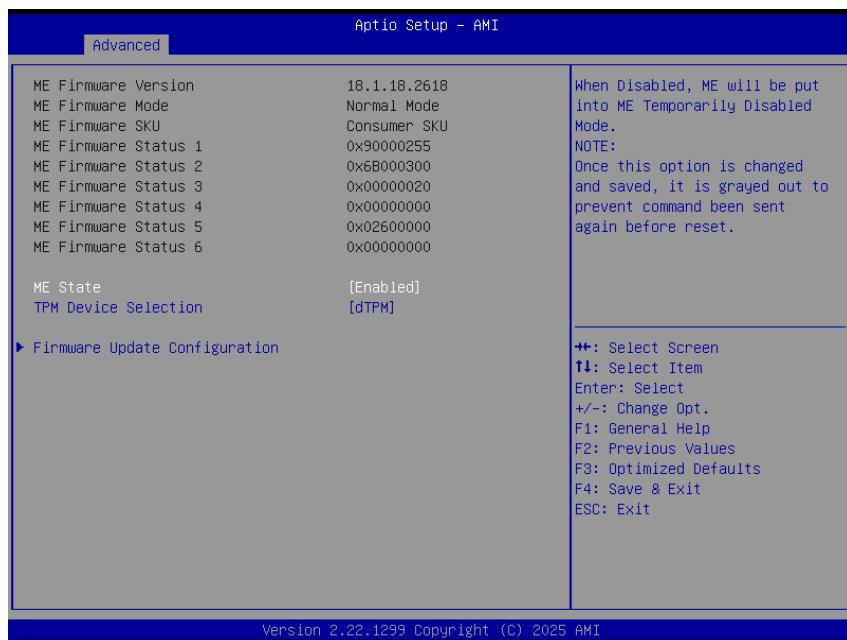
#### 4.6.2.5.4 SerialIO Configuration



Item	Option	Description
<b>I2C0 Controller</b>	Disabled Enabled <b>[Default]</b>	Enables/Disables SerialIO Controller. If given device is Function 0 PSF disabling is skipped. PSF default will remain and device PCI CFG Space will still be visible.

		<p>This is needed to allow PCI enumerator access functions above 0 in a multifunction device. The following devices depend on each other: I2C0 and I2C1,2,3 UART0 and UART1, SPI0,1 UART2 and I2C4,5. UART 0 (00:30:00) cannot be disabled when: 1. Child device is enabled like CNVi Bluetooth (\SB.PC00.UA00.BTH0) UART 0 (00:30:00) cannot be disabled when: 1. Child device is enabled like CNVi Bluetooth (_SB.PC00.UA00.BTH0). UART 0 (00:30:00) cannot be enabled when: 1. I2S Audio codec is enabled (_SB.PC00.I2C0.HDAC)</p>
--	--	---

### 4.6.2.6 PCH-FW Configuration



Item	Option	Description
<b>ME State</b>	Disabled Enabled <b>[Default]</b> ,	When Disabled ME will be put into ME Temporarily Disabled Mode.
<b>TPM Device Selection</b>	<b>dTPM</b> <b>[Default]</b> PTT	Select TPM device: PTT or dTPM. PTT-Enables PTT in SkuMgr dTPM 1.2-Disables PTT in SkuMgr Warning! PTT/dTPM will be disabled and all data saved on it will be lost.

#### 4.6.2.6.1 Firmware Update Configuration



Item	Option	Description
ME FW Image Re-Flash	Disabled[Default], Enabled	Enable/Disable Me FW Image Re-Flash function.

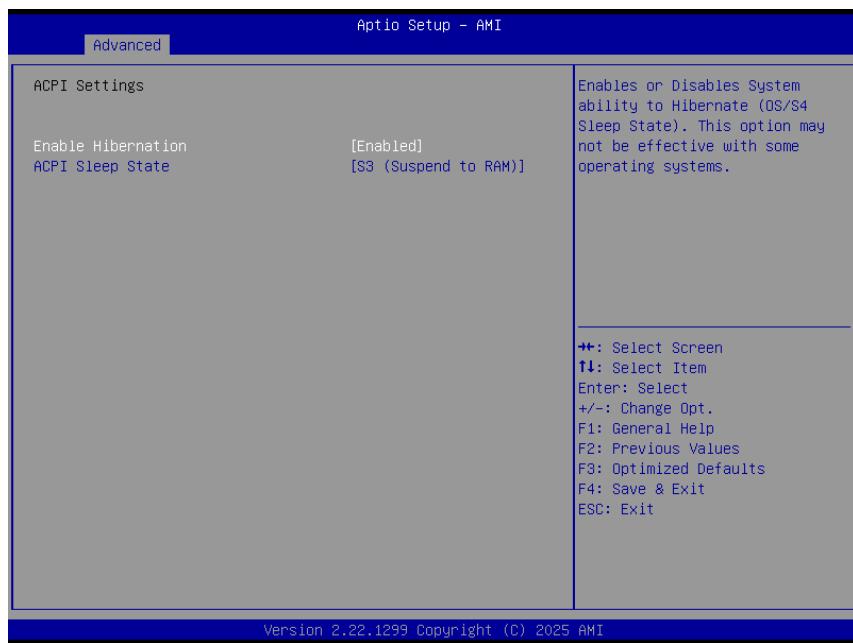
#### 4.6.2.7 Trusted Computing



Item	Options	Description
Security Device Support	Disabled, Enabled[Default]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

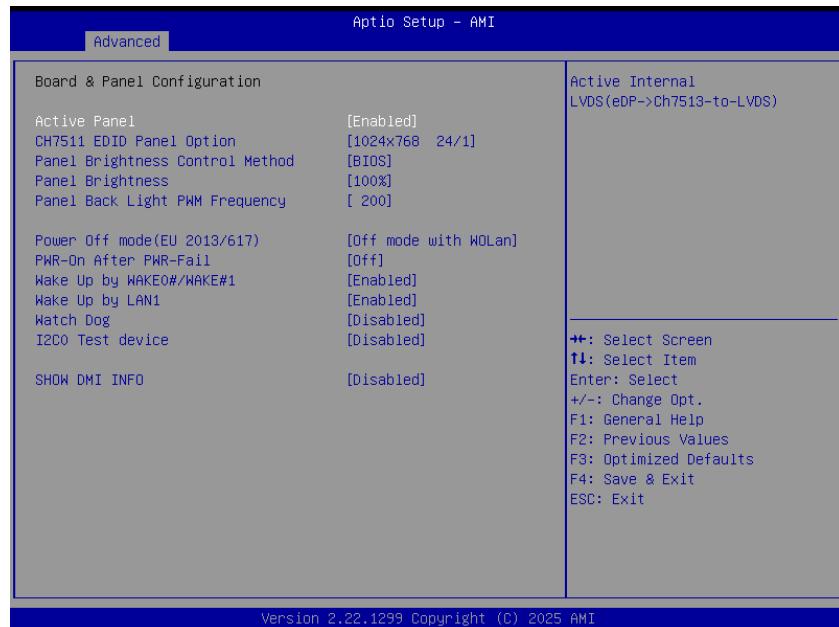
## ESM-ARL User's Manual

### 4.6.2.8 APCI Settings



Item	Options	Description
<b>Enable Hibernation</b>	Disabled Enabled[ <b>Default</b> ],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some OS.
<b>ACPI Sleep State</b>	Suspend Disabled, S3 (Suspend to RAM)[ <b>Default</b> ]	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

### 4.6.2.9 Board & Panel Configuration



Item	Option	Description
<b>Active Panel</b>	Disabled Enabled[ <b>Default</b> ]	Active Internal LVDS(eDP->Ch7511-to-LVDS).
<b>CH7511 EDID Panel Option</b>	1024x768 24/1[ <b>Default</b> ] 800x600 18/1 1024x768 18/1 1366x768 18/1 1024x600 18/1 1280x800 18/1 1920x1200 24/2 1920x1080 18/2 1280x1024 24/2 1440x900 18/2 1600x1200 24/2 1366x768 24/1 1920x1080 24/2 1680x1050 24/2	Port-EDP to LVDS(Chrotel 7511) Panel EDID Option.
<b>Panel Brightness Control Method</b>	BIOS[ <b>Default</b> ] BR Button VR OS driver	Panel Brightness Control Method. 1.BIOS 2.Brightness Button 3.Variable Resistor 4.OS Driver.
<b>Panel Brightness</b>	00% 25% 50% 75% 100%[ <b>Default</b> ]	Select Panel back light PWM duty.
<b>Panel Back Light PWM Frequency</b>	200[ <b>Default</b> ] 300 400 500 700 1k 2k 3k 5k 10k 20k	Select Panel back light PWM Frequency.
<b>Power Off mode(EU 2013/617)</b>	Traditional_S5 Off mode w/o WOL(ErP) Off mode with WOLan[ <b>Default</b> ]	Off mode with WOLan: Wakeup from Lan1/PWR button Off mode w/o WOL(ErP) Wakeup from PWR button.
<b>PWR-On After PWR-Fail</b>	Off[ <b>Default</b> ] On Last state	AC loss resume.
<b>Wake Up by WAKE0#/WAKE#1</b>	Disabled Enabled[ <b>Default</b> ]	Wake Up by Ring from S3/S4/S5.
<b>Wake Up by LAN1</b>	Disabled Enabled[ <b>Default</b> ]	Wake Up by LAN1 from S3/S4/S5.
<b>Watch Dog</b>	Disabled[ <b>Default</b> ] 30 sec 40 sec	Select WatchDog.

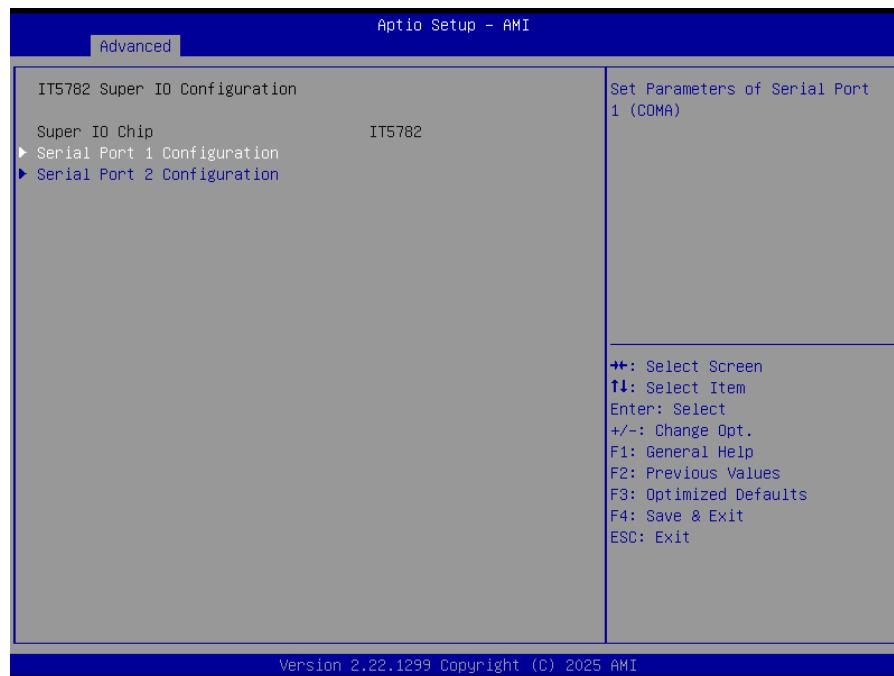
## ESM-ARL User's Manual

	50 sec 1 min 2 min 10 min 30 min	
<b>I2C0 Test device</b>	Disabled[ <b>Default</b> ] Enabled	HID of I2C0 is SPBA0000(CTB-20 NCT5655, 0x20).
<b>SHOW DMI INFO</b>	Disabled[ <b>Default</b> ] Enabled	SHOW DMI INFO.

### 4.6.2.10 IT5782 Super IO Configuration

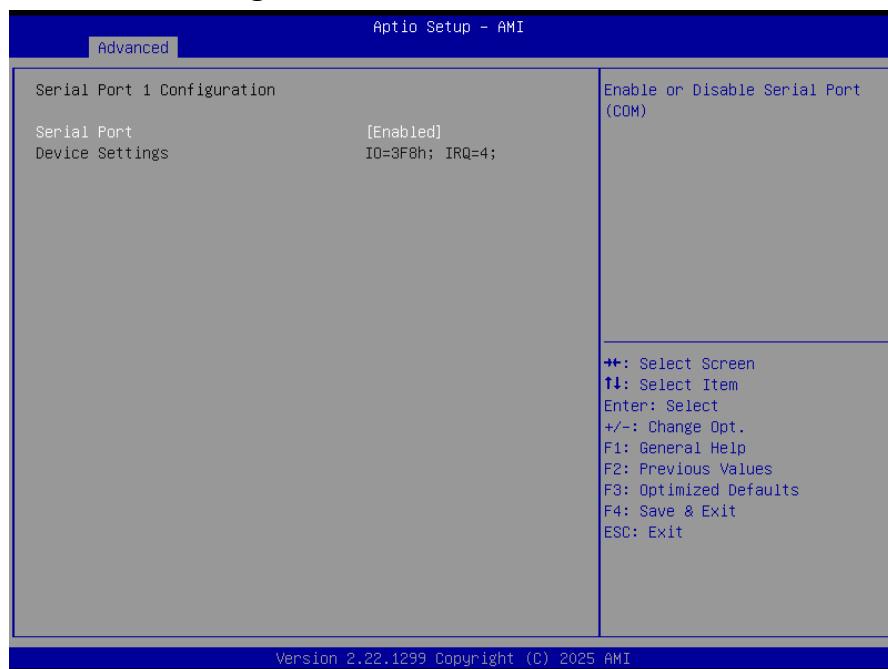
You can use this item to set up or change the IT5782 Super IO configuration for serial ports.

Please refer to 4.6.2.10.1 ~ 4.6.2.10.2 for more information.



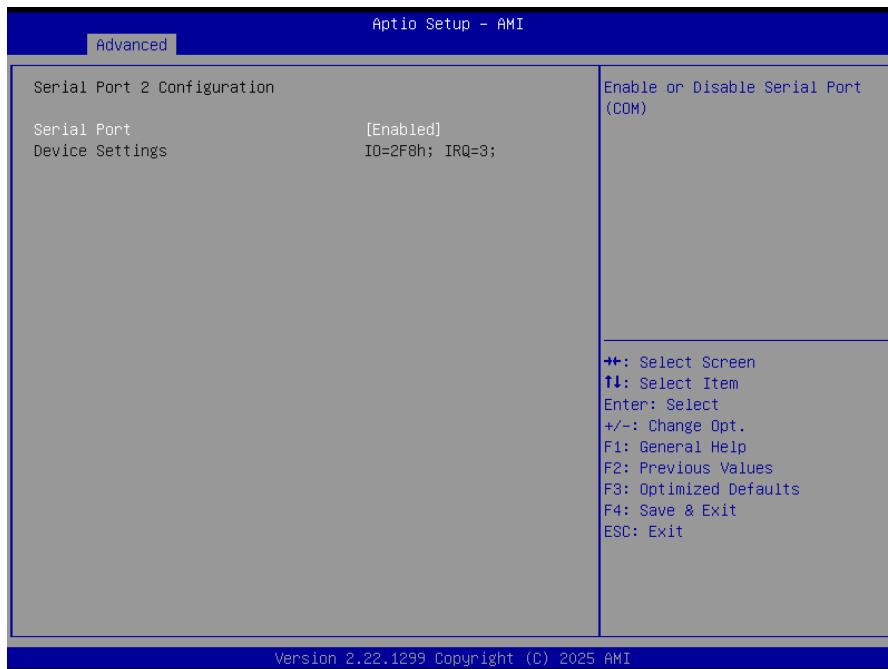
Item	Description
<b>Serial Port 1 Configuration</b>	Set Parameters of Serial Port 1 (COMA).
<b>Serial Port 2 Configuration</b>	Set Parameters of Serial Port 2 (COMB).

#### 4.6.2.10.1 Serial Port 1 Configuration



Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

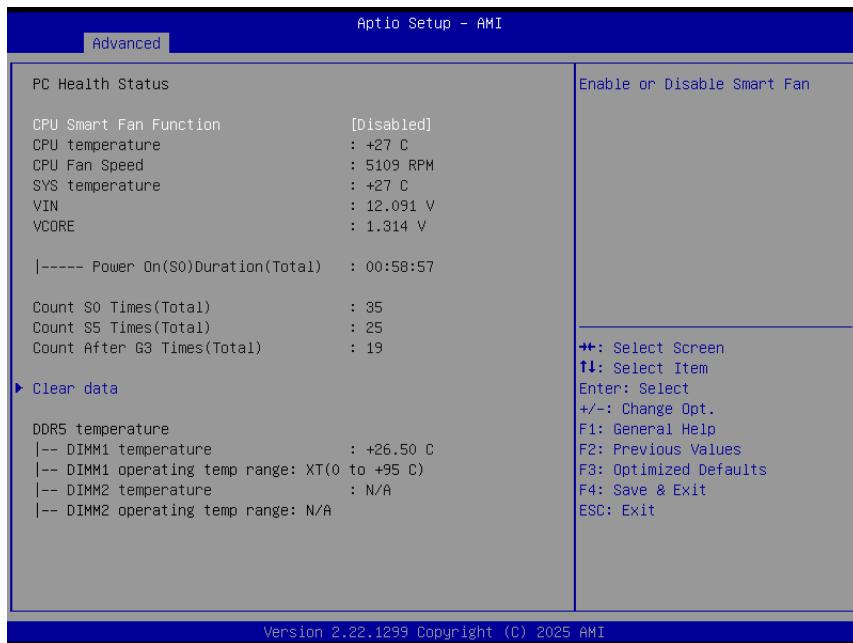
#### 4.6.2.10.2 Serial Port 2 Configuration



Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

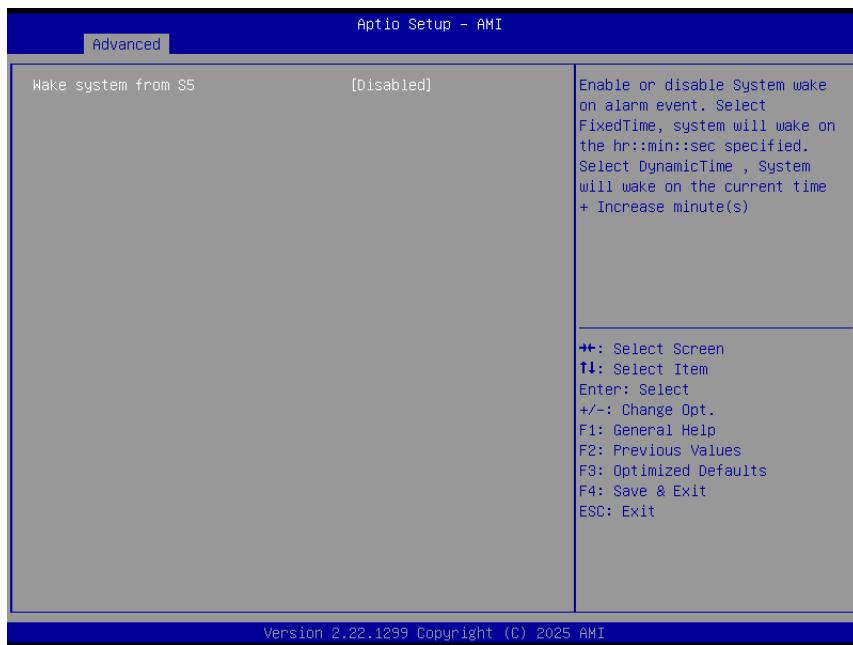
## ESM-ARL User's Manual

### 4.6.2.11 EC 5782 HW monitor



Item	Options	Description
<b>CPU Smart Fan Function</b>	Enabled, Disabled <b>[Default]</b>	Enables or Disables Smart Fan.

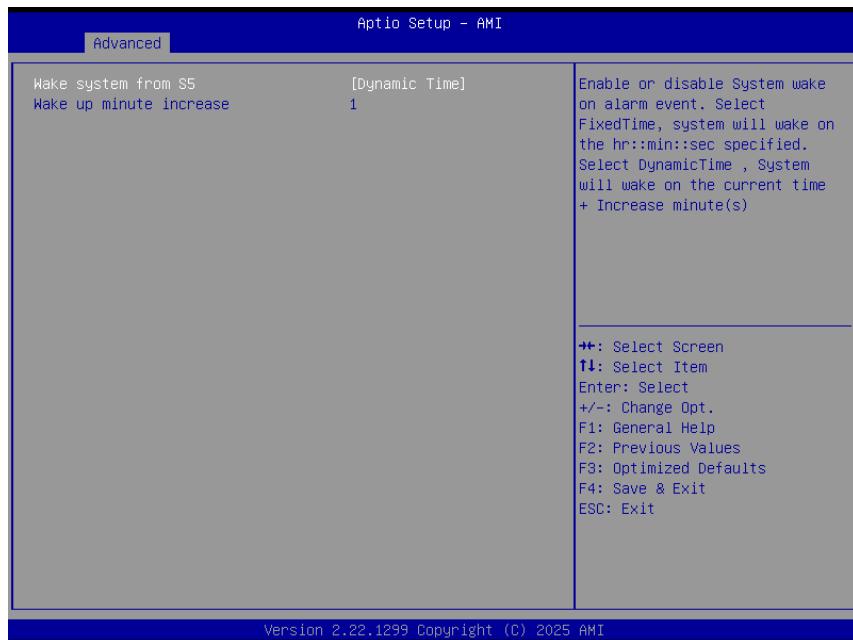
### 4.6.2.12 S5 RTC Wake Settings



Item	Options	Description
<b>Wake system from S5</b>	<b>Disabled</b> <b>[Default]</b> , Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).



Item	Options	Description
<b>Wake system from S5</b>	Disabled, Fixed Time <b>[Default]</b> Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr:min::sec specified. Select Dynamic Times, System will wake on the current time + Increase minute(s).
<b>Wake up day of week</b>	Disabled <b>[Default]</b> , Monday-Friday Monday-Saturday	Wake up day of week. (Monday-Friday) or (Monday-Saturday).
<b>Wake up day</b>	1-31	Select 0 For daily system wake up 1-31 for which day of the month that you would like the system to wake up.
<b>Wake up hour</b>	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
<b>Wake up minute</b>	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
<b>Wake up second</b>	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.



Item	Options	Description
<b>Wake system from S5</b>	Disabled, Fixed Time Dynamic Time <b>[Default]</b>	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Times, System will wake on the current time + Increase minute(s).
<b>Wake up minute increase</b>	1-5	1-5.

## 4.6.2.13 Serial Port Console Redirection



Item	Options	Description
<b>Console Redirection</b>	Disabled <b>[Default]</b> , Enabled	Console Redirection Enable or Disable.
<b>Console Redirection EMS</b>	Disabled <b>[Default]</b> , Enabled	Console Redirection Enable or Disable.

## 4.6.2.13.1 COM0

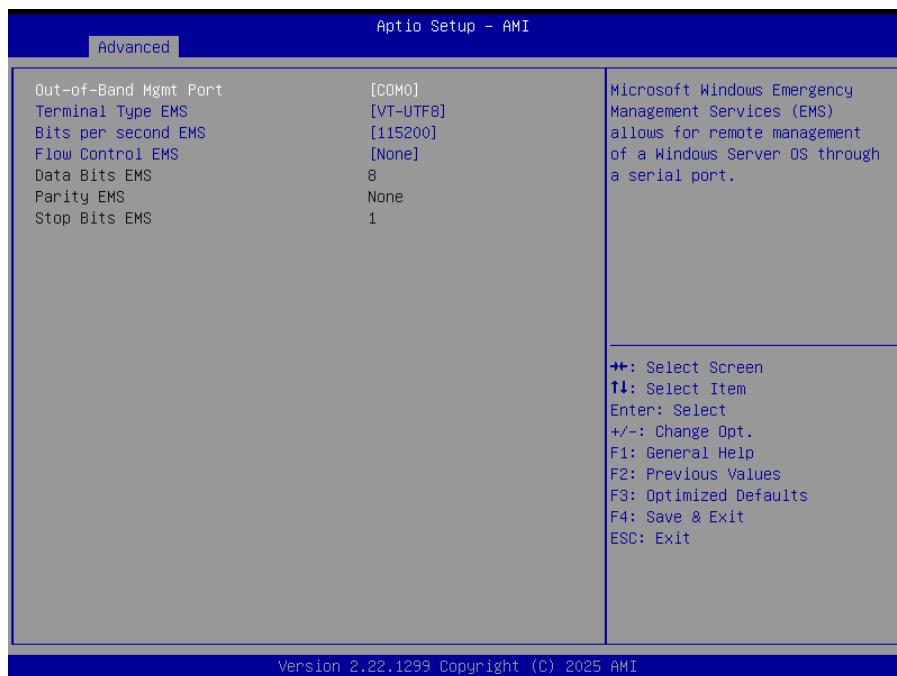


Item	Option	Description
Terminal Type	VT100 VT100+ VT-UTF8 ANSI[Default],	Emulation: ANSI: Extender ASCII char set. VT100: ASCII char set. VT100+:Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
Bits per second	9600 19200 38400 57600 115200[Default]	Select serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Data Bits	7 8[Default]	Data Bits.
Parity	None[Default] Even Odd Mark Space	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
Stop Bits	1[Default] 2	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
Flow Control	None[Default] Hardware RTS/CTS	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can

## ESM-ARL User's Manual

		be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
<b>VT-UTF8 Combo Key Support</b>	Disabled Enabled[ <b>Default</b> ]	Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.
<b>Recorder Mode</b>	Disabled[ <b>Default</b> ] Enabled	With this mode enabled only text will be sent. This is to capture Terminal data.
<b>Resolution 100x31</b>	Disabled[ <b>Default</b> ] Enabled	Enables or disables extended terminal resolution.
<b>Putty KeyPad</b>	VT100[ <b>Default</b> ] Intel Linux XTERMR6 SCO ESCN VT400	Select FunctionKey and KeyPad on Putty.

### 4.6.2.13.2 Console Redirection EMS

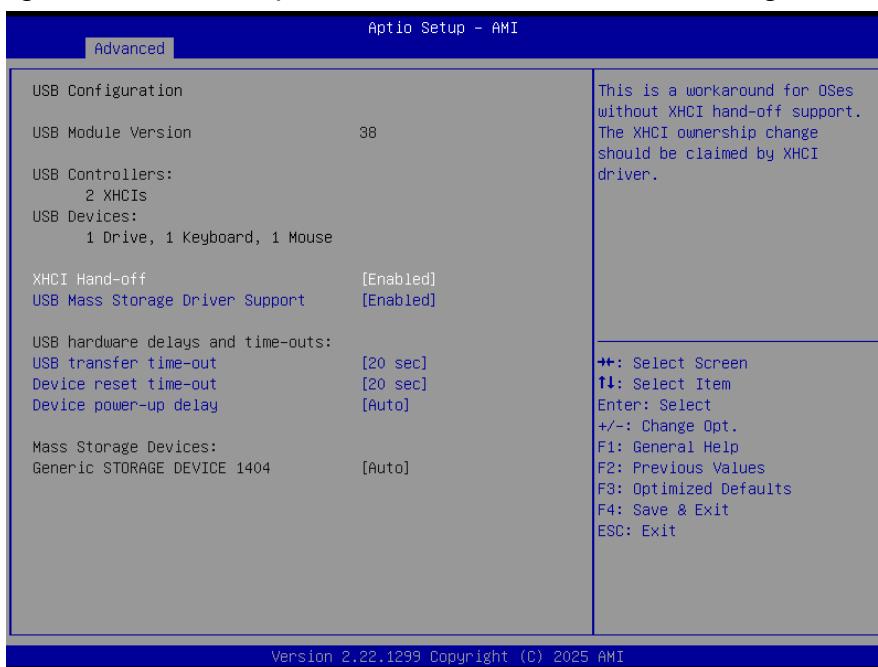


Item	Option	Description
<b>Out-of-Band Mgmt Port</b>	COM0[ <b>Default</b> ],	Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.
<b>Terminal Type</b>	VT100 VT100+ VT-UTF8[ <b>Default</b> ] ANSI,	Emulation: ANSI: Extender ASCII char set. VT100: ASCII char set. VT100+:Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
<b>Bits per second EMS</b>	9600 19200	Select serial port transmission speed. The speed must be matched on the other side.

	38400 57600 115200[Default]	Long or noisy lines may require lower speeds.
Flow Control EMS	None[Default] Hardware RTS/CTS	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

#### 4.6.2.14 USB Configuration

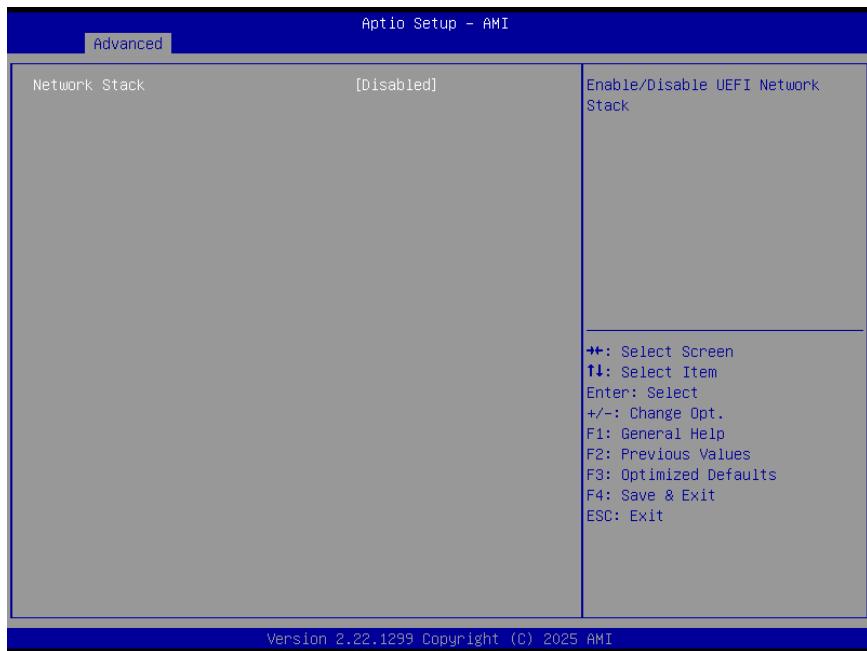
The USB Configuration menu helps read USB information and configures USB settings.



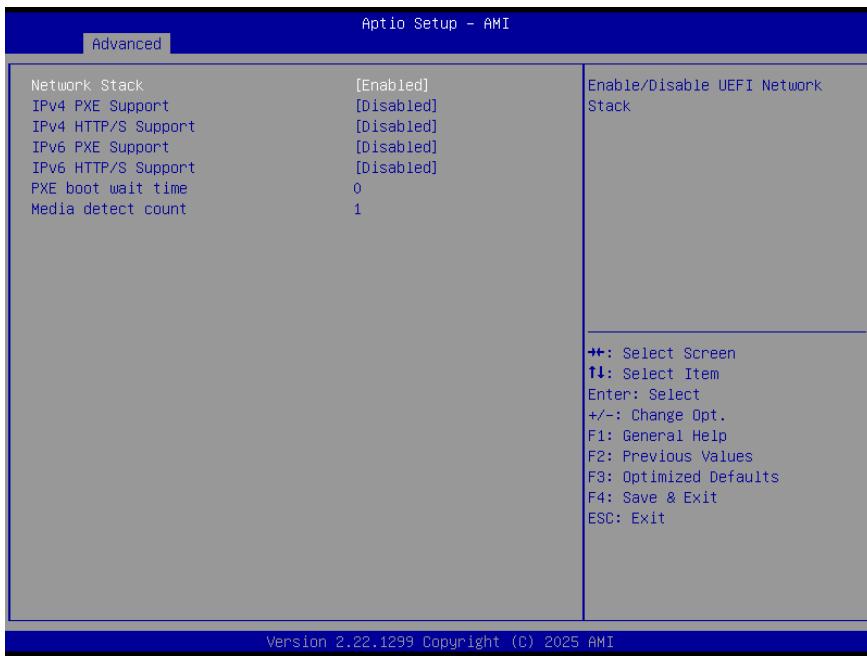
Item	Options	Description
XHCI Hand-off	Enabled[Default] Disabled	This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	Disabled Enabled[Default]	Enable/Disable USB Mass Storage Driver Support.
USB transfer time-out	1 sec 5 sec 10 sec 20 sec[Default]	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	10 sec 20 sec[Default] 30 sec 40 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	Auto[Default] Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken from Hub descriptor.

## ESM-ARL User's Manual

### 4.6.2.15 Network Stack Configuration



Item	Options	Description
<b>Network Stack</b>	Enabled Disabled <b>[Default]</b>	Enable/Disable UEFI Network Stack.



Item	Options	Description
<b>Network Stack</b>	Enabled <b>[Default]</b> Disabled	Enable/Disable UEFI Network Stack.
<b>Ipv4 PXE Support</b>	Enabled Disabled <b>[Default]</b>	Enable Ipv4 PXE Boot Support. If disabled IPV4 PXE boot option will not be created.
<b>Ipv4 HTTP Support</b>	Enabled Disabled <b>[Default]</b>	Enable Ipv4 HTTP Boot Support. If disabled IPV4 HTTP boot option will not be created.

<b>Ipv6 PXE Support</b>	Enabled Disabled[ <b>Default</b> ]	Enable Ipv6 PXE Boot Support. If disabled IPV6 PXE boot option will not be created.
<b>Ipv6 HTTP Support</b>	Enabled Disabled[ <b>Default</b> ]	Enable Ipv6 HTTP Boot Support. If disabled IPV4 HTTP boot option will not be created.
<b>PXE boot wait time</b>	0	Wait time to press ESC key to abort the PXE boot.
<b>Media detect count</b>	1	Number of times presence of media will be checked.

#### 4.6.2.16 NVMe Configuration



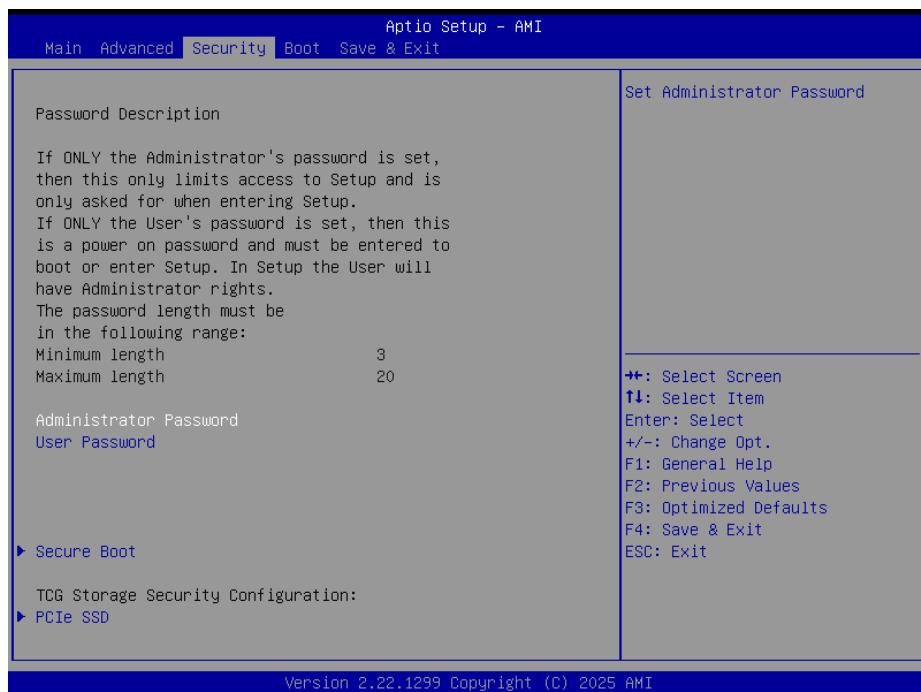
## ESM-ARL User's Manual

### 4.6.2.16.1 PCIe SSD



Item	Option	Description
<b>Self Test Option</b>	Short [ <b>Default</b> ] Extended	Select either Short or Extended Self Test. Short option will take couple of minutes and extended option will take several minutes to complete.
<b>Self Test Action</b>	Controller only test [ <b>Default</b> ] Controller and NameSpace Test	Select either to test Controller alone or Controller and NameSpace. Selecting Controller and Namespace option will take lot longer to complete the test.
<b>Run Device Self Test</b>	Run Device Self Test.	

### 4.6.3 Security



#### ● Administrator Password

Set setup Administrator Password

#### ● User Password

Set User Password

#### 4.6.3.1 Secure Boot



## ESM-ARL User's Manual

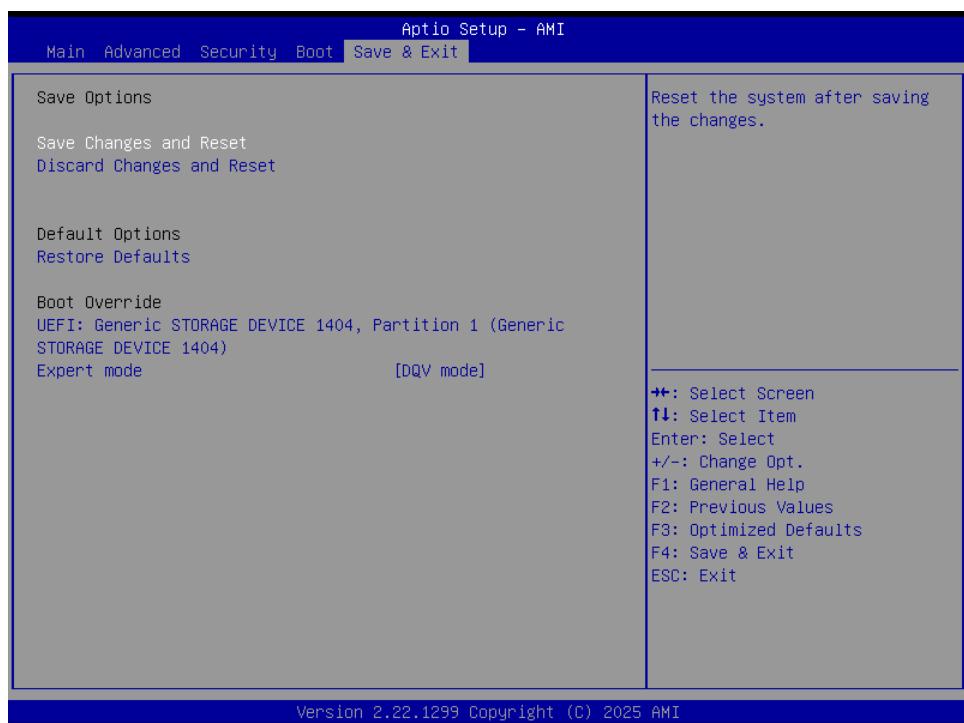
Item	Option	Description
Secure Boot	Disabled[ <b>Default</b> ] Enabled	Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset.
Secure Boot Mode	Standard Custom[ <b>Default</b> ]	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.

### 4.6.4 Boot



Item	Option	Description
Setup Prompt Timeout	1~ 65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On[ <b>Default</b> ] Off	Select the keyboard NumLock state
Fast Boot	Disabled[ <b>Default</b> ] Enabled	Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options.
Quiet Boot	Disabled[ <b>Default</b> ] Enabled	Enables or disables Quiet Boot option
Boot Option #1/2	Set the system boot order.	

## 4.6.5 Save and Exit



### 4.6.5.1 Save Changes and Reset

Reset the system after saving the changes.

### 4.6.5.2 Discard Changes and Reset

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

### 4.6.5.3 Restore Defaults

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

### 4.6.5.4 Launch EFI Shell from filesystem device

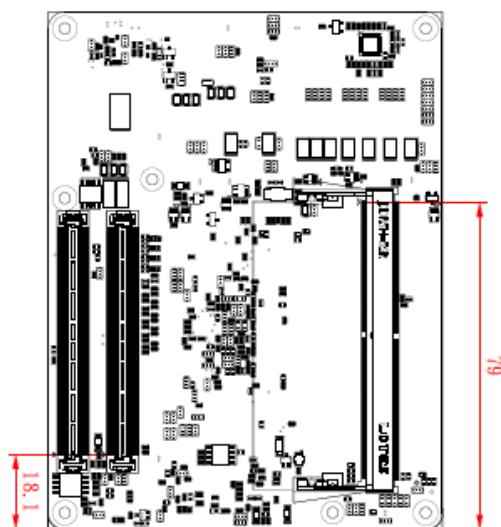
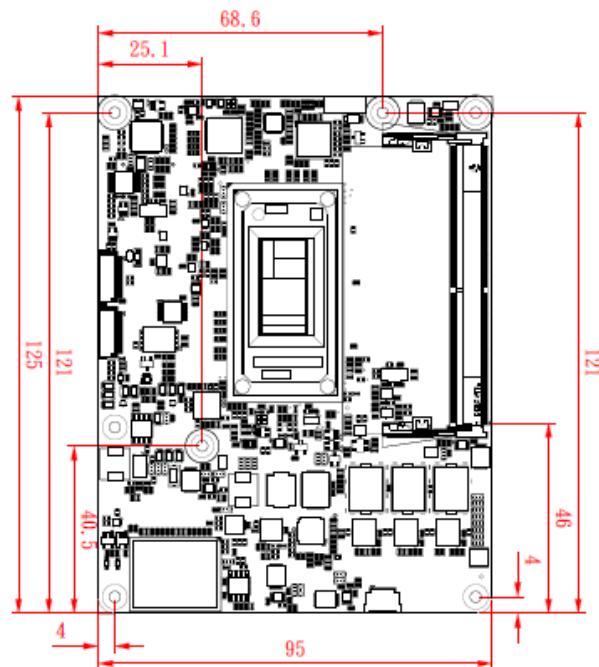
Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.

### 4.6.5.5 Expert mode [DQV mode]

Switch Expert mode or DQV mode.

Configuration options: [DQV mode] [Expert mode]

# 5. Mechanical Drawing



Bottom side height: 18.1 mm

Unit: mm

