

IRN556

3.5" SBC Motherboard
User's Manual

Copyright

This publication contains information that is protected by copyright. No part of it may be reproduced in any form or by any means or used to make any transformation/adaptation without the prior written permission from the copyright holders.

This publication is provided for informational purposes only. The manufacturer makes no representations or warranties with respect to the contents or use of this manual and specifically disclaims any express or implied warranties of merchantability or fitness for any particular purpose. The user will assume the entire risk of the use or the results of the use of this document. Further, the manufacturer reserves the right to revise this publication and make changes to its contents at any time, without obligation to notify any person or entity of such revisions or changes.

Changes after the publication's first release will be based on the product's revision. The website will always provide the most updated information.

© 2025. All Rights Reserved.

Trademarks

Product names or trademarks appearing in this manual are for identification purpose only and are the properties of the respective owners.

FCC and DOC Statement on Class B

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio TV technician for help.

Notice:

1. The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.
2. Shielded interface cables must be used in order to comply with the emission limits.

Table of Contents

- Chapter 1 - Introduction..... 6
 - Specifications 6
 - Block Diagram 8
 - Dimension 8
- Chapter 2 - Hardware Installation..... 9
 - Board Layout..... 9
 - Top View..... 9
 - Bottom View..... 9
 - Installing the heat sink..... 10
 - Installing the Heat Sink 10
 - Jumper Settings 11
 - Panel Backlight Selection (DPJP1) 11
 - Panel Inverter Power Selection (DPJP2) 11
 - Panel Power Jumper (DPJP3) 12
 - Pin Assignment 12
 - RTC Battery (J1) 12
 - DC IN (CN8000) 13
 - CPU Fan (Opt.) (J3) 13
 - Audio Speaker Out (AUCN1)..... 14
 - I2C/SMBus (Opt.) (J2) 14
 - Front Panel (JP1)..... 15
 - Front Audio (AUJ1)..... 15
 - USB2.0 Connector (UBJ1)..... 16
 - DIO (CN1) 16
 - COM1 (TSJ1) 17
 - COM2 (TSJ2) 17
 - COM3 (TSJ3) 18
 - COM4 (TSJ4) 18
 - LVDS (DPCN1)..... 19
 - Expansion Slots..... 20
 - Installing the M.2 Module 20
- Chapter 3 - BIOS Settings..... 22
 - Overview 22
 - Main..... 23
 - Advanced 23
 - RC ACPI Settings 24
 - CPU Configuration..... 24
 - Power & Performance..... 25
 - PCH-FW Configuration 25
 - Trusted Computing..... 26
 - PTN3460 Configuration 26
 - IT8786 Super IO Configuration 27
 - IT8786 Super IO Configuration ▶ **Serial Port 1, 2 Configuration** 27
 - IT8786 Super IO Configuration ▶ **Serial Port 3, 4 Configuration** 28
 - IT8786 Hardware Monitor..... 28
 - Serial Port Console Redirection 29
 - Serial Port Console Redirection ▶ **Console Redirection Settings**..... 29
 - Network Stack Configuration..... 30
 - USB Power Control 31
 - Chipset 32
 - PCH-IO Configuration 32
 - PCH-IO Configuration ▶ **PCI Express Configuration** 33

- PCH-IO Configuration ▶ **SATA Configuration** 33
- PCH-IO Configuration ▶ **Audio Configuration**..... 34
- Security 34
 - Secure Boot..... 35
- Boot 35
 - Save & Exit 36
 - Updating the BIOS..... 36
 - Notice: BIOS SPI ROM..... 36
- Chapter 4 - Mating Connectors List 37

About this Manual

This manual can be downloaded from the website.

The manual is subject to change and update without notice, and may be based on editions that do not resemble your actual products. Please visit our website or contact our sales representatives for the latest editions.

Warranty

1. Warranty does not cover damages or failures that occur from misuse of the product, inability to use the product, unauthorized replacement or alteration of components and product specifications.
2. The warranty is void if the product has been subjected to physical abuse, improper installation, modification, accidents or unauthorized repair of the product.
3. Unless otherwise instructed in this user's manual, the user may not, under any circumstances, attempt to perform service, adjustments or repairs on the product, whether in or out of warranty. It must be returned to the purchase point, factory or authorized service agency for all such work.
4. We will not be liable for any indirect, special, incidental or consequential damages to the product that has been modified or altered.

Static Electricity Precautions

It is quite easy to inadvertently damage your PC, system board, components or devices even before installing them in your system unit. Static electrical discharge can damage computer components without causing any signs of physical damage. You must take extra care in handling them to ensure against electrostatic build-up.

1. To prevent electrostatic build-up, leave the system board in its anti-static bag until you are ready to install it.
2. Wear an antistatic wrist strap.
3. Do all preparation work on a static-free surface.
4. Hold the device only by its edges. Be careful not to touch any of the components, contacts or connections.
5. Avoid touching the pins or contacts on all modules and connectors. Hold modules or connectors by their ends.



Important:

Electrostatic discharge (ESD) can damage your processor, disk drive and other components. Perform the upgrade instruction procedures described at an ESD workstation only. If such a station is not available, you can provide some ESD protection by wearing an antistatic wrist strap and attaching it to a metal part of the system chassis. If a wrist strap is unavailable, establish and maintain contact with the system chassis throughout any procedures requiring ESD protection.

Safety Measures

- To avoid damage to the system, use the correct AC input voltage range.
- To reduce the risk of electric shock, unplug the power cord before removing the system chassis cover for installation or servicing. After installation or servicing, cover the system chassis before plugging the power cord.

About the Package

The package contains the following items. If any of these items are missing or damaged, please contact your dealer or sales representative for assistance.

- 1 IRN556 motherboard

The board and accessories in the package may not come similar to the information listed above. This may differ in accordance with the sales region or models in which it was sold. For more information about the standard package in your region, please contact your dealer or sales representative.

Before Using the System Board

When installing the system board in a new system, you will need at least the following internal components.

- Memory module
- Storage device such as a hard disk drive.
- Power supply

External system peripherals may also be required for navigation and display, including at least a keyboard, a mouse and a video display monitor.

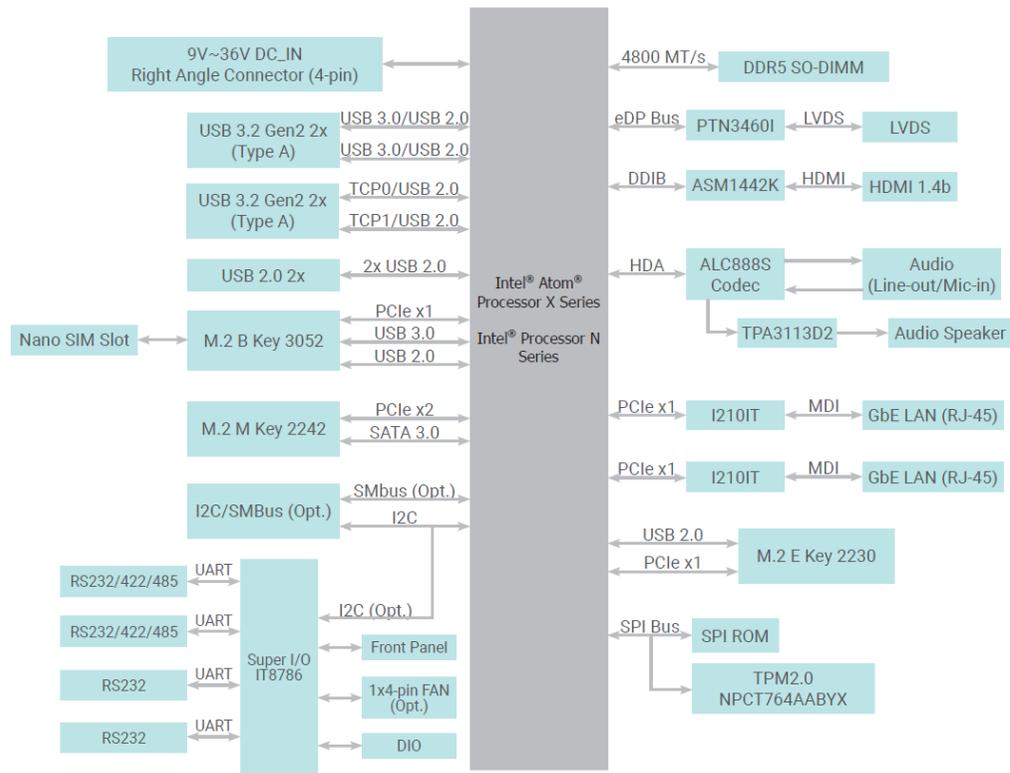
Chapter 1 - Introduction

► Specifications

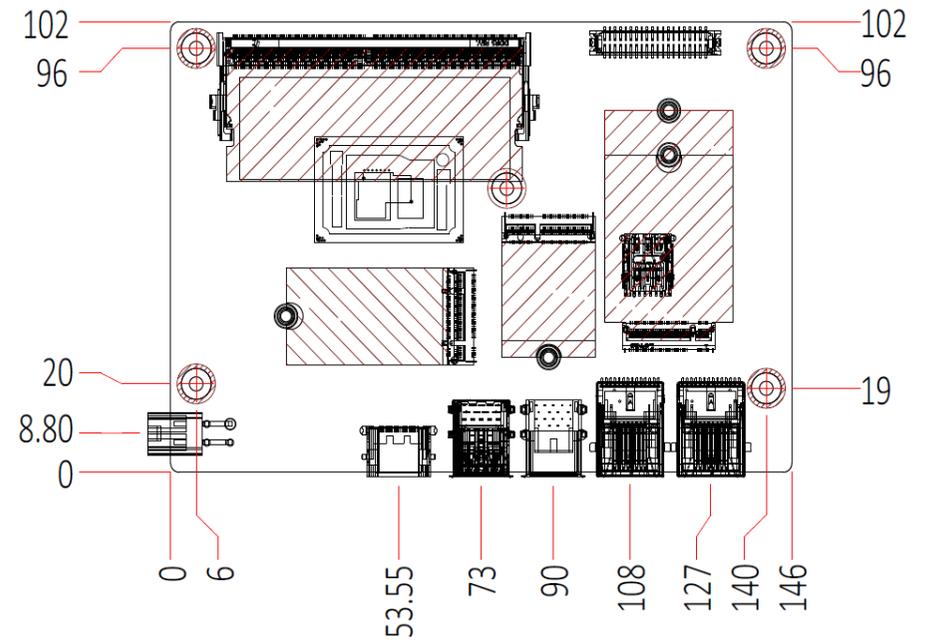
SYSTEM	Processor	Intel Atom® Processor X Series (Code Name: Amston Lake) Intel Atom® Processor x7211RE, 2 Cores, 1.0~3.2 GHz, 6 W Intel Atom® Processor x7433RE, 4 Cores, 1.5~3.4 GHz, 9 W Intel Atom® Processor x7835RE, 8 Cores, 1.3~3.6 GHz, 12 W Intel® Processor N series (Code Name: Twin Lake) Intel® Processor N150, 4 Cores, 1.0~3.6 GHz, 6W Intel® Core™ 3 Processor N355, 8 Cores, 1.8~3.9 GHz, 15W
	Memory	One 262-pin SODIMM up to 16GB Single Channel DDR5 4800MHz
	BIOS	AMI SPI 256Mbit
GRAPHICS	Controller	Intel® UHD Graphics
	Feature	OpenGL 4.6, Direct X 12.1, OpenCL 3.0 HW Decode: HEVC, VP9, AV1, AVC HW Encode: HEVC, VP9, AVC
	Display	1 x HDMI HDMI: resolution up to 4096x2160 @ 24Hz 1 x LVDS LVDS: dual channel 24-bit, resolution up to 1920x1200 @ 60Hz
	Triple Displays	HDMI + LVDS
EXPANSION	Interface	1 x M.2 M key 2242 (PCIe Gen3 x2/SATA3.0) 1 x M.2 B key 3052 (PCIe x1/USB3.0/USB2.0) 1 x nano SIM slot 1 x M.2 E key 2230 (USB2.0/PCIe x1)
REAR I/O	Ethernet	2 x GbE (RJ-45)
	USB	4 x USB 3.2
	Display	1 x HDMI
INTERNAL I/O	Serial	2 x RS-232/422/485 (2 x 9-pin, 1.00mm pitch) 2 x RS-232 (2 x 9-pin, 1.00mm pitch)
	USB	2 x USB 2.0 (1 x 8-pin, 1.00mm pitch)
	Display	1 x LVDS LCD Panel Connector
	Audio	1 x Audio (Line-out/Mic-in) 1 x Speaker out (5W/8ohm)
	DIO	1 x 8-bit DIO (1 x 10-pin, 1.00mm pitch)

AUDIO	Audio Codec	Realtek ALC888S
ETHERNET	Controller	2 x Intel® i210IT(10/100/1000Mbps)
WATCHDOG TIMER	Output & Interval	System Reset, Programmable via Software from 1 to 255 Seconds
SECURITY	TPM	dTPM2.0
POWER	Type	9V to 36V DC
	Connector	Right Angle Connector (4-pin)
	Consumption	Typical: x7835RE, 36V @ 0.35A (12.6W) Max: x7835RE, 36V @ 1.25A (45.0W) Typical: i3-N355, 36V @ 0.35A (12.6W) Max: i3-N355, 36V @ 1.08A (38.88W) Typical: N150, 36V @ 0.34A (12.24W) Max: N150, 36V @ 0.55A (19.8W)
	RTC Battery	CR2032 Coin Cell
OS SUPPORT	OS Support (UEFI Only)	Windows 11 & 10 IoT Enterprise (64-bit) Linux
ENVIRONMENT	Temperature	Operating: -40°C ~ 85°C, -5°C ~ 65°C Storage: -40°C ~ 85°C
	Humidity	Operating: 5 to 95% RH Storage: 5 to 90% RH
	MTBF	809,938 hrs @ 25°C; 458,347 hrs @ 45°C; 291,792 hrs @ 60°C; 215,309 hrs @ 70°C; 136,953 hrs @ 85°C Calculation Model: Telcordia Issue 4 Environment: GB, GC - Ground Benign, Controlled
MECHANISM	Dimensions	3.5" SBC Form Factor: 146mm (5.75") x 102mm (4.02")
	Height	PCB: 1.6mm
STANDARDS AND CERTIFICATIONS	Certifications	CE, FCC Class B, RoHS, UKCA

► Block Diagram



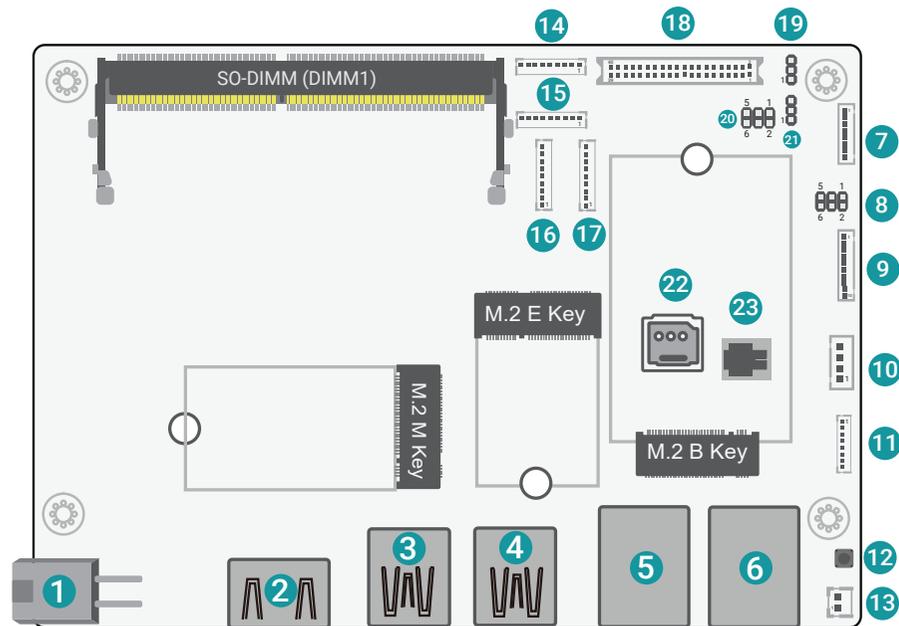
► Dimension



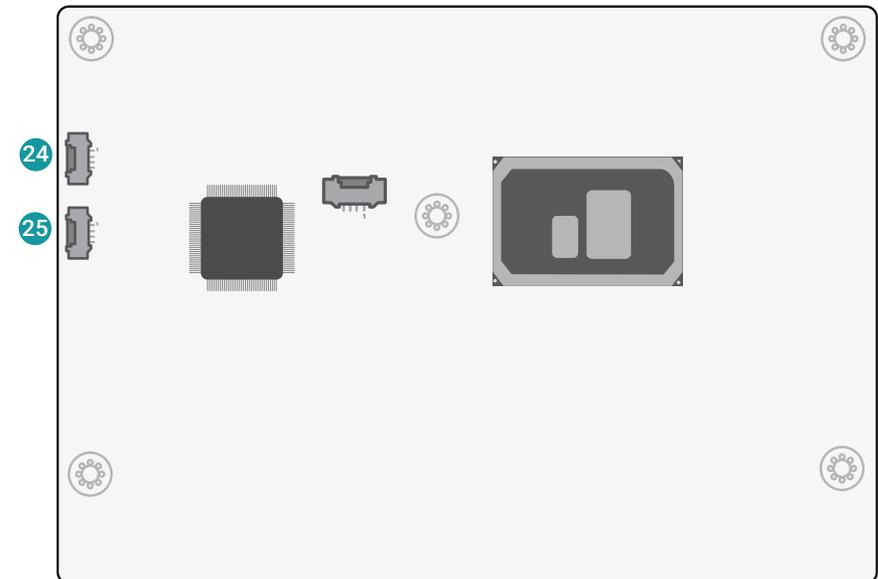
Chapter 2 - Hardware Installation

► Board Layout

Top View



Bottom View



► **Installing the heat sink**

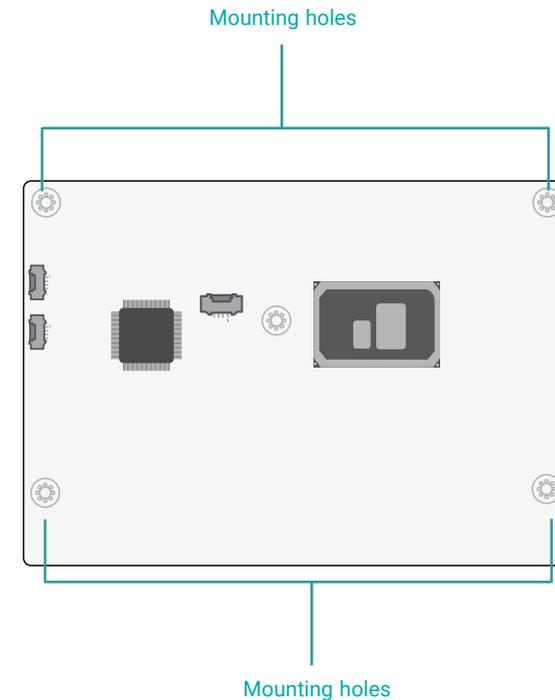
Installing the Heat Sink

The CPU must be kept cool by using a heat sink, otherwise the CPU will overheat damaging both the CPU and system board.

1. Before you install the fan / heat sink, you must apply a thermal paste onto the top of the CPU. The thermal paste is usually supplied when you purchase the fan / heat sink assembly. Do not spread the paste all over the surface. When you later place the heat sink on top of the CPU, the compound will disperse evenly.

Some heat sinks come with a patch of preapplied thermal paste. Do not apply thermal paste if the fan / heat sink already has a patch of thermal paste on its underside. Peel the strip that covers the paste before you place the fan / heat sink on top of the CPU.

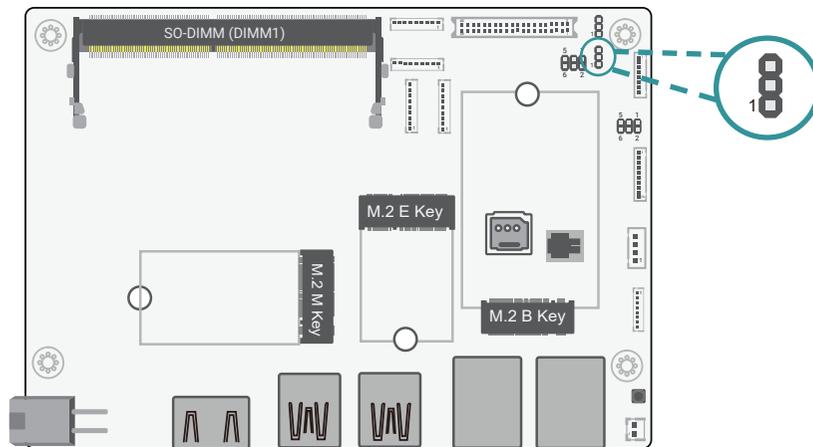
2. Place the heat sink on top of the CPU. The 4 spring screws around the heat sink, which are used to secure the heat sink onto the system board, must match the 4 mounting holes around the board.
3. Screw tight two of the spring screws at opposite corners into the mounting holes. And then proceed with the other two spring screws.



1 DC IN	14 COM1
2 HDMI	15 COM2
3 2x USB3.2	16 COM4
4 2x USB3.2	17 COM3
5 LAN2	18 LVDS
6 LAN1	19 Panel Inverter Power Selection
7 USB2.0 Connector	20 Panel Power Jumper
8 Front Panel	21 Panel Backlight Selection
9 DIO	22 SIM Card Slot
10 Audio Speaker Out	23 SPI
11 Front Audio	24 CPU FAN (Opt.)
12 Clear Cmos	25 I2C/SMBus (Opt.)
13 RTC Battery	

► Jumper Settings

Panel Backlight Selection (DPJP1)

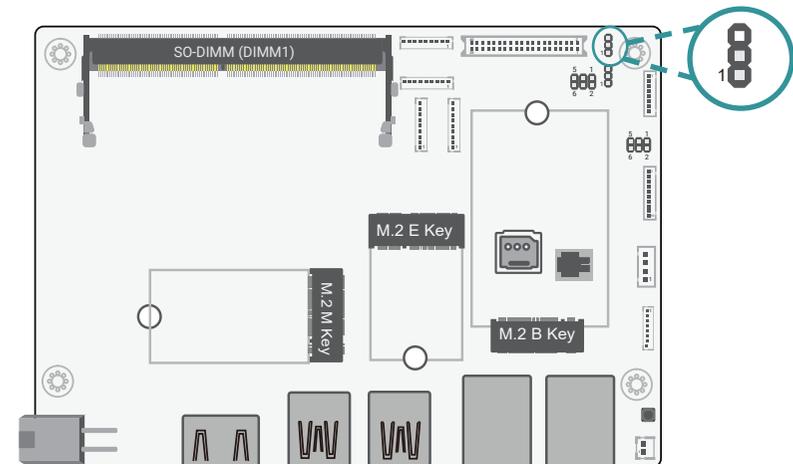


■ 1-2 On: 3V3 (default)



■ 2-3 On: 5V

Panel Inverter Power Selection (DPJP2)



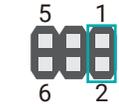
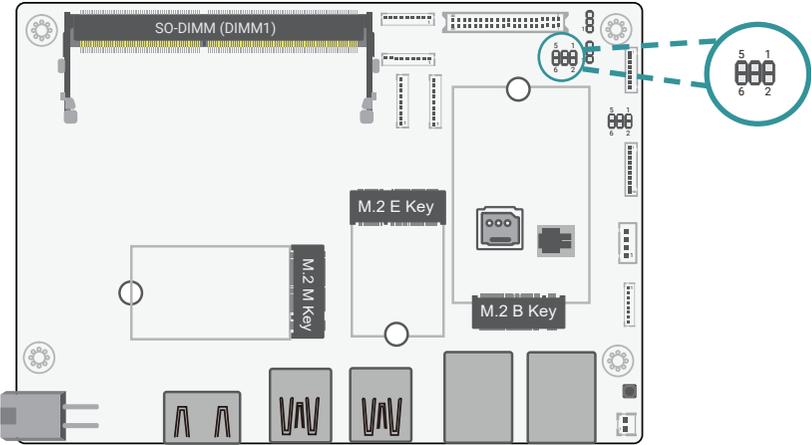
■ 1-2 On: 12V



■ 2-3 On: 5V (default)

► Pin Assignment

Panel Power Jumper (DPJP3)



■ 1-2 On: 12V

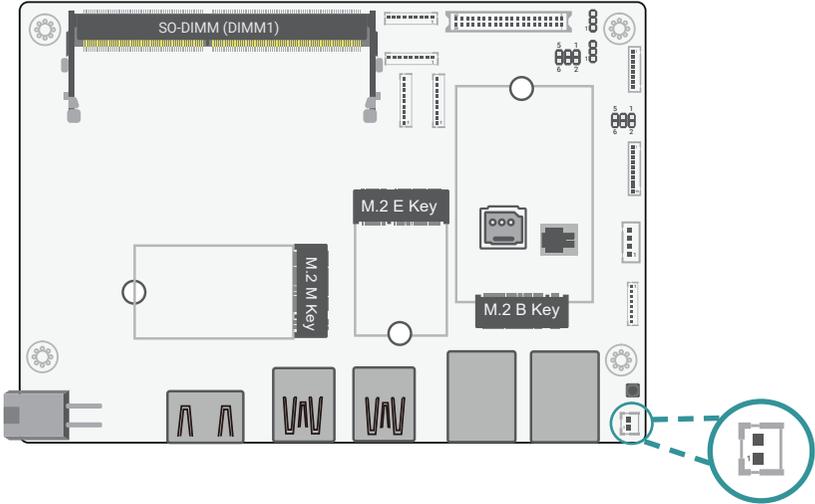


■ 3-4 On: 5V



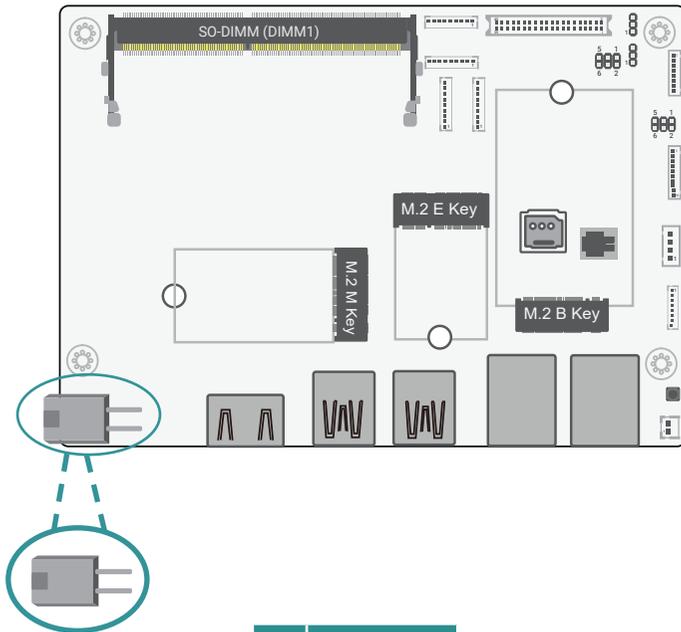
■ 5-6 On: 3V3 (default)

RTC Battery (J1)



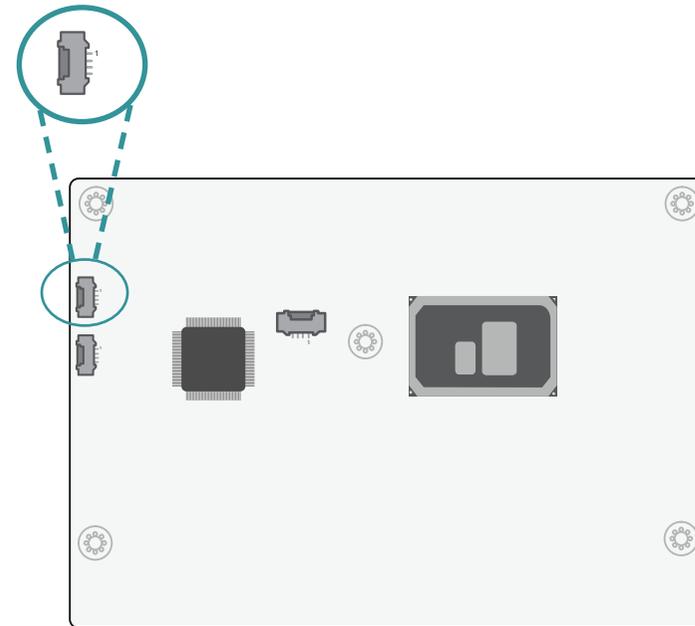
Pin	Assignment
1	RTC power
2	GND

DC IN (CN8000)



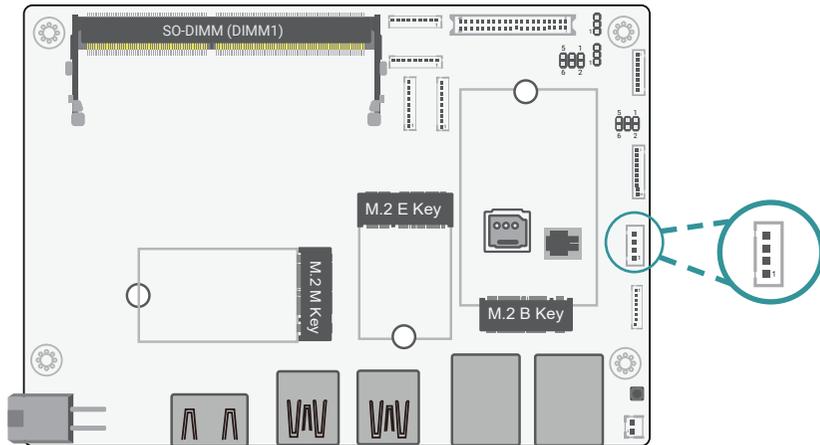
Pin	Assignment
1	GND
2	GND
3	DC_IN
4	DC_IN

CPU Fan (Opt.) (J3)



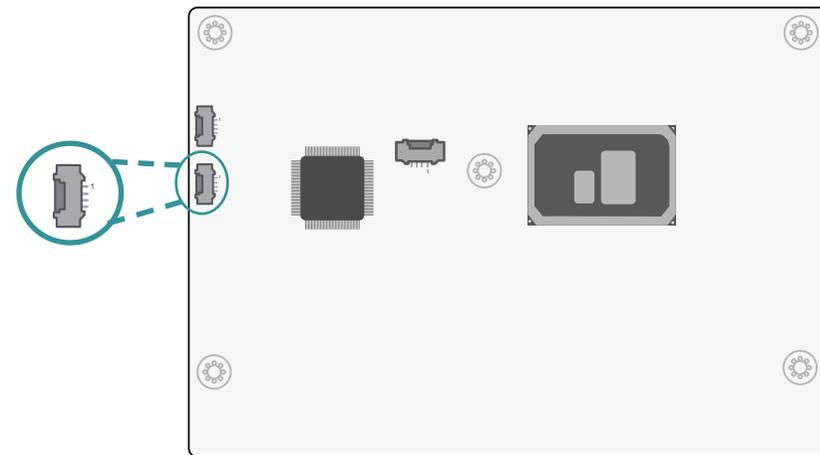
Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

Audio Speaker Out (AUCN1)



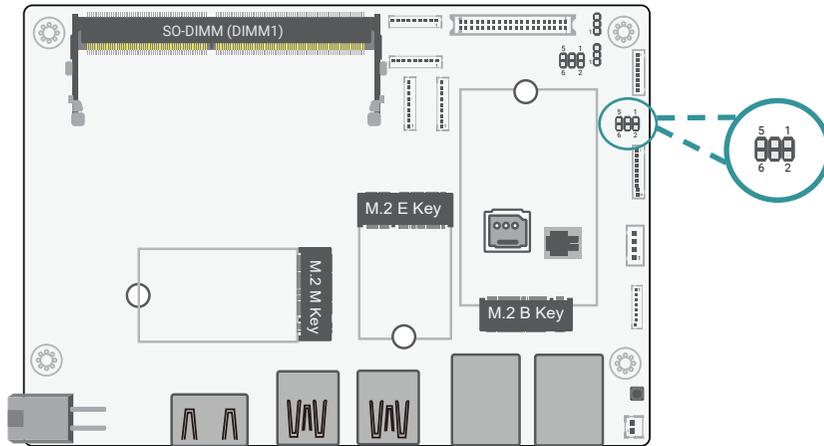
Pin	Assignment
1	SPKOUT_L+
2	SPKOUT_L-
3	SPKOUT_R+
4	SPKOUT_R-

I2C/SMBus (Opt.) (J2)



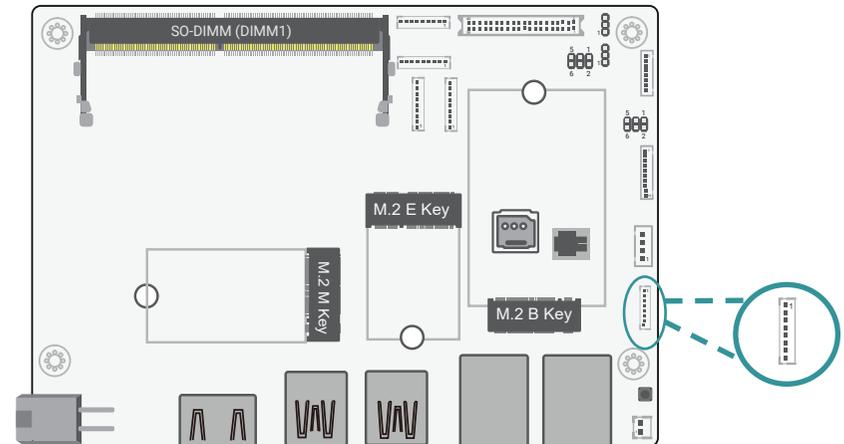
Pin	Assignment
1	3V3SB
2	GND
3	I2C_SMB_CLK
4	I2C_SMB_DATA
5	I2C_INT_SMB_ALERT#

Front Panel (JP1)



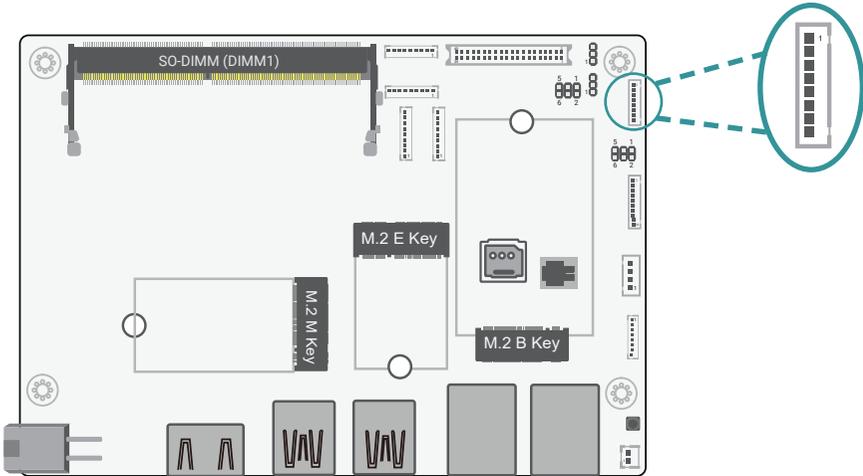
Pin	Assignment
1	Front_BTN
2	V_SUS_LED
3	GND
4	V_SUS_LED
5	SYS_RST
6	HDD_LED

Front Audio (AUJ1)



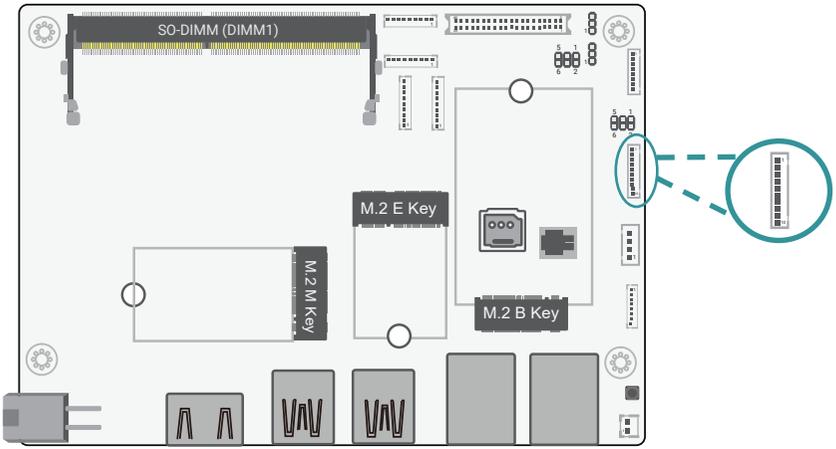
Pin	Assignment
1	MIC_L
2	A_GND
3	MIC_R
4	LINE_R
5	MIC_JD
6	A_GND
7	LINE_L
8	LINE_JD

USB2.0 Connector (UBJ1)



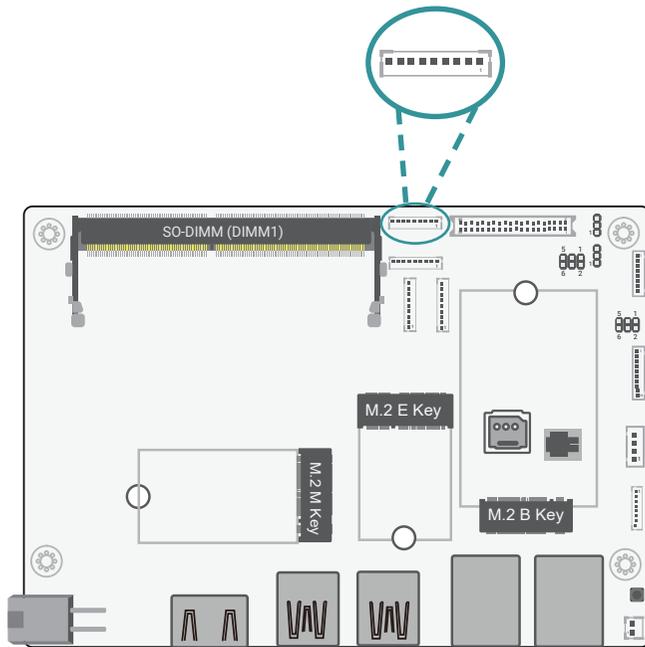
Pin	Assignment
1	USB power
2	DATA-
3	DATA+
4	GND
5	USB power
6	DATA-
7	DATA+
8	GND

DIO (CN1)



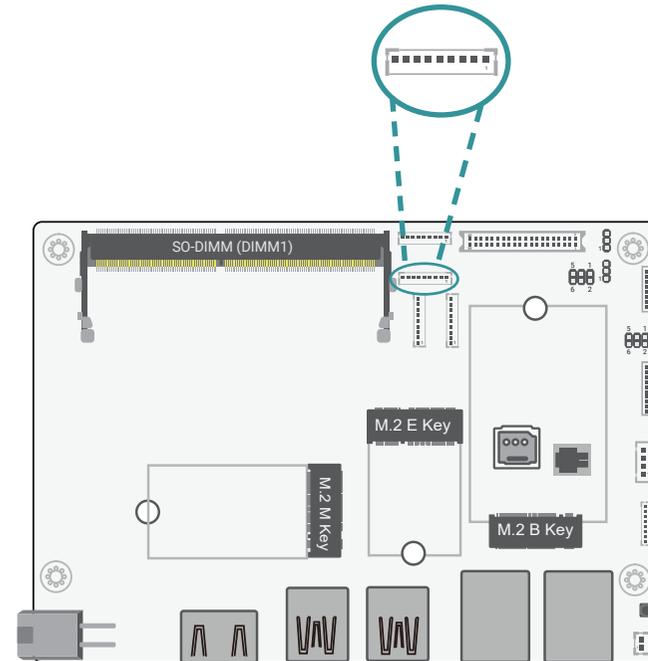
Pin	Assignment
1	DIO7
2	DIO6
3	DIO5
4	DIO4
5	DIO3
6	DIO2
7	DIO1
8	DIO0
9	5V
10	GND

COM1 (TSJ1)



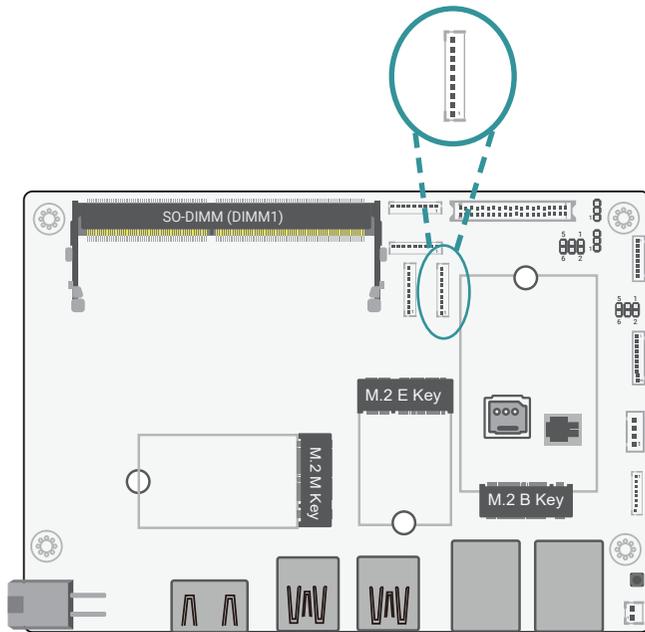
Pin	RS232	RS422	RS485
1	MDCD-	TX-	Data-
2	MSIN-	TX+	Data+
3	MSO-	RX+	N.C
4	MDTR-	RX-	N.C
5	GND	N.C	N.C
6	MDSR-	N.C	N.C
7	MRTS-	N.C	N.C
8	MCTS-	N.C	N.C
9	MRI-	N.C	N.C

COM2 (TSJ2)



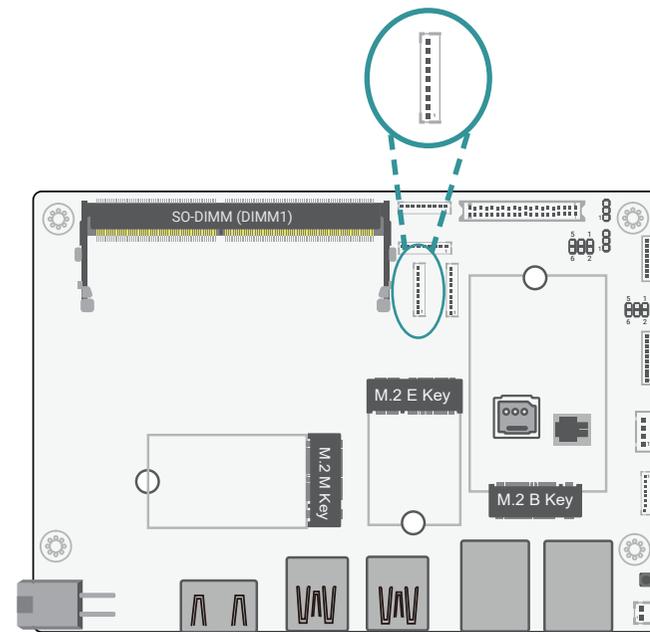
Pin	RS232	RS422	RS485
1	MDCD-	TX-	Data-
2	MSIN-	TX+	Data+
3	MSO-	RX+	N.C
4	MDTR-	RX-	N.C
5	GND	N.C	N.C
6	MDSR-	N.C	N.C
7	MRTS-	N.C	N.C
8	MCTS-	N.C	N.C
9	MRI-	N.C	N.C

COM3 (TSJ3)



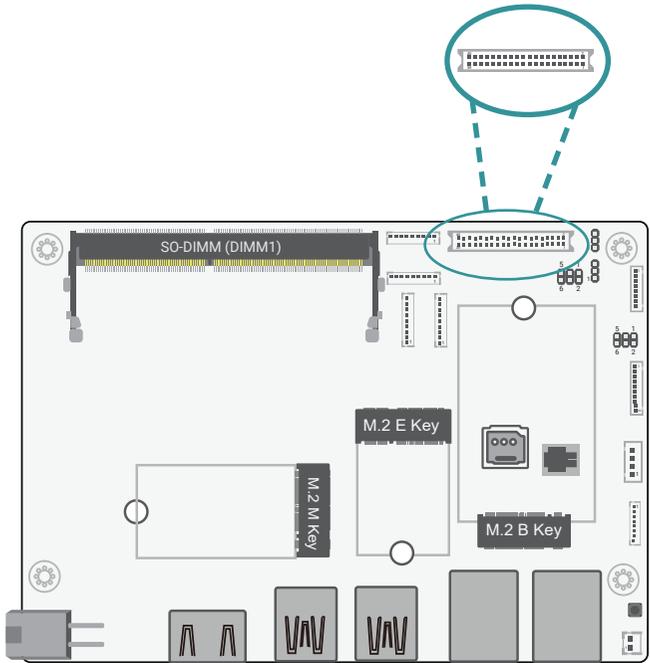
Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-		

COM4 (TSJ4)



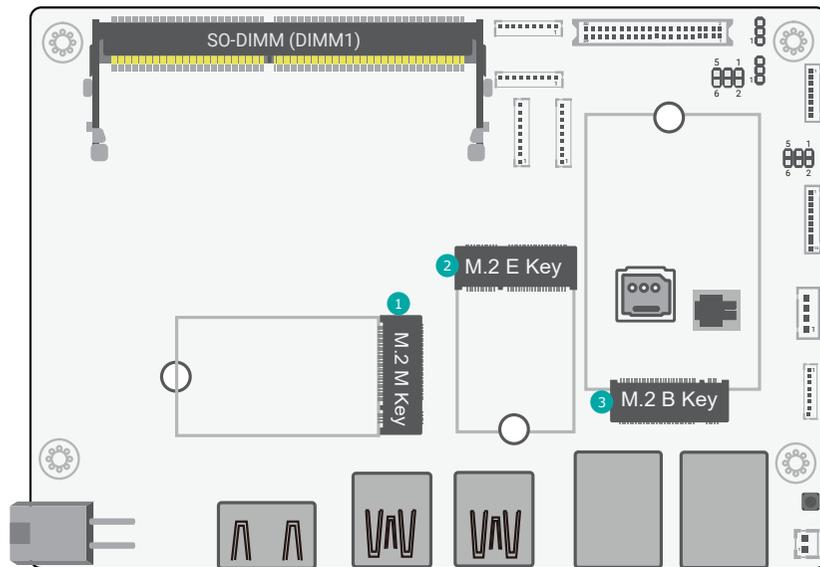
Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-		

LVDS (DPCN1)



Pin	Assignment	Pin	Assignment
1	INV_PWR	2	INV_PWR
3	INV_PWR	4	INV_PWR
5	INV_PWR	6	GND
7	3V3	8	GND
9	VCC_PANEL_PWR	10	VCC_PANEL_PWR
11	LVDS_DDC_CLK	12	LVDS_DDC_DATA
13	DIMMING	14	PVCCEN
15	BLONOFF	16	GND
17	LVDSA_LANE0_N	18	LVDSA_LANE0_P
19	LVDSA_LANE1_N	20	LVDSA_LANE1_P
21	LVDSA_LANE2_N	22	LVDSA_LANE2_P
23	LVDSA_CLK_N	24	LVDSA_CLK_P
25	LVDSA_LANE3_N	26	LVDSA_LANE3_P
27	GND	28	GND
29	LVDSB_LANE0_N	30	LVDSB_LANE0_P
31	LVDSB_LANE1_N	32	LVDSB_LANE1_P
33	LVDSB_LANE2_N	34	LVDSB_LANE2_P
35	LVDSB_CLK_N	36	LVDSB_CLK_P
37	LVDSB_LANE3_N	38	LVDSB_LANE3_P
39	GND	40	GND

► Expansion Slots

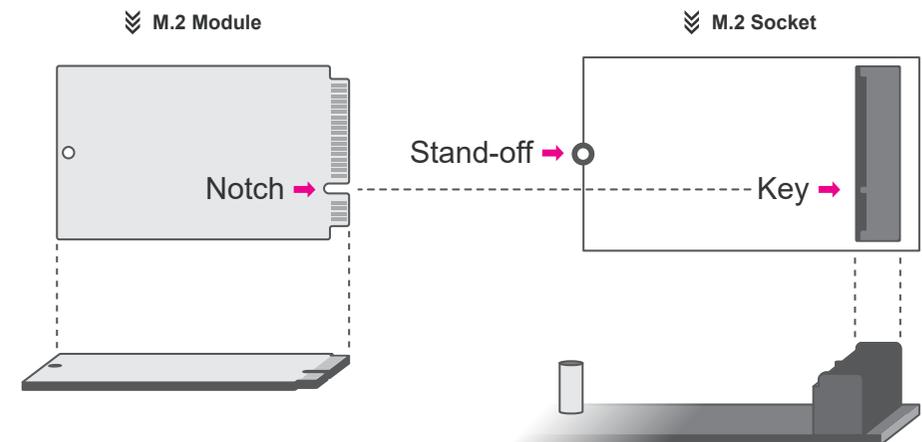


- 1 M.2 M-Key
- 2 M.2 E-Key
- 3 M.2 B-Key

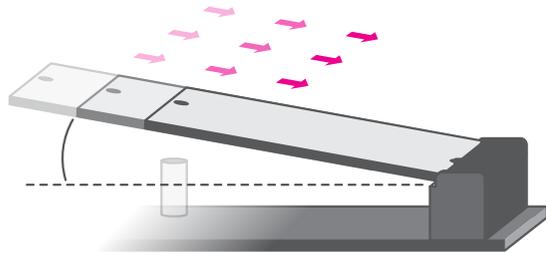
Installing the M.2 Module

Before installing the M.2 module into the M.2 socket, please make sure that the following safety cautions are well-attended.

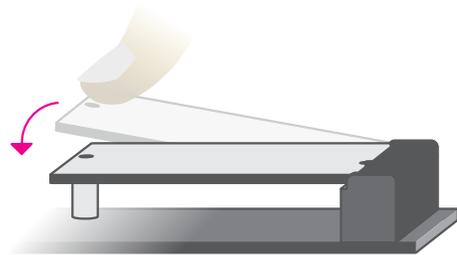
1. Make sure the PC and all other peripheral devices connected to it has been powered down.
2. Disconnect all power cords and cables.
3. Locate the M.2 socket on the system board
4. Make sure the notch on card is aligned to the key on the socket.
5. Make sure the standoff screw is removed from the standoff.



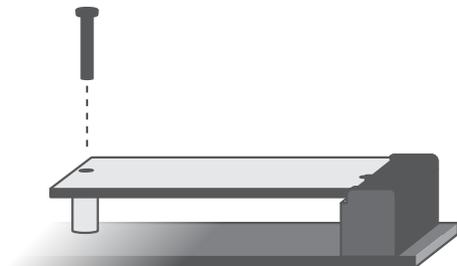
Please follow the steps below to install the card into the socket.



Step 1:
Insert the card into the socket at an angle while making sure the notch and key are perfectly aligned.



Step 2:
Press the end of the card far from the socket down until against the stand-off.



Step 3:
Screw tight the card onto the stand-off with a screw driver and a stand-off screw until the gap between the card and the stand-off closes up. The card should be lying parallel to the board when it's correctly mounted.

Chapter 3 - BIOS Settings

► Overview

The BIOS is a program that takes care of the basic level of communication between the CPU and peripherals. It contains codes for various advanced features found in this system board. The BIOS allows you to configure the system and save the configuration in a battery-backed CMOS so that the data retains even when the power is off. In general, the information stored in the CMOS RAM of the EEPROM will stay unchanged unless a configuration change has been made such as a hard drive replaced or a device added. It is possible that the CMOS battery will fail causing CMOS data loss. If this happens, you need to install a new CMOS battery and reconfigure the BIOS settings.



Note:

The BIOS is constantly updated to improve the performance of the system board; therefore the BIOS screens in this chapter may not appear the same as the actual one. These screens are for reference purpose only.

Default Configuration

Most of the configuration settings are either predefined according to the Load Optimal Defaults settings which are stored in the BIOS or are automatically detected and configured without requiring any actions. There are a few settings that you may need to change depending on your system configuration.

Entering the BIOS Setup Utility

The BIOS Setup Utility can only be operated from the keyboard and all commands are keyboard commands. The commands are available at the right side of each setup screen.

The BIOS Setup Utility does not require an operating system to run. After you power up the system, the BIOS message appears on the screen and the memory count begins. After the memory test, the message "Press DEL to run setup" will appear on the screen. If the message disappears before you respond, restart the system or press the "Reset" button. You may also restart the system by pressing the <Ctrl> <Alt> and keys simultaneously.

Legends

Keys	Function
Right / Left arrow	Move the highlight left or right to select a menu
Up / Down arrow	Move the highlight up or down between submenus or fields
<Enter>	Enter the highlighted submenu
+ (plus key)/F6	Scroll forward through the values or options of the highlighted field
- (minus key)/F5	Scroll backward through the values or options of the highlighted field
<F1>	Display general help
<F2>	Display previous values
<F7>	Popup Boot Device List
<F9>	Optimized defaults
<F10>	Save and Exit
<Esc>	Return to previous menu

Scroll Bar

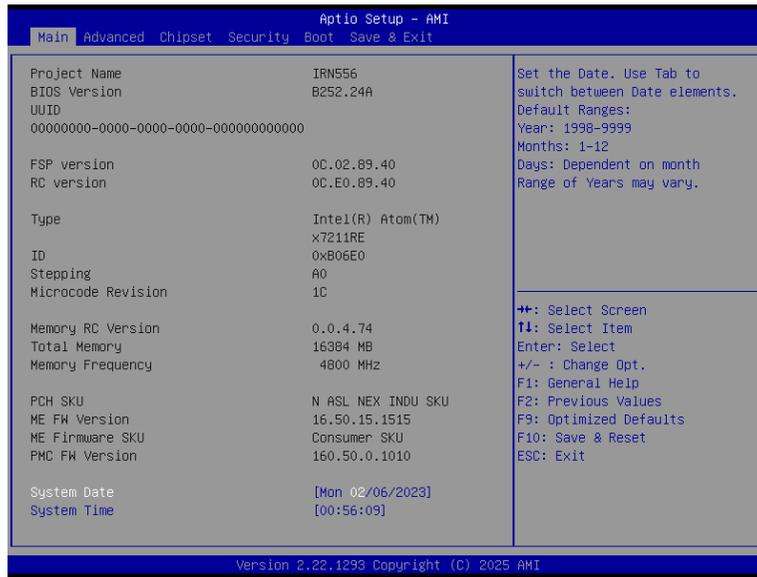
When a scroll bar appears to the right of the setup screen, it indicates that there are more available fields not shown on the screen. Use the up and down arrow keys to scroll through all the available fields.

Submenu

When "►" appears on the left of a particular field, it indicates that a submenu which contains additional options are available for that field. To display the submenu, move the highlight to that field and press <Enter>.

► Main

The Main menu is the first screen that you will see when you enter the BIOS Setup Utility.



System Date

The date format is <month>, <date>, <year>. Press "Tab" to switch to the next field and press "-" or "+" to modify the value.

System Time

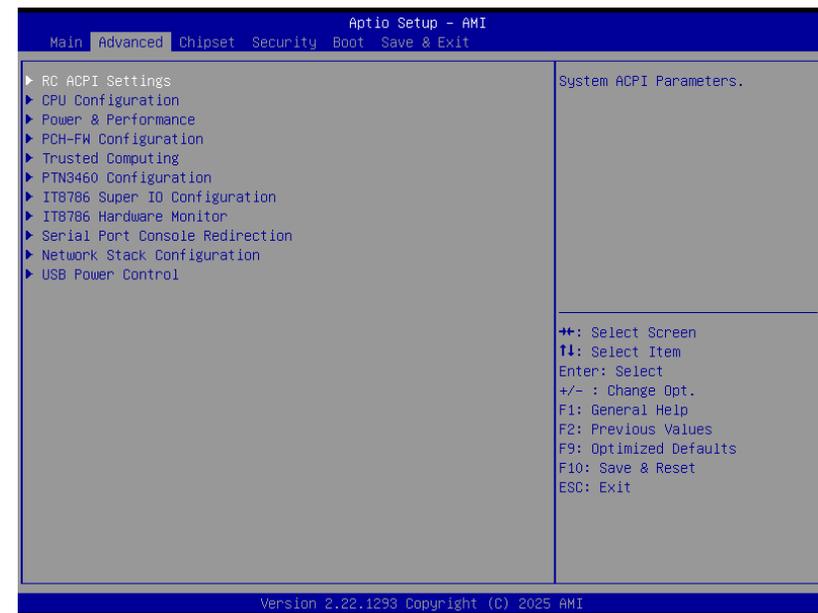
The time format is <hour>, <minute>, <second>. The time is based on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00. Hour displays hours from 00 to 23. Minute displays minutes from 00 to 59. Second displays seconds from 00 to 59.

► Advanced

The Advanced menu allows you to configure your system for basic operation. Some entries are defaults required by the system board, while others, if enabled, will improve the performance of your system or let you set some features according to your preference.

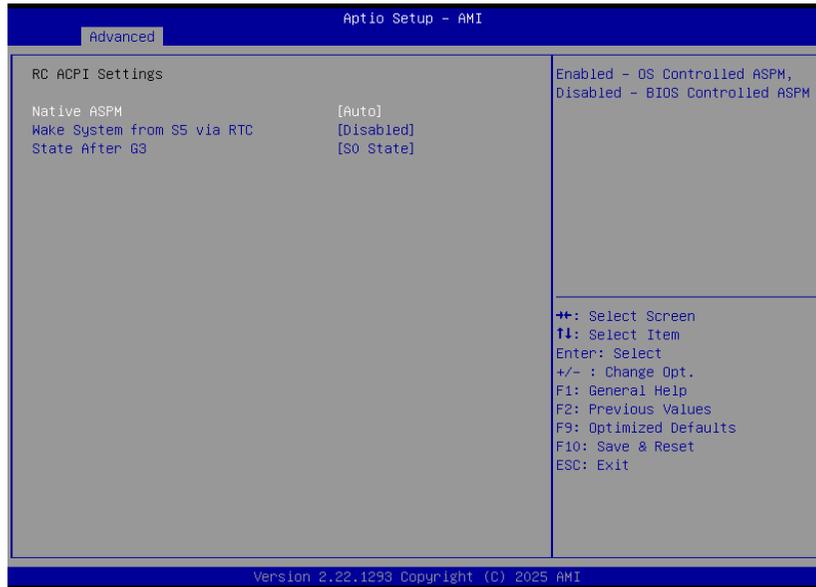


Important:
Setting incorrect field values may cause the system to malfunction.



▶ Advanced

RC ACPI Settings



Native ASPM

Enabled - OS Controlled ASPM,
Disabled - BIOS Controlled ASPM

Wake system from S5 via RTC

When Enabled, the system will automatically power up at a designated time every day. Once it's switched to [Enabled], please set up the time of day – hour, minute, and second – for the system to wake up.

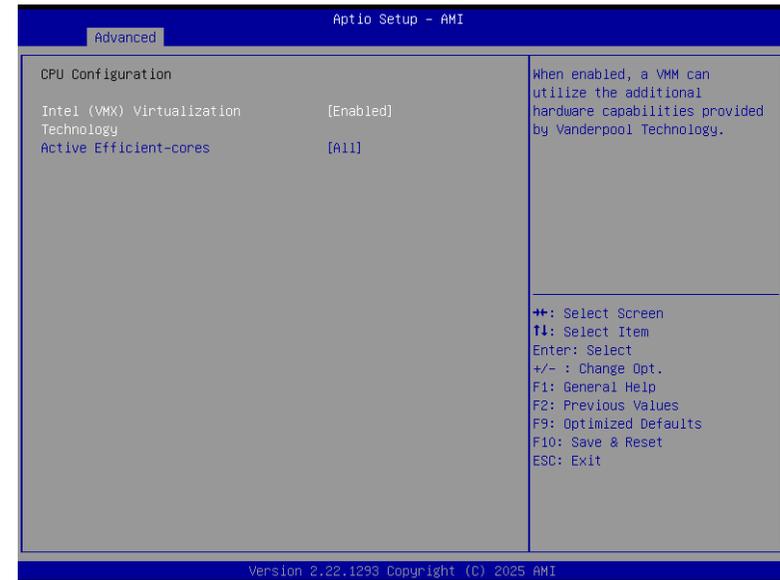
State After G3

Select between S0 State, and S5 State. This field is used to specify what state the system is set to return to when power is re-applied after a power failure (G3 state).

- **S0 State** The system automatically powers on after power failure.
- **S5 State** The system enter soft-off state after power failure. Power-on signal input is required to power up the system.

▶ Advanced

CPU Configuration



Intel (VMX) Virtualization Technology

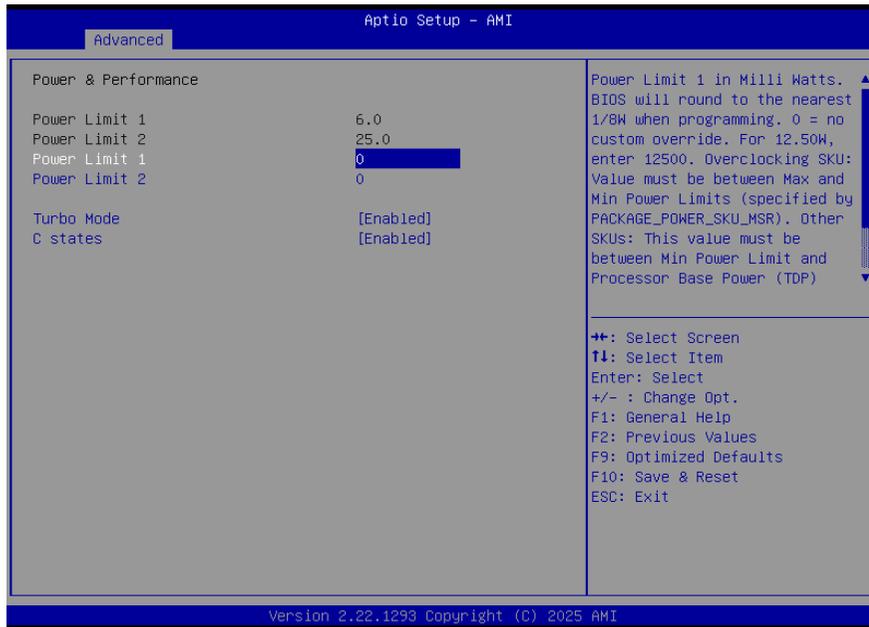
When this field is set to Enabled, the VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.

Active Efficient Cores

Select number of cores to enable in each processor package: all or 1.

▶ Advanced

Power & Performance



Power Limit 1

Power Limit 1 in Milli Watts. BIOS will round to the nearest 1/8W when programming. 0 = no custom override. For 12.50H, enter 12500. Overclocking SKU: Value must be between Max and Min Power Limits (specified by PACKAGE_POWER_SKU_MSR).

Power Limit 2

Power Limit 2 value in Milli Watts. BIOS will round to the nearest 1/8H when programming. If the value is 0, BIOS will program this value as 1.25*Processor Base Power (TDP). For 12.50H, enter 12500. Processor applies control policies such that the package power does not exceed this limit.

Turbo Mode

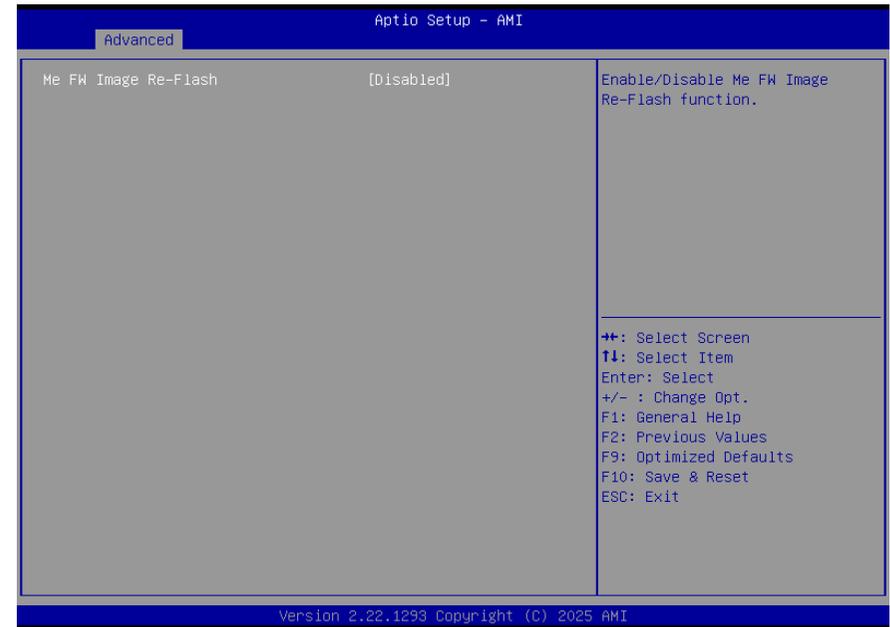
Enable or disable turbo mode of the processor. This field will only be displayed when EIST is enabled.

C states

Enable or disable CPU Power Management. It allows CPU to enter "C states" when it's idle and nothing is executing.

▶ Advanced

PCH-FW Configuration

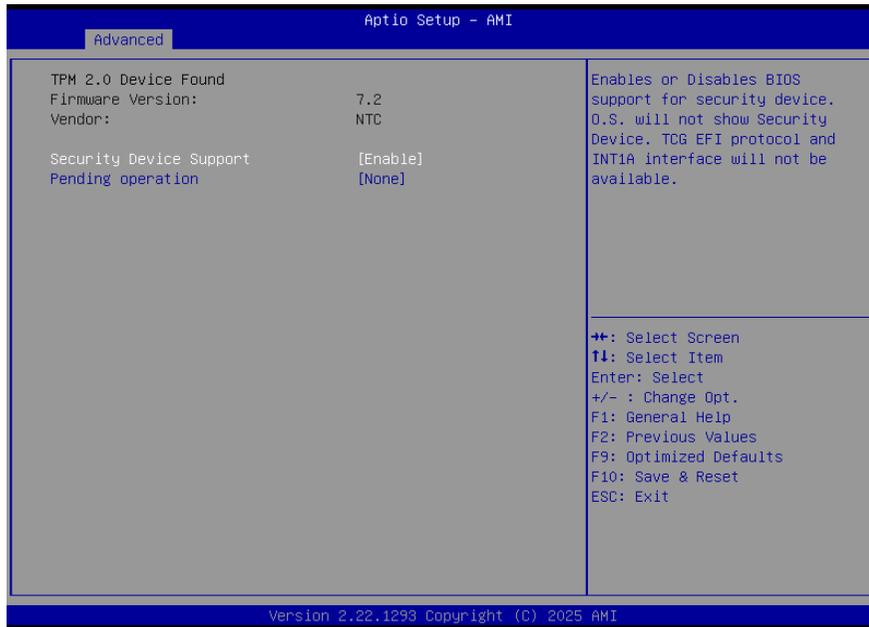


ME FW Image Re-Flash

Enable/Disable Me FW Image Re-Flash function.

▶ **Advanced**

Trusted Computing



Security Device Support

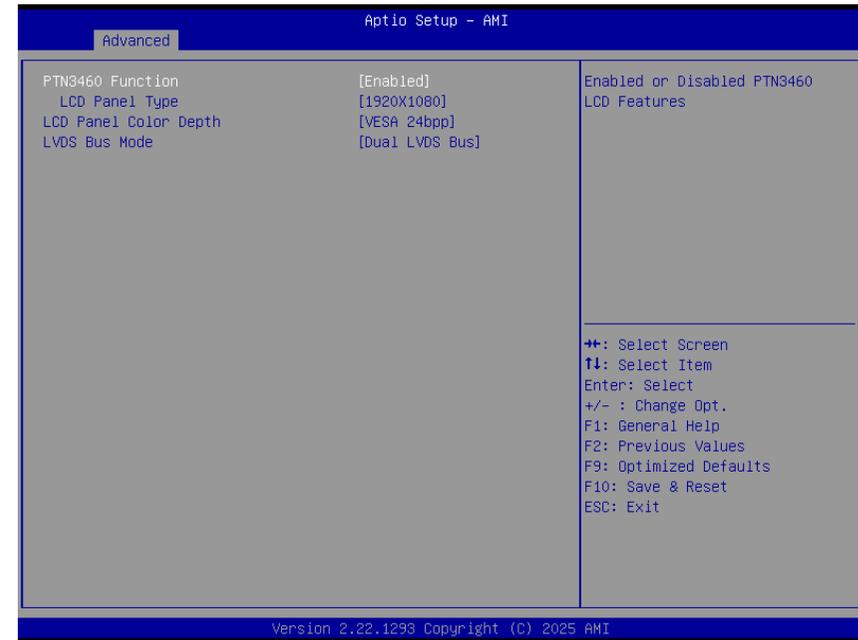
This field is used to enable or disable BIOS support for the security device such as an TPM 2.0 to achieve hardware-level security via cryptographic keys.

Pending operation

To clear the existing TPM encryption, select "TPM Clear" and restart the system. This field is not available when "Security Device Support" is disabled.

▶ **Advanced**

PTN3460 Configuration



PTN3460 Function

Enable or Disable PTN3460 LCD Features. When this field is disabled, the following fields will remain hidden.

LCD Panel Type

Select the resolution of the LCD Panel — 1280X1024, 1920X1080, 1920X1200, 1366X768, 1920x1080, or 1024x768.

LCD Panel Color Depth

Select the color depth of the LCD Panel — 18 Bit, 24 Bit, 36 Bit, 48 Bit.

LCD Bus Mode

Select PTN3460 LVDS Bus Mode.— Single LVDS BUS / Dual LVDS Bus

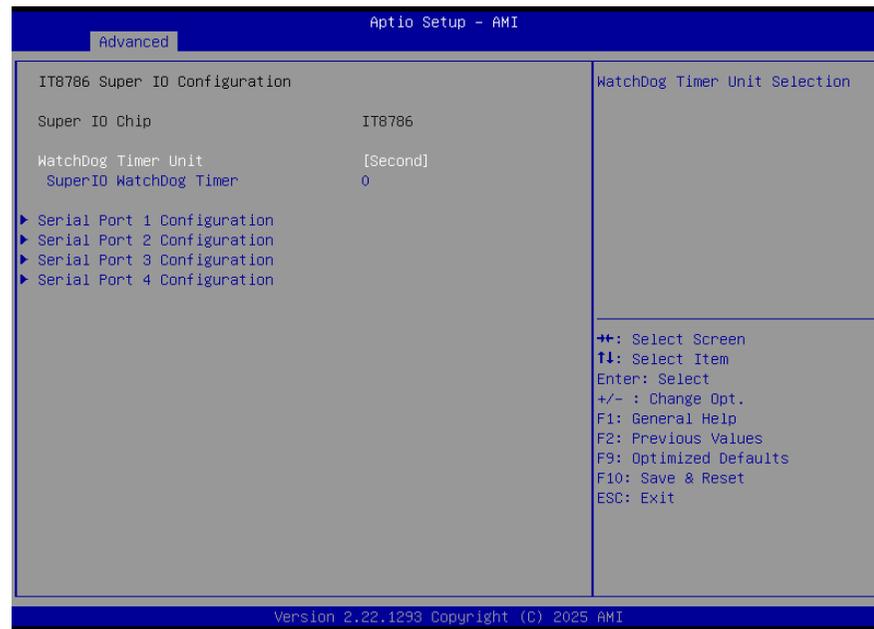


Note:

The configuration must match the specifications of your LCD Panel in order for the LCD Panel to display properly.

▶ **Advanced**

IT8786 Super IO Configuration



WatchDog Timer Unit

Select WatchDog Timer Unit — Second or Minute.

SuperIO WatchDog Timer

Set SuperIO WatchDog Timer Timeout value. The range is from 0 (disabled) to 255.

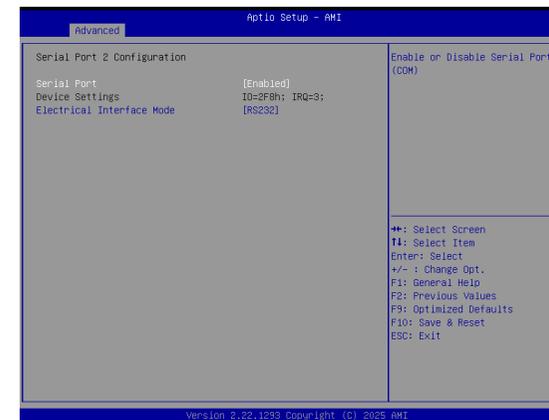
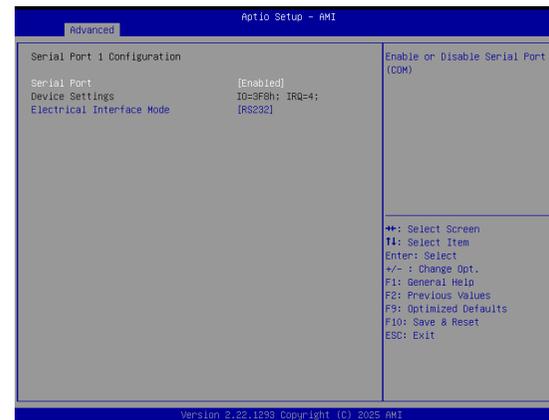


Note:

The sub-menus are detailed in following sections.

▶ **Advanced**

IT8786 Super IO Configuration ▶ Serial Port 1, 2 Configuration

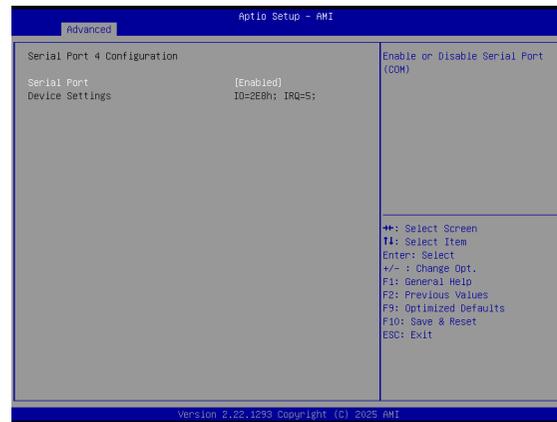


Serial Port

Enable or disable serial port.

▶ Advanced

IT8786 Super IO Configuration ▶ Serial Port 3, 4 Configuration

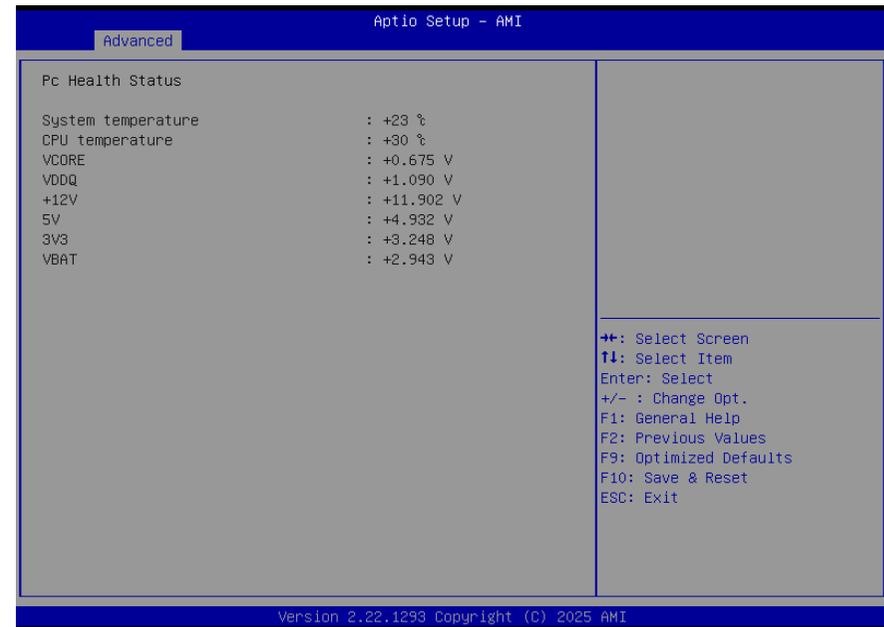


Serial Port

Enable or disable serial port.

▶ Advanced

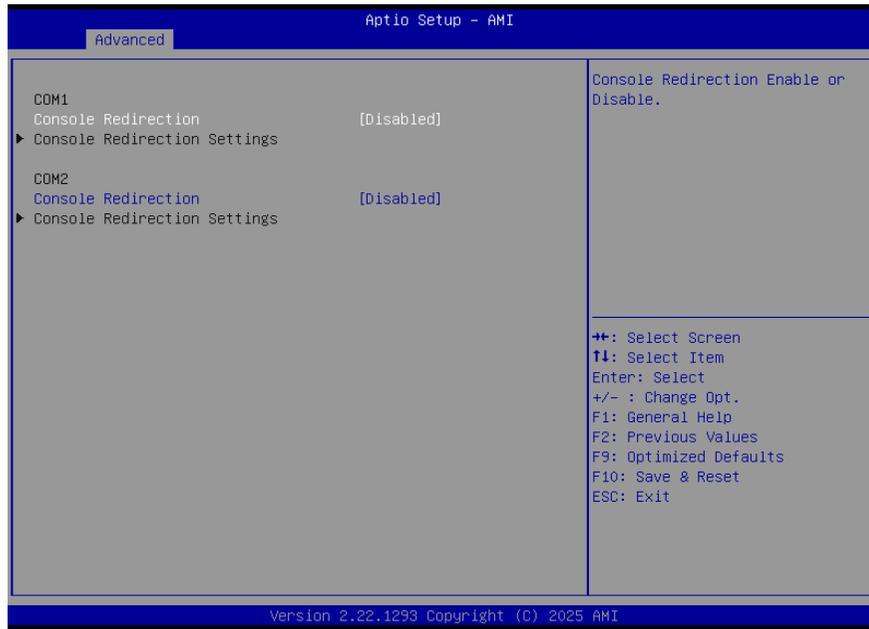
IT8786 Hardware Monitor



This section displays the system's health information, i.e. voltage readings, CPU and system temperatures, and fan speed readings

▶ Advanced

Serial Port Console Redirection

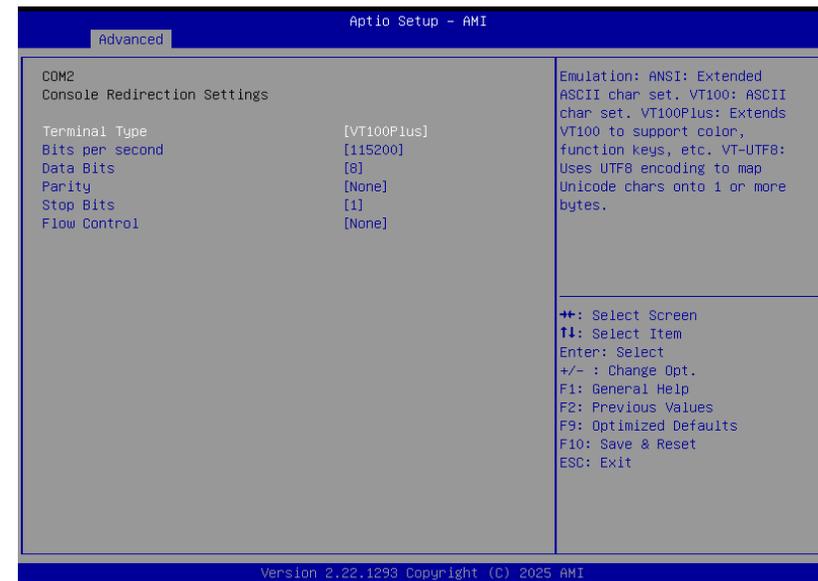
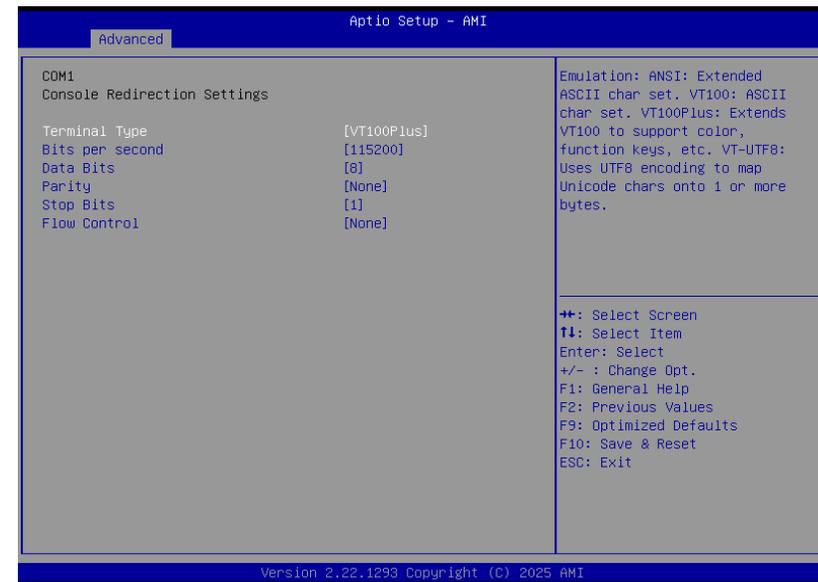


Console Redirection

By enabling Console Redirection of a COM port, the sub-menu of console redirection settings will become available for configuration as detailed in the following.

▶ Advanced

Serial Port Console Redirection ▶ Console Redirection Settings



Configure the serial settings of the current COM port.

Terminal Type

Select terminal type: VT100, VT100+, VT-UTF8 or ANSI.

Bits per second

Select serial port transmission speed: 9600, 19200, 38400, 57600 or 115200.

Data Bits

Select data bits: 7 bits or 8 bits.

Parity

Select parity bits: None, Even, Odd, Mark or Space.

Stop Bits

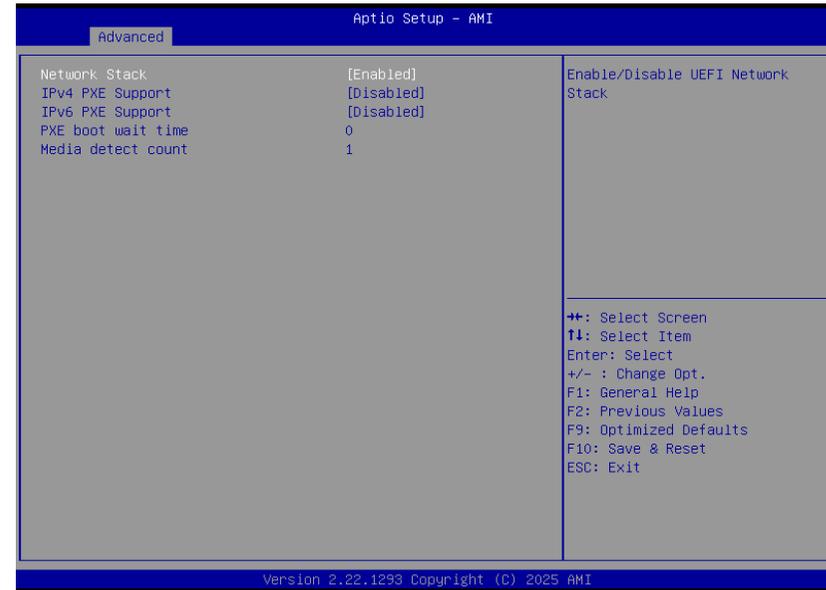
Select stop bits: 1 bit or 2 bits.

Flow Control

Select flow control type: None or Hardware RTS/CTS. Flow Control is for RS485 mode and is only supported by Serial Port 1 (COM1).

► **Advanced**

Network Stack Configuration



Network Stack

Enable or disable UEFI network stack. The following fields will appear when this field is enabled.

Ipv4 PXE Support

Enable or disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.

Ipv6 PXE Support

Enable or disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.

PXE boot wait time

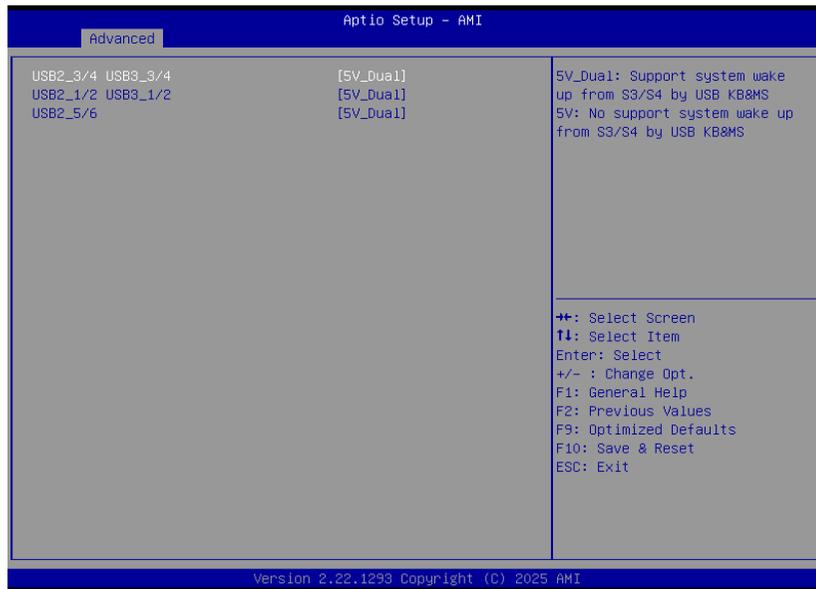
Set the wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.

Media detect count

Set the number of times the presence of media will be checked. Use either +/- or numeric keys to set the value.

► **Advanced**

USB Power Control

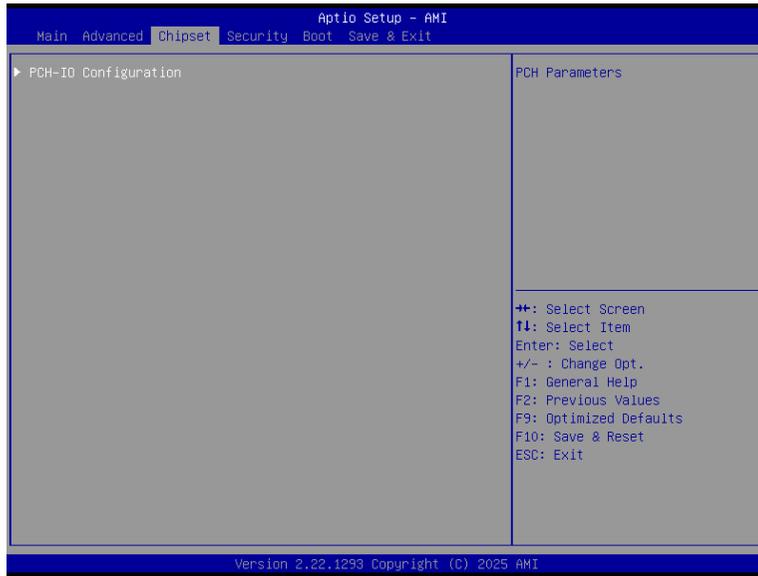


Server CA Configuration

5_Dual: Support system wake up from S3/S4 by USB KB&MS

5V: No support system wake up from S3/S4 by USB KB&MS

► **Chipset**



Please select a submenu and press Enter. The submenus are detailed in the following pages.

► **Chipset**

PCH-IO Configuration



PCI Express Configuration

PCI Express Configuration Settings

SATA Configuration

SATA Device Options Settings

HD Audio Configuration

Audio Subsystem Configuration Settings

► Chipset

PCH-IO Configuration ► PCI Express Configuration



Select one of the PCI Express channels and press enter to configure the following settings.

LAN1&2 ,M.2-E, M.2-M, M.2-B

Control the PCI Express Root Port.

► Chipset

PCH-IO Configuration ► SATA Configuration



SATA Controller(s)

This field is used to enable or disable the Serial ATA controller.

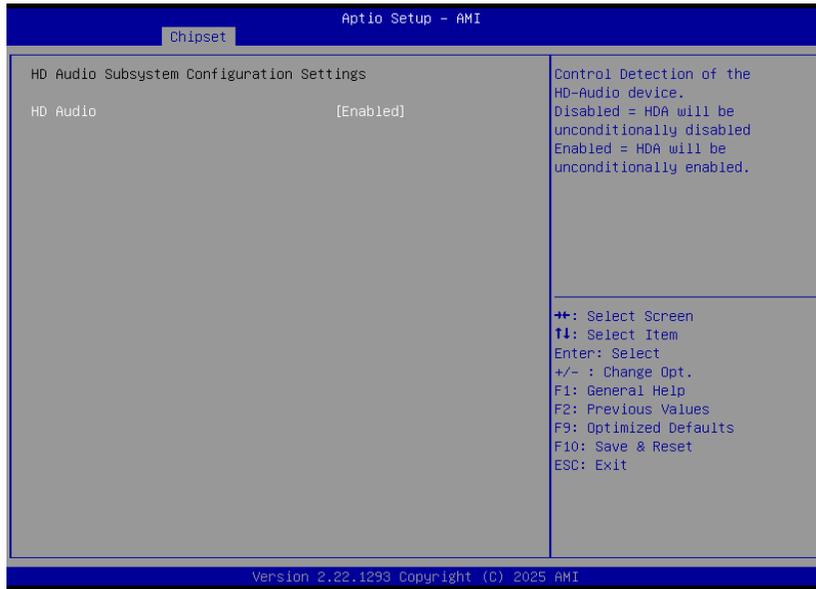
SATA Mode Selection

The mode selection determines how the SATA controller(s) operates.

- **AHCI** This option allows the Serial ATA controller(s) to use AHCI (Advanced Host Controller Interface).

► Chipset

PCH-IO Configuration ► Audio Configuration

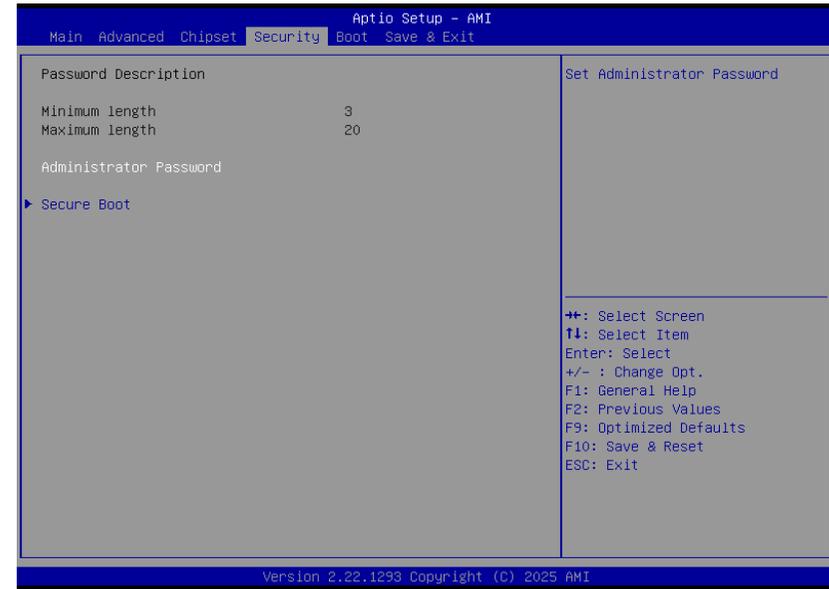


HD Audio

Control the detection of the Audio device.

- **Disabled** HDA will be unconditionally disabled.
- **Enabled** HDA will be unconditionally enabled.

► Security

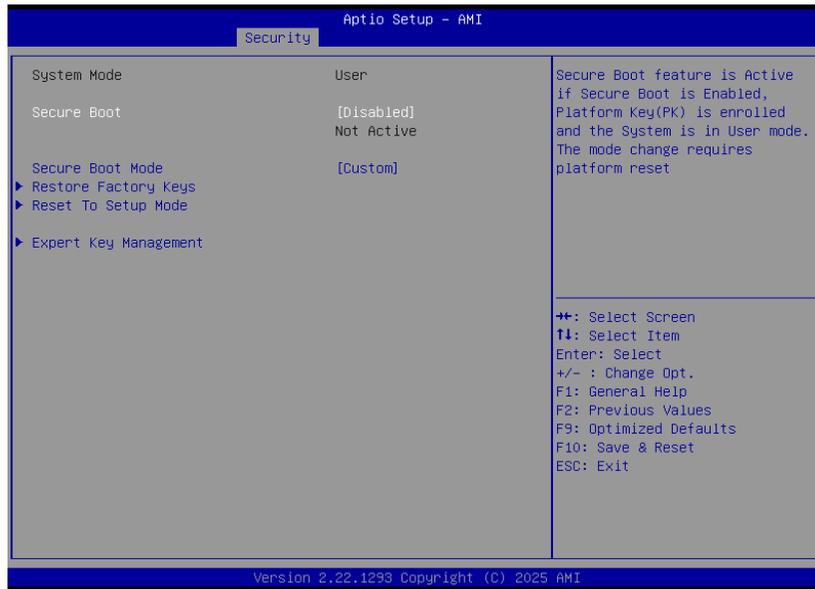


Administrator Password

Set the administrator password. To clear the password, input nothing and press enter when a new password is asked. Administrator Password will be required when entering the BIOS.

► Security

Secure Boot



Secure Boot

The Secure Boot store a database of certificates in the firmware and only allows the Oses with authorized signatures to boot on the system. To activate Secure Boot, please make sure that “Secure Boot” is “[Enabled]”, Platform Key (PK) is enrolled, “System Mode” is “User”, and CSM is disabled. After enabling/disabling Secure Boot, please save the configuration and restart the system. When configured and activated correctly, the Secure Boot status will be “Active”.

Secure Boot Mode

Select the secure boot mode — Standard or Custom. When set to Custom, the following fields will be configurable for the user to manually modify the key database.

Restore Factory Keys

Force system to User Mode. Load OEM-defined factory defaults of keys and databases onto the Secure Boot. Press Enter and a prompt will show up for you to confirm.

Reset To Setup Mode

Clear the database from the NVRAM, including all the keys and signatures installed in the Key Management menu. Press Enter and a prompt will show up for you to confirm.

Key Management

Enables expert users to modify Secure Boot Policy variables without full authentication.

► Boot



Setup Prompt Timeout

Set the number of seconds to wait for the setup activation key. 65535 (0xFFFF) denotes indefinite waiting.

Bootup NumLock State

Select the keyboard NumLock state: On or Off.

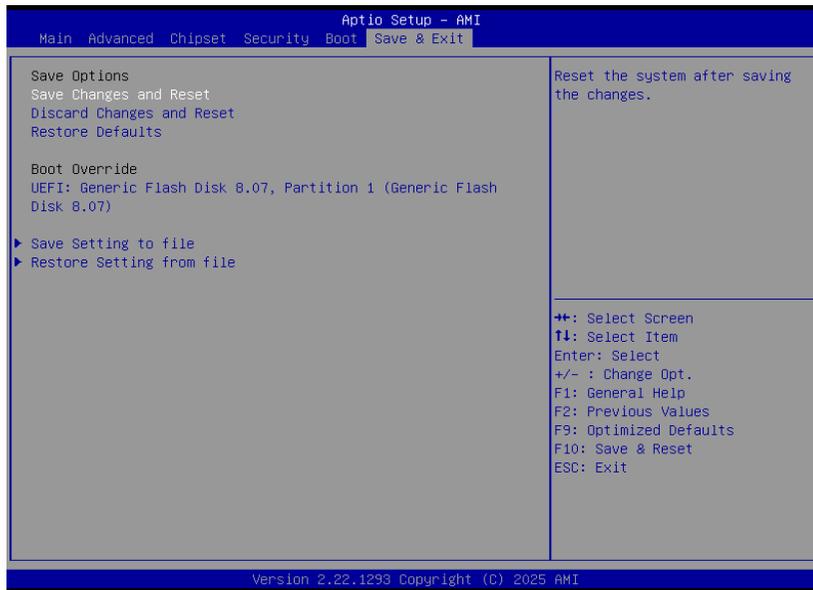
Quiet Boot

This section is used to enable or disable quiet boot option.

Boot Option Priorities

Rearrange the system boot order of available boot devices.

► Save & Exit



Save Changes and Reset

To save the changes, select this field and then press <Enter>. A dialog box will appear. Select Yes to reset the system after saving all changes made.

Discard Changes and Reset

To discard the changes, select this field and then press <Enter>. A dialog box will appear. Select Yes to reset the system setup without saving any changes.

Restore Defaults

To restore and load the optimized default values, select this field and then press <Enter>. A dialog box will appear. Select Yes to restore the default values of all the setup options.

Boot Override

Move the cursor to an available boot device and press Enter, and then the system will immediately boot from the selected boot device. The Boot Override function will only be effective for the current boot. The “Boot Option Priorities” configured in the Boot menu will not be changed.

- **Save Setting to file** Select this option to save BIOS configuration settings to a USB flash device.
- **Restore Setting from file** This field will appear only when a USB flash device is detected. Select this field to restore setting from the USB flash device.

► Updating the BIOS

To update the BIOS, you will need the new BIOS file and a flash utility. Please contact technical support or your sales representative for the files and specific instructions about how to update BIOS with the flash utility.

► Notice: BIOS SPI ROM

1. The Intel® Management Engine has already been integrated into this system board. Due to the safety concerns, the BIOS (SPI ROM) chip cannot be removed from this system board and used on another system board of the same model.
2. The BIOS (SPI ROM) on this system board must be the original equipment from the factory and cannot be used to replace one which has been utilized on other system boards.
3. If you do not follow the methods above, the Intel® Management Engine will not be updated and will cease to be effective.



Note:

- a. You can take advantage of flash tools to update the default configuration of the BIOS (SPI ROM) to the latest version anytime.
- b. When the BIOS IC needs to be replaced, you have to populate it properly onto the system board after the EEPROM programmer has been burned and follow the technical person's instructions to confirm that the MAC address should be burned or not.

Chapter 4 - Mating Connectors List

Please refer to the following list of the mating connectors.

Function	Location	Connector information	Rate output (Voltage/Ampere)
Front Audio	AUJ1	JST, BM08B-SRSS-TB1(LF)(SN), 1*8P, 1.0mm, BOX HEADER	N/A
Audio Speaker	AUCN1	MOLEX, 53398-0471, 1x4P, 1.25mm, WAFER	N/A
DIO	CN1	JST, BM10B-SRSS-TB1(LF)(SN), 1*10P, 1.0mm, BOX HEADER	5V / 1A
Front Panel	JP1	PINREX, 222-97-03GBE1(R), 2*3P, 2.0mm, PIN PLUG	N/A
USB 2.0	UBJ1	JST, BM08B-SRSS-TB1(LF)(SN), 1*8P, 1.0mm, BOX HEADER	5V / 0.5A
LVDS	DPCN1	HIROSE, DF13E-40DP-1.25V(52), 2*20P, 1.25mm, BOX HEADER	Panel Power : 12V, 5V, 3.3V / 2A Panel Backlight Power : 5V, 3.3V / 2A Panel Inverter Power : 12V, 5V / 2A
COM1, COM2, COM3, COM4	TSJ1, TSJ2, TSJ3, TSJ4	PINREX, 710-93-095WR00B, 1*9P, 1.0mm, BOX HEADER	N/A
I2C/SMBus (Opt.)	J2	E-CALL, 0110-3221050, 1*5P, 1.25mm, WAFER	3.3V / 1A
CPU Fan (Opt.)	J3	YIMTEX, 501MW1*04MTR-2R, 1*4P, 1.25mm, BOX HEADER	12V / 0.8A