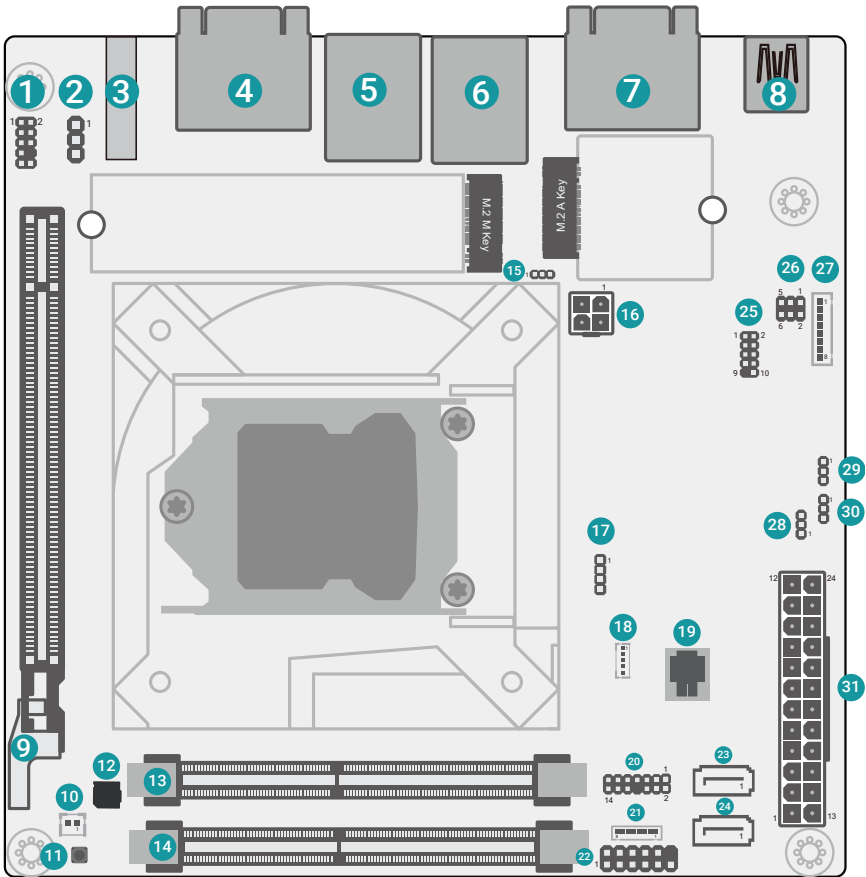
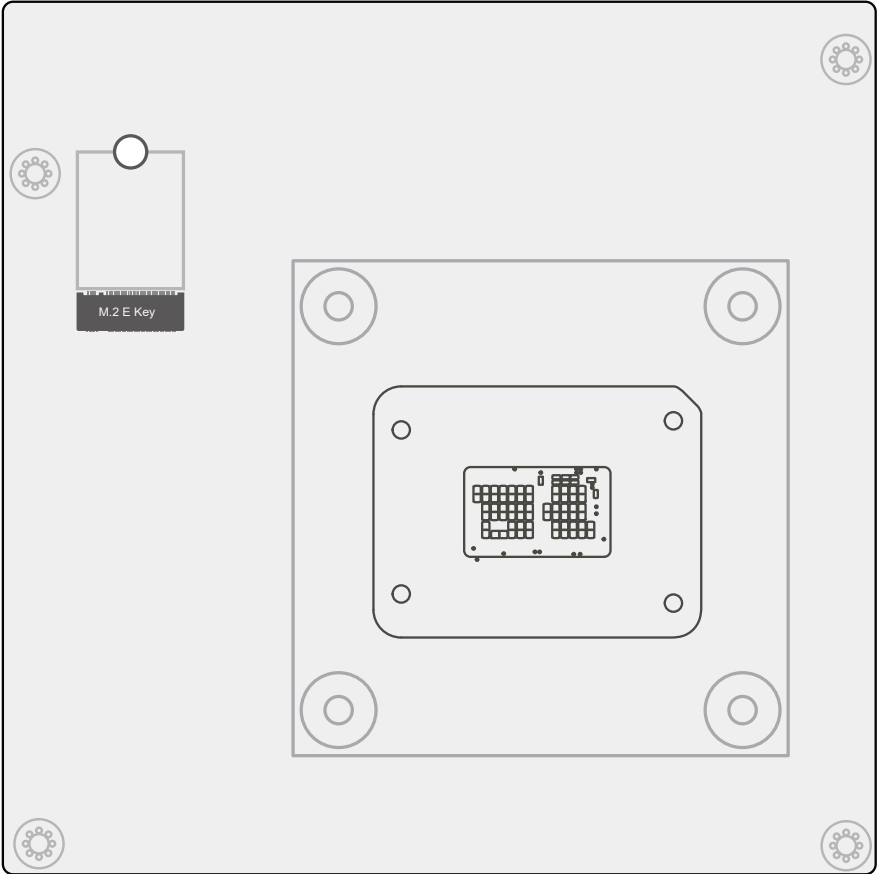


Board Layout

Top View



Bottom View



- 1 Front Audio

- 2 System Fan1

- 3 DP++

- 4 ▲VGA
▼HDMI

- 5 ▲2.5G LAN2
▼USB3.2 Gen1

- 6 ▲2.5G LAN1
▼USB3.2 Gen1

- 7 ▲COM1
▼COM2

- 8 **R680E:**
USB 3.2 Gen2
Q670E:
USB 3.2 Gen1

- 9 PCIE x16

- 10 RTC Battery

- 11 Clear CMOS

- 12 Buzzer

- 13 SO-DIMM1

- 14 SO-DIMM2

- 15 M2CN3 M2-Mkey Power Select

- 16 +12V Power

- 17 CPU Fan

- 18 SMBus Header

- 19 SPI

- 20 ESPI Header

- 21 DIO (Select by resistor)

- 22 Front Panel

- 23 SATA2

- 24 SATA1

- 25 USB2_P7/8

- 26 VCC Panel Power Select

- 27 M2A-eDP/LVDS Inverter Power

- 28 AT/ATX Mode Select

- 29 Inverter Panel Power Select

- 30 Panel Backlight ON Level Select

- 31 ATX Power

Jumper Settings

28 AT/ATX Mode Select	JP26
AT Mode (Default)	1-2 On
ATX Mode	2-3 On

29 Inverter Panel Power Select	DPJP2
12V	1-2 Short
5V (Default)	2-3 Short

15 M2CN3 M2-Mkey Power Select	JP32
3V3 (Default)	1-2 On
3V3DU	2-3 On

26 VCC Panel Power Select	DPJP3
12V	1-2 Short
5V	3-4 Short
3V3 (Default)	5-6 Short

30 Panel Backlight ON Level Select	DPJP4
3V3 (Default)	1-2 Short
5V	2-3 Short

PIN Assignment

2 ▶ System Fan1 (J7)

Pin	Assignment
1	GND
2	PWM
3	TACH

17 ▶ CPU Fan (J8)

Pin	Assignment
1	GND
2	+12V
3	RPM
4	CTRL

22 ▶ Front Panel (J18)

Pin	Assignment	Pin	Assignment
1	NC	2	V_SUS_LED
3	3V3	4	V_SUS_LED
5	HD_LED#	6	SUS_LED#
7	GND	8	GND
9	SYS_RST	10	PWR_BTN
11	NC	12	---

24 ▶ SATA1 (J32)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

23 ▶ SATA2 (J17)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

21 ▶ DIO (SOJ2)

Pin	Assignment
1	DI3
2	DI2
3	DI1
4	DI0
5	DO3
6	DO2
7	DO1
8	DO0

18 ▶ SMBus Header (J12)

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK_RESUME	4	SMB_DATA_RESUME
5	SMBALERT_PCH-		

1 ▶ Front Audio (AUJ2)

Pin	Assignment
1	MIC2-L
2	AUD_GND
3	MIC2-R
4	NC
5	LINE2-R
6	MIC2-JD
7	AUD_GND
8	NC
9	LINE2-L
10	LINE2-JD

27 ▶ M2A-eDP/LVDS Inverter Power (DPJ1)

Pin	Assignment
1	GND
2	GND
3	DIMMING
4	VCC_PANEL_PWR
5	LVDS_3V3
6	BLONOFF
7	INVTER_PWR
8	INVTER_PWR

25 ► USB2_P7/8 (UBJ2)

Pin	Assignment	Pin	Assignment
1	5VSB	2	5VSB
3	USBP_C_7N	4	USBP_C_8N
5	USBP_C_7P	6	USBP_C_8P
7	GND	8	GND
9	NC	10	NC

20 ► ESPI Header (J35)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	ESPI_CLK
3	ESPI_RESET#	4	GND
5	ESPI_ALT#	6	GND
7	ESPI_D0	8	---
9	ESPI_D1	10	ESPI_CS
11	ESPI_D2	12	3V3SB
13	ESPI_D3	14	3V3SB

7 ► COM1/COM2

Pin	RS232	RS422 Full Duplex	RS485
1	DCD-	TX-	Data-
2	RD	TX+	Data+
3	TD	RX+	N.C
4	DTR-	RX-	N.C
5	GND	GND	GND
6	DSR-	N.C	N.C
7	RTS-	N.C	N.C
8	CTS-	N.C	N.C
9	RI-	N.C	N.C
10	N.C	N.C	N.C



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

