

User Manual

# SKY-MXM-2000A

MXM 3.1 Type A NVIDIA<sup>®</sup>  
Quadro<sup>®</sup> Embedded 2000 Ada  
with DP 1.4a

**ADVANTECH**

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## FCC Class A

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

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- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for assistance.

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This warranty does not apply to any products that have been repaired or altered by persons other than repair personnel authorized by Advantech, or products that have been subject to misuse, abuse, accident, or improper installation. Advantech assumes no liability under the terms of this warranty as a consequence of such events.

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1. Collect all the information about the problem encountered. (For example, CPU speed, Advantech products used, other hardware and software used, etc.) Note anything abnormal and list any onscreen messages displayed when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information readily available.
3. If your product is diagnosed as defective, obtain a return merchandise authorization (RMA) number from your dealer. This allows us to process your return more quickly.
4. Carefully pack the defective product, a completed Repair and Replacement Order Card, and a proof of purchase date (such as a photocopy of your sales receipt) into a shippable container. Products returned without a proof of purchase date are not eligible for warranty service.
5. Write the RMA number clearly on the outside of the package and ship the package prepaid to your dealer.

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## Initial Inspection

Before you begin installing your MXM GPU card, please make sure that the following materials have been shipped:

- SKY-MXM-2000A GPU card x 1
- China RoHS Doc x 1

If any of these items are missing or damaged, contact your distributor or sales representative immediately. We have carefully inspected the SKY-MXM-2000A mechanically and electrically before shipment. It should be free of marks and scratches and in perfect working order upon receipt. As you unpack the SKY-MXM-2000A, check it for signs of shipping damage. (For example, damaged box, scratches, dents, etc.) If it is damaged or it fails to meet the specifications, notify our service department or your local sales representative immediately. Also notify the carrier. Retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

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# Chapter 1

Hardware  
Configuration

## 1.1 Introduction

As an NVIDIA Elite Partner, Advantech provides MXM GPU cards featuring NVIDIA's embedded GPUs, tailored for image processing and Edge AI acceleration in manufacturing, medical, and gaming sectors. Based on NVIDIA's latest architectures, Advantech MXM GPUs offer cutting-edge technologies, ensuring high-performance computing and responsive capabilities while minimizing latency. This makes them suitable for applications like auto-optical inspection, driver-assistance systems, and surgical equipment.

The Advantech SKY-MXM-2000A MXM module stands out as a high-performance graphic module built on NVIDIA 2000 Ada chipsets. It strikes a balance between performance and stability, boasting features like 3072 CUDA cores, 8GB GDDR6 memory, and 12.99 TFLOPS Single-Precision Performance. With support for 3 display ports, it can handle tasks such as professional CAD drawing, medical applications, and image processing efficiently. Additionally, its small form factor facilitates seamless system integration, while its low power consumption ensures optimal performance in embedded solutions requiring high-end graphics capabilities.

## 1.2 Features

- NVIDIA® Quadro® 2000 Ada with MXM 3.1 TYPE A form factor (82 x 70 mm)
- Up to 3072 CUDA cores, 12.99 TFLOPS
- GDDR6 8GB memory, 128-bit, bandwidth 256 GB/s
- Up to 3 DisplayPort 1.4a outputs
- Long life cycle, supports 5+ years availability

## 1.3 Specifications

### 1.3.1 Processor System

Table 1.1: Processor System	
GPU	Quadro® 2000 Ada
Graphic architecture	NVIDIA® Ada-Lovelace™ AD107-975-A1
CUDA parallel-processing cores	3072 CUDA® cores
GPU base/boost clock	1635 MHZ / 2115 MHZ
FP32 performance	12.99 TFLOPS

### 1.3.2 Memory

Table 1.2: Memory	
Technology	GDDR6 8GB memory
Interface width	128-bit
Bandwidth	256 GB/s
Max Clock	8000 MHz

### 1.3.3 Display Support and Options

**Table 1.3: Display Support and Options**

<b>DP</b>	3 x DisplayPort 1.4a, 4K at 120Hz or 8K at 60Hz (Requires DSC) Supports HDMI 2.0 via DP dual-mode, 4K at 60 Hz
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### 1.3.4 Operating Systems

**Table 1.4: Operating Systems**

<b>Microsoft</b>	Windows 10/11 64-bit
<b>Linux</b>	Linux Drivers, 64-bit

### 1.3.5 Software Support

- CUDA Toolkit 11.8 and higher
- CUDA Compute Capability 8.9
- Direct3D 12 Ultimate supporting feature level 12.2
- Shader Model 8.9
- OpenCL™ 3.0
- OpenGL 4.6
- Vulkan 1.3

### 1.3.6 Graphics Options

The SKY-MXM-2000A supports 3 x DP 1.4a, with display options port as shown below.

<b>MXM Port</b>	DP_A	DP_B	DP_C
<b>Advantech</b>	Link A	Link B	Link C

<b>Link</b>	<b>Display</b>
Link A	DisplayPort (DP++)*
Link B	DisplayPort (DP++)*
Link C	DisplayPort (DP++)*, HDMI (optional)
Link D	Not supported
Link E	N/A

\* Dual-mode DP (DP++) support requires one of the follow external DP-AUX device when driving an external connector directly.

- DIODES PI3AUX221ZTAEX
- ANPEC APL3585

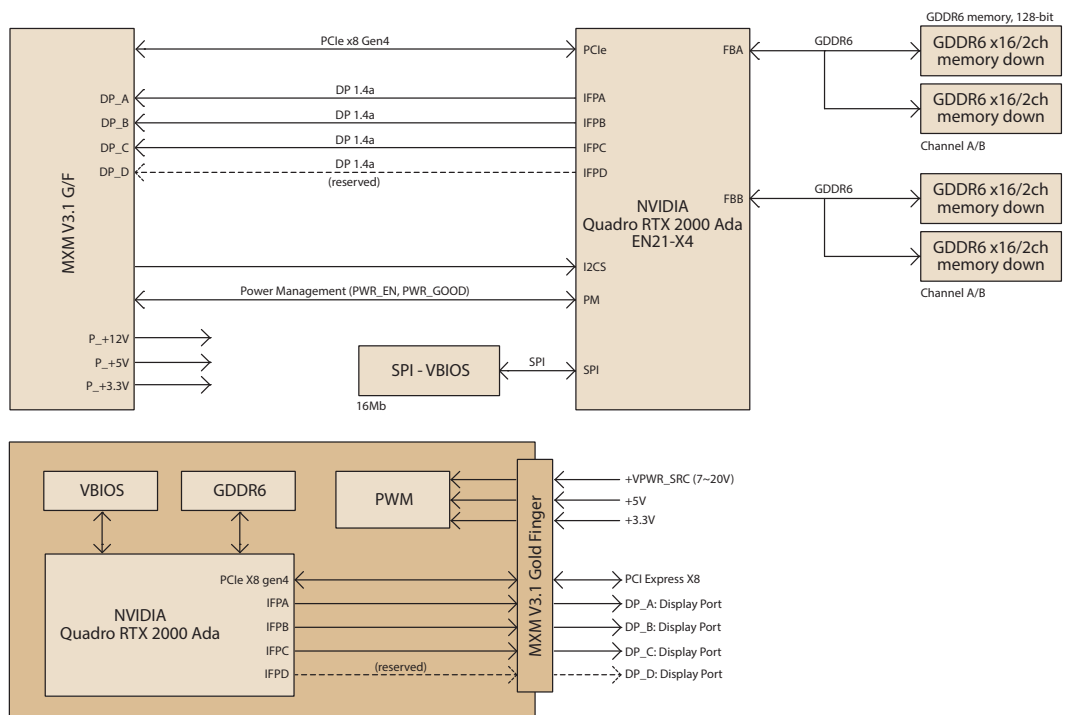
### 1.3.7 Physical

- **Dimensions:** 82 (W) x 70 (D) MM
- **Form Factor:** MXM 3.1 Type A

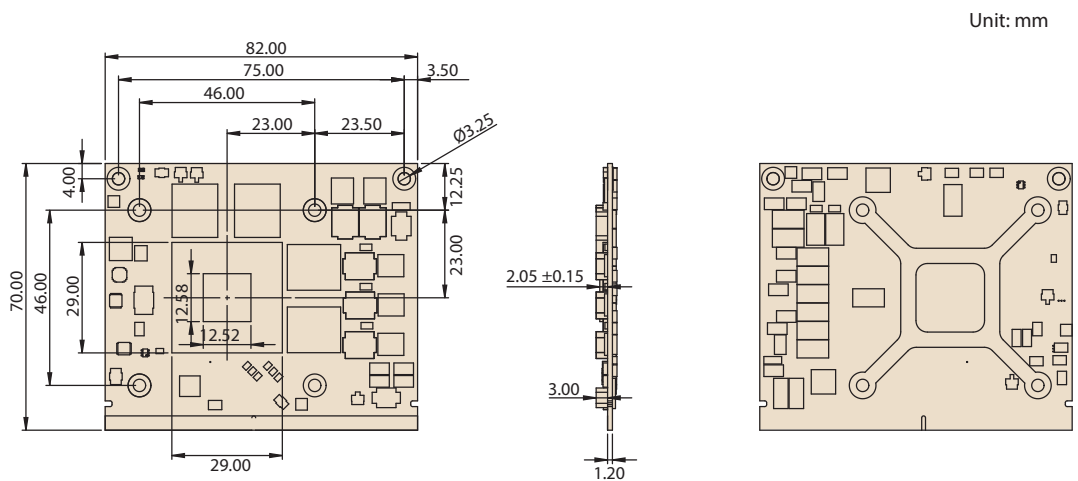
### 1.3.8 Environment

- **Operating:** 0 ~ 55°C (Depend on CPU and cooler solution)
- **Non-Operating:** -40°C ~ 85°C

# 1.4 Block Diagram



# 1.5 Board Dimensions



## 1.6 SKY-MXM-2000A Connector Pin Definition

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
E1	PWR_SRC	E2	PWR_SRC	141	PEX_RX1#	142	PEX_TX1#
E3	GND	E4	GND	143	PEX_RX1	144	PEX_TX1
1	5V	2	PRSNT_R#	145	GND	146	GND
3	5V	4	RSVD	147	PEX_RX0#	148	PEX_TX0#
5	5V	6	PWR_GOOD	149	PEX_RX0	150	PEX_TX0
7	5V	8	PWR_EN	151	GND	152	GND
9	5V	10	RSVD	153	PEX_REF-CLK#	154	PEX_CLK_REQ#
11	GND	12	GND	155	PEX_REFCLK	156	PEX_RST#
13	GND	14	RSVD	157	GND	158	RSVD
15	GND	16	RSVD	159	RSVD	160	RSVD
17	GND	18	RSVD	161	RSVD	162	RSVD
19	RSVD	20	TH_OVERT#	163	RSVD	164	RSVD
21	RSVD	22	TH_ALERT#	165	RSVD	166	GND
23	RSVD	24	RSVD	167	RSVD	168	RSVD
25	RSVD	26	RSVD	169	RSVD	170	RSVD
27	RSVD	28	RSVD	171	RSVD	172	RSVD
29	RSVD	30	RSVD	173	GND	174	GND
31	RSVD	32	SMB_DAT	175	RSVD	176	RSVD
33	RSVD	34	SMB_CLK	177	RSVD	178	RSVD
35	RSVD	36	GND	179	GND	180	GND
37	GND	38	RSVD	181	RSVD	182	RSVD
39	RSVD	40	RSVD	183	RSVD	184	RSVD
41	RSVD	42	RSVD	185	GND	186	GND
43	RSVD	44	RSVD	187	RSVD	188	RSVD
45	RSVD	46	GND	189	RSVD	190	RSVD
47	GND	48	PEX_TX15#	191	GND	192	GND
49	PEX_RX15#	50	PEX_TX15	193	RSVD	194	RSVD
51	PEX_RX15	52	GND	195	RSVD	196	RSVD
53	GND	54	PEX_TX14#	197	GND	198	GND
55	PEX_RX14#	56	PEX_TX14	199	DP_C_L0#	200	RSVD
57	PEX_RX14	58	GND	201	DP_C_L0	202	RSVD
59	GND	60	PEX_TX13#	203	GND	204	GND
61	PEX_RX13#	62	PEX_TX13	205	DP_C_L1#	206	DP_D_L0#
63	PEX_RX13	64	GND	207	DP_C_L1	208	DP_D_L0
65	GND	66	PEX_TX12#	209	GND	210	GND
67	PEX_RX12#	68	PEX_TX12	211	DP_C_L2#	212	DP_D_L1#
69	PEX_RX12	70	GND	213	DP_C_L2	214	DP_D_L1
71	GND	72	PEX_TX11#	215	GND	216	GND
73	PEX_RX11#	74	PEX_TX11	217	DP_C_L3#	218	DP_D_L2#
75	PEX_RX11	76	GND	219	DP_C_L3	220	DP_D_L2
77	GND	78	PEX_TX10#	221	GND	222	GND
79	PEX_RX10#	80	PEX_TX10	223	DP_C_AUX#	224	DP_D_L3#
81	PEX_RX10	82	GND	225	DP_C_AUX	226	DP_D_L3

83	GND	84	PEX_TX9#	227	RSVD	228	GND
85	PEX_RX9#	86	PEX_TX9	229	RSVD	230	DP_D_AUX#
87	PEX_RX9	88	GND	231	RSVD	232	DP_D_AUX
89	GND	90	PEX_TX8#	233	RSVD	234	DP_C_HPD
91	PEX_RX8#	92	PEX_TX8	235	RSVD	236	DP_D_HPD
93	PEX_RX8	94	GND	237	RSVD	238	RSVD
95	GND	96	PEX_TX7#	239	RSVD	240	3V3
97	PEX_RX7#	98	PEX_TX7	241	RSVD	242	3V3
99	PEX_RX7	100	GND	243	RSVD	244	GND
101	GND	102	PEX_TX6#	245	RSVD	246	DP_B_L0#
103	PEX_RX6#	104	PEX_TX6	247	RSVD	248	DP_B_L0
105	PEX_RX6	106	GND	249	RSVD	250	GND
107	GND	108	PEX_TX5#	251	GND	252	DP_B_L1#
109	PEX_RX5#	110	PEX_TX5	253	DP_A_L0#	254	DP_B_L1
111	PEX_RX5	112	GND	255	DP_A_L0	256	GND
113	GND	114	PEX_TX4#	257	GND	258	DP_B_L2#
115	PEX_RX4#	116	PEX_TX4	259	DP_A_L1#	260	DP_B_L2
117	PEX_RX4	118	GND	261	DP_A_L1	262	GND
119	GND	120	PEX_TX3#	263	GND	264	DP_B_L3#
121	PEX_RX3#	122	PEX_TX3	265	DP_A_L2#	266	DP_B_L3
123	PEX_RX3	124	GND	267	DP_A_L2	268	GND
125	GND	126	KEY	269	GND	270	DP_B_AUX#
127	KEY	128	KEY	271	DP_A_L3#	272	DP_B_AUX
129	KEY	130	KEY	273	DP_A_L3	274	DP_B_HPD
131	KEY	132	KEY	275	GND	276	DP_A_HPD
133	GND	134	GND	277	DP_A_AUX#	278	3V3
135	PEX_RX2#	136	PEX_TX2#	279	DP_A_AUX	280	3V3
137	PEX_RX2	138	PEX_TX2	281	PRSNT_L#	-	-
139	GND	140	GND				

## 1.7 Safety Precautions

**Warning!** Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.



**Caution!** Always ground yourself to remove any static charge before touching the MXM GPU card. Modern electronic devices are very sensitive to static electric discharges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis.



# Chapter 2

## System Requirements

## 2.1 Power Sequencing

There is no power sequencing requirement for the input voltages to the MXM module. However the PWR\_EN signal may be asserted only after all power rails are within specified tolerance. The state of PWR\_GOOD is undefined until all rails are fully ramped. Refer to Figure 2.1 for details.

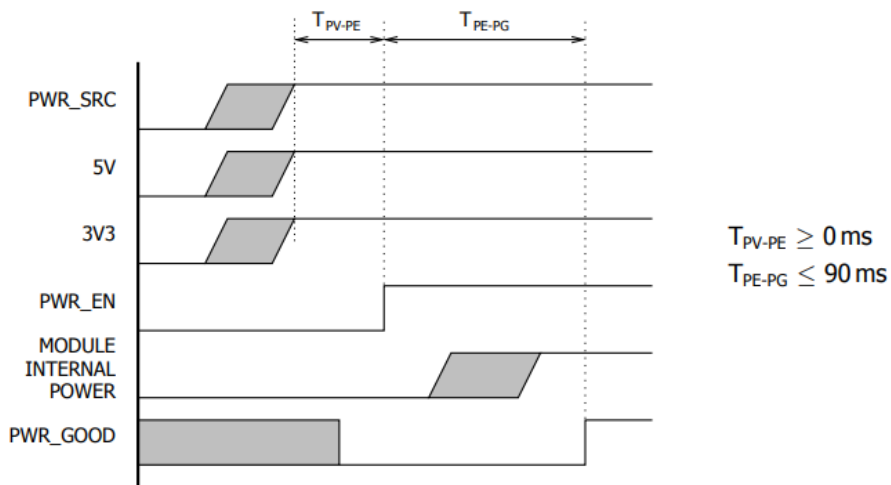


Figure 2.1 Power Sequencing

Table 2.1: Signals Exempted from

Group	Signals
Power and Thermal	SMB_CLK, SMB_DAT, TH_OVER#, TH_ALERT#, PWR_GOOD
System Management	WAKE#, PEX_CLK_REQ#
Display	DP_x_HPD, LVDS_x_HPD

**Note!** No voltage shall be applied to any MXM module signal pin (except power pins and open drain signals specified in Table 2.1) until PWR\_GOOD is asserted.



## 2.2 Input Voltage Requirements

Input voltage to the GPU must strictly follow the ranges listed in the below table (reference to GND).

Operating outside the required voltage range will cause permanent damage to the GPU

Table 2.2: Input Voltage Requirements

Power Rail	Min.	Typ.	Max.
PWR_SRC	6.5V	7-20V	22V
5V	4.7V	5V	5.3V
3V3	3.1V	3.3V	3.5V

## 2.3 Module Power Down and Power Up

The MXM module may be powered down using the PWR\_EN signal. The system designer may choose to shut down or keep the input power while the module is powered down. Refer to Figure 2.2 for details.

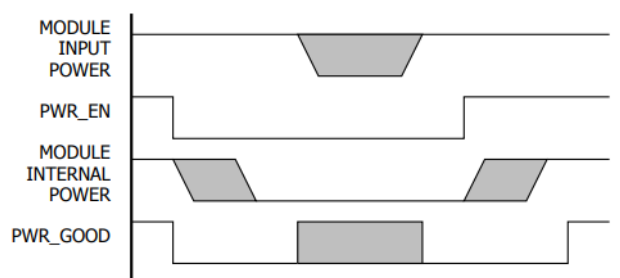


Figure 2.2 Power Sequencing

**Note!** All output signals from the MXM module are undefined when PWR\_GOOD is deasserted or undefined. The system is recommended to gate critical signals using an appropriate qualifier.



## 2.4 Reset Requirements

System reset may be deasserted only after the assertion of the PWR\_GOOD signal. Figure 2.3 shows the reset requirements relative to the PWR\_EN and PWR\_GOOD signals. This sequence must be followed on initial power on, system reset and resume from suspend/hibernate.

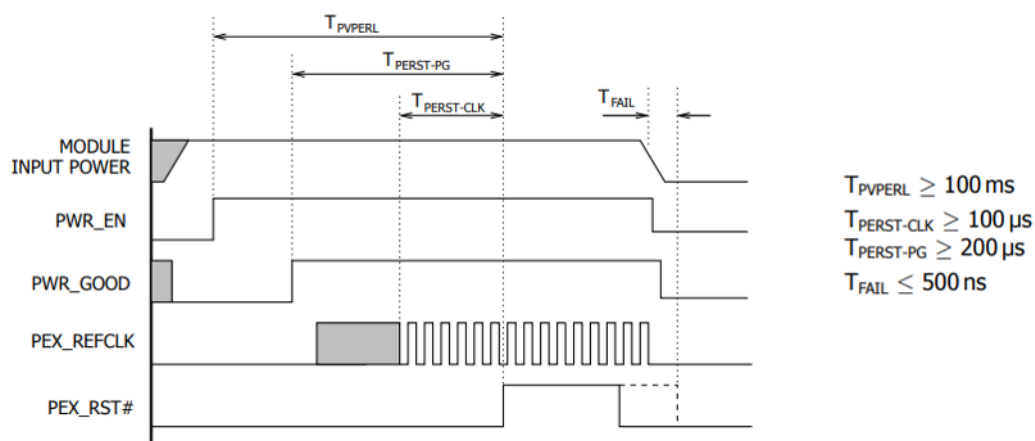


Figure 2.3 Reset Sequencing

**Note!** In order to reduce boot time, a system that monitors the PWR\_GOOD signal, is allowed to violate the TPVPERL specification as long as the TPERST-PG timing is still met.



## 2.5 Dual-Mode DisplayPort Configuration

The NVIDIA GPU also supports Multimode DisplayPort configurations. The dual-mode DisplayPort interface allows for support for an HDMI dongle on this interface. To support this dual mode, the AUX channel must be able to support I2C signals, which are required for HDMI. Hence, the ability to switch between AUX and I2C signals is required. This can be implemented with a simple DP-AUX device that is controlled by the DP Mode (DA\_MODE) signal (pin 13) on the DisplayPort connector.

Figure 2.4 is a conceptual diagram illustrating the connections required for the Dual-Mode DisplayPort mode. In the Dual-Mode case, the following are all required:

100 k $\Omega$  pull-downs on both AUX\_N and AUX\_P placed near the GPU

A DP-AUX device used to short-out the capacitor and level shift when a DP to HDMI dongle is detected.

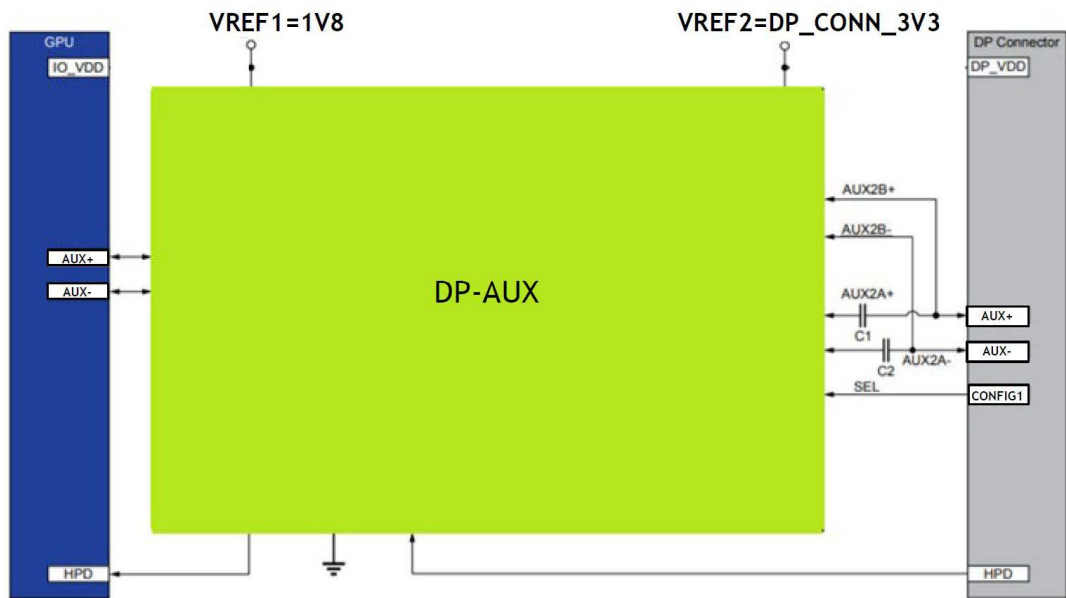


Figure 2.4 AUX in Dual Mode

## 2.6 HDMI on DP Interface

Native DVI or HDMI connector support can be implemented using a display port interface. Additional circuitry is required on the system and the proper signal mapping must be observed. As Figure 2.5 shows, 499  $\Omega$  1% pull-down resistors to ground on the DP lane signals must be placed on the connector side of the AC coupling capacitors gated by a FET to limit the leakage. Additionally level shifting circuits must also be implemented on DDC Data and Clock, refer to the MXM Version 3.0 System Design Guide for specific details.

Table 2.2 shows the mapping to connect the signals from the MXM connector to the HDMI/DVI connector. For the optional dual-link DVI support refer to Table 2.3.

Table 2.3: DisplayPort Multiplexed Signal Definition	
Pin Name	DVI/HDMI
DP_x_L0 DP_x_L0#	TX_x_D2 TX_x_D2#
DP_x_L1 DP_x_L1#	TX_x_D1 TX_x_D1#
DP_x_L2 DP_x_L2#	TX_x_D0 TX_x_D0#
DP_x_L3 DP_x_L3#	TX_x_CLK TX_x_CLK#
DP_x_AUX	DDC_x_CLK
DP_x_AUX#	DDC_x_DAT

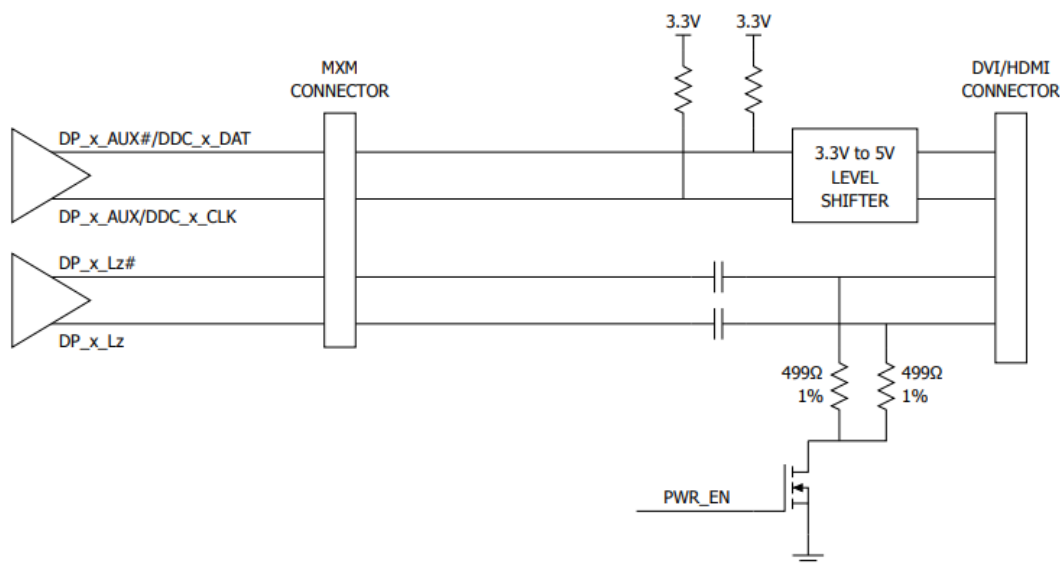


Figure 2.5 Dual-mode DisplayPort Implementation

### 2.6.1 HDMI Interface

The High-Definition Multimedia Interface (HDMI) supports the Transition Minimized Differential Signaling (TMDS) and Fixed Rate Link (FRL) and can provide up to 48 Gbps. The HDMI Connector uses the DDC interface for monitor identification and capabilities. The recommended pull-up resistor is a 2 k $\Omega$  (1% precision) to the DDC\_5V power rail. Near the GPU, the I2C lines are pulled up via a 10 k $\Omega$ . DDC filters are recommended and may be tuned during the validation phase for each PCB design to meet the display validation criteria and/or EMI compliance. The DDC output requires a level shifter circuit because the IFPx\_AUX lines are not 3.3 V tolerant.

## 2.7 Mechanical and Environmental Specifications

Table 2.4: Package Loading Specifications						
Symbol	Parameter	Min	Nominal	Max	Units	Notes
Pcont	Maximum allowable pressure during PCA, system assembly and operation.	-	≤60	≤80	psi	1
Treflow	Maximum package temperature during surface mount to printed circuit board	Refer to NVIDIA specifications 630-0011-001				
emax	Maximum allowable strain during PCA, system assembly or operation			≤500	μstrains	2

**Note!**



1. *This specification is based on the following conditions:*
  - *This specification is based on solder ball deformation and die chips and cracks. Additional requirements may be needed to meet the thermal performance and/or long term reliability as to specific application,*
  - *When a compliant thermal interface is used between die and heat sink, the bond line thickness must have less than 20% in variation.*
  - *The pressure should be measured on the top of the die surface by an instrument equipped pressure sensors.*
  - *Nominal pressure is the total force divided by the die surface area. Since the pressure may have variations across the whole surfaces. The following additional requirement is applied:*
  - *The pressure has to be measured from the top of the die surface with a grid resolution of 1x1mm<sup>2</sup> for the pressure sensor.*
  - *Both nominal and maximum pressure requirement must be met.*
2. *Strain measurement shall follow IPC-9704, particularly on following items:*
  - *The strain shall be measured on the top side of PCB close to the four corners of the package. A rigid PCB is assumed.*
  - *For generic application, the max. allowable strain must be no more than 500 μstrains for a board thickness from 1.0 to 3.2mm. A separate requirement may be specified and the qualification test should be performed if*
    - *A sensitive PCB laminate and build up structure is used where the pad cratering occurs at a PCB strain of 500 μstrains or below.*
    - *A weak surface finish of PCB is used where cracked solder joint has been observed at a PCB strain of 500 μstrains or below.*
    - *The strain rate is too high (5000 μstrains/second) during the PCA operations.*
  - *For PCB thickness less than 1,0mm, the max. allowable strain shall follow IPC 9704.*

## 2.8 GPU Thermal Policy

GPU Core Clock throttles at temperatures (Tj) higher than those listed in below table, which lists the throttling temperature and behavior. Thermal throttling is necessary to ensure that the hottest temperature on the die does not exceed the sense temperature for prolonged periods of time.

Table 2.5: GPU Thermal Policy		
Parameter	SKY-MXM-2000A	Units
Thermal Resistance (Junction to Case, RJC)	0.048	°C/W
Thermal Resistance (Junction to PCB Board, RJB)	1.450	°C/W
GPU Shutdown Temperature (OVERT)*	101	°C
GPU Slowdown Temperature (THERM_ALERT)**	91	°C
GPU Maximum Operating Temperature***	89	°C
GPU Target Temperature	87	°C

**Note!** \*OVERT assertion results in an 87.5% (÷8) hardware clock slowdown.



\*\*THERM\_ALERT assertion results in a 50%(÷2) hardware clock slowdown.

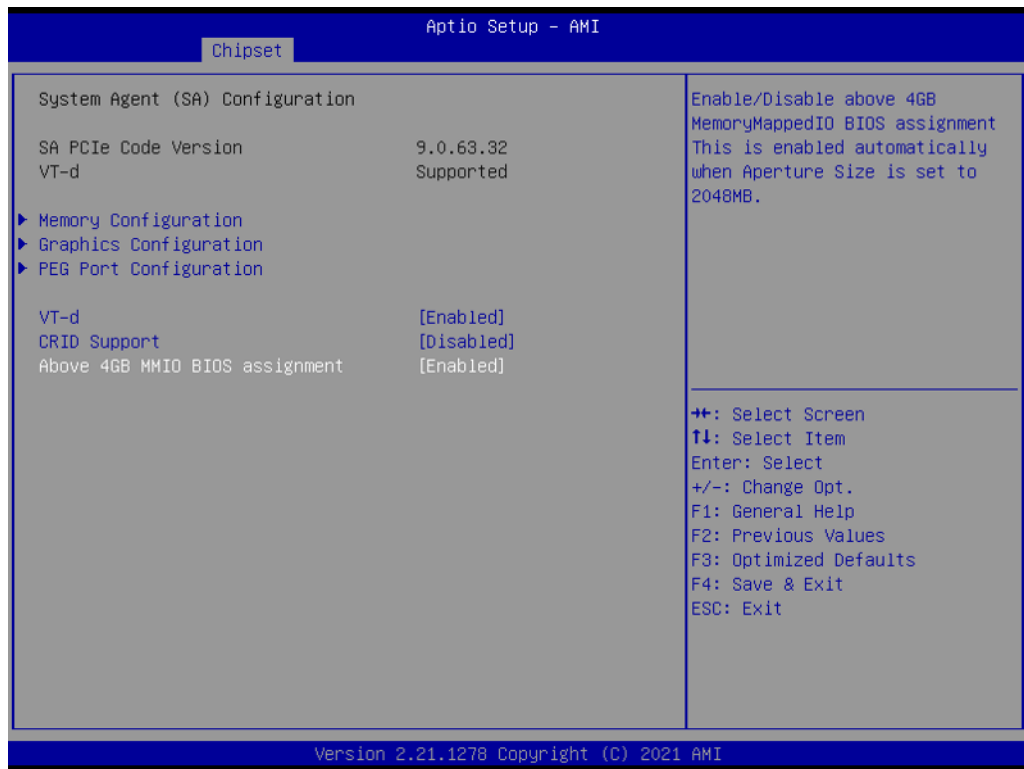
\*\*\*The GPU maximum operating temperature is the maximum GPU temperature at which the GPU is guaranteed to operate at the target performance (Base Clock) under the total board power level.

## 2.9 System BIOS Requirements

Before you install the MXM module, please make sure your system BIOS is configured with the following settings:

1. CSM Support [Disabled]
2. Above 4G Decoding [Enabled]





**Note!** Option names may vary depending on system architecture.





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