



1	DC-In	21	SATA0
2	USB3.1	22	SATA Power
3	USB3.1	23	Front Panel
4	2.5G LAN	24	DP++
5	LAN	25	Vertical USB
6	COM1	26	COM3
7	COM2	27	System Fan
8	USB2.0	28	COM4
9	eSPI Header (Debug)	29	VGA
10	COM1 (Option)	30	Clear Cmos
11	SMBus		
12	RTC Battery		
13	COM2 (Option)		
14	CAN BUS Termination		
15	Case Open		
16	COM2/CAN BUS Select		
17	COM2/CAN BUS Select		
18	COM1/DIO Select		
19	COM1/DIO Select		
20	Front Audio		

## Jumper Settings

30 Clear CMOS	SW4
Normal (default)	1-2 Open
Clear CMOS Data	1-2 Short

14 CAN BUS Termination	JP13
Middle of the bus (default)	1-2 On
End of the bus	2-3 On

## PIN Assignment

### 27 ▶ System Fan (J8)

Pin	Assignment
1	GND
2	PWM
3	TACH

### 12 ▶ RTC Battery (J20)

Pin	Assignment
1	V_BAT
2	GND

### 22 ▶ SATA Power (CN8)

Pin	Assignment
1	12V
2	GND
3	GND
4	5V

### 21 ▶ SATA0 (J2)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### 15 ▶ Case Open (J6)

Pin	Assignment
1	Case Open-
2	GND

### 23 ▶ Front Panel (JP12)

Pin	Assignment	Pin	Assignment
1	PWR_BTN0	2	V_SUS_LED
3	GND	4	SUS_LED#
5	SYS_RST0	6	HD_LED#

### 11 ▶ SMBus (J14)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_ALERT#	6	----

### 10 ▶ COM1 \*option (TSJ1)

Pin	Assignment	Pin	Assignment
1	X_MDCD1-	2	X_MSIN1- COM
3	X_MSO1- COM	4	X_MDTR1- COM
5	GND	6	X_MDSR1-
7	X_MRST1-	8	X_MCTS1-
9	X_MRI1-	10	----

### 13 ▶ COM2 \*option (TSJ7)

Pin	Assignment	Pin	Assignment
1	X_MDCD2-	2	X_MSIN2- COM
3	X_MSO2- COM	4	X_MDTR2- COM
5	GND	6	X_MDSR2-
7	X_MRST2-	8	X_MCTS2-
9	X_MRI2-	10	----

### 26 ▶ COM3 (TSJ3)

Pin	Assignment	Pin	Assignment
1	MDCD3-	2	MSIN3-
3	MSO3-	4	MDTR3-
5	GND	6	MDSR3-
7	MRST3-	8	MCTS3-
9	MRI3-	10	----

### 28 ▶ COM4 (TSJ4)

Pin	Assignment	Pin	Assignment
1	MDCD4-	2	MSIN4-
3	MSO4-	4	MDTR4-
5	GND	6	MDSR4-
7	MRST4-	8	MCTS4-
9	MRI4-	10	----

## 8 ► USB2.0 (UBJ1)

Pin	Assignment	Pin	Assignment
1	5V	2	5V
3	USB2_7_N	4	USB2_8_N
5	USB2_7_P	6	USB2_8_P
7	GND	8	GND
9	----	10	NC

## 20 ► Front Audio (AUJ1)

Pin	Assignment	Pin	Assignment
1	MIC2_L	2	GND
3	MIC2_R	4	NC
5	LINE2_R	6	MIC2-JD
7	A_GND	8	NC
9	LINE2_L	10	LINE2_JD

## 18 ► COM1/DIO BUS Select (JP8)

Pin	Assignment	Pin	Assignment	Pin	Assignment
1	MDCD1-	2	X_MDCD1-	3	D_IOA0
4	MSIN1- COM	5	X_MSIN1- COM	6	D_IOA1
7	MSO1- COM	8	X_MSO1- COM	9	D_IOA2
10	MDTR1- COM	11	X_MDTR1- COM	12	D_IOA3

## 19 ► COM1/DIO BUS Select (JP9)

Pin	Assignment	Pin	Assignment	Pin	Assignment
1	MDSR1-	2	X_MDSR1-	3	D_IOA6
4	MRTS1-	5	X_MRTS1-	6	D_IOA7
7	MCTS1-	8	X_MCTS1-	9	D_IOA4
10	MRI1-	11	X_MRI1-	12	D_IOA5

## 16 ► COM2/CAN BUS Select (JP10)

Pin	Assignment	Pin	Assignment	Pin	Assignment
1	MDCD2-	2	X_MDCD2-	3	NC
4	MSIN2- COM	5	X_MSIN2- COM	6	CAN0_L
7	MSO2- COM	8	X_MSO2- COM	9	SOUT5
10	MDTR2- COM	11	X_MDTR2- COM	12	SIN5

## 17 ► COM2/CAN BUS Select (JP11)

Pin	Assignment	Pin	Assignment	Pin	Assignment
1	MDSR2-	2	X_MDSR2-	3	NC
4	MRTS2-	5	X_MRTS2-	6	CAN0_H
7	MCTS2-	8	X_MCTS2-	9	NC
10	MRI2-	11	X_MRI2-	12	NC

## 9 ► eSPI \*Debug (J3)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	ESPI_CLK
3	ESPI_RST#	4	GND
5	ESPI_ALERT#	6	GND
7	ESPI_IO0	8	---
9	ESPI_IO1	10	ESPI_CS
11	ESPI_IO2	12	3V3SB
13	ESPI_IO3	14	3V3SB

## 29 ► VGA (J27)

Pin	Assignment	Pin	Assignment
1	VGA_RED	2	VGA_GRE
3	VGA_BLE	4	NC
5	GND	6	GND
7	GND	8	GND
9	V_VDO_5V	10	GND
11	NC	12	VGA_SDA_5V
13	V_HSYNC	14	V_VSYNC
15	VGA_SCL_5V	16	NC



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