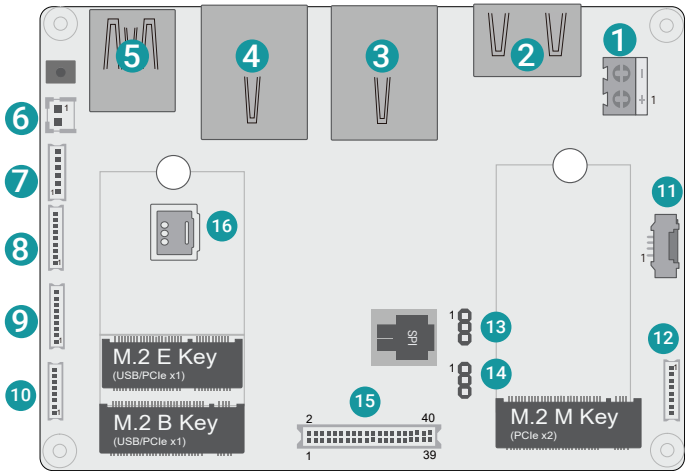


Board Layout

Top View



1 Power Connector

2 HDMI

3 LAN1

4 LAN2

5 USB3.2 Gen2

6 RTC Battery

7 Front Panel

8 COM1

9 COM2

10 USB2.0

11 CPU Fan

12 Front Audio

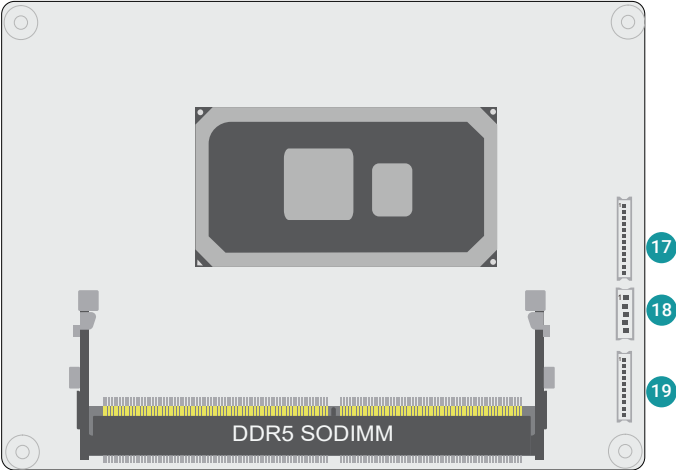
13 Panel Inverter
Backlight Power Selection

14 Panel VDD Power Jumper
Panel Backlight Selection

15 LVDS/eDP (Opt.)

16 Nano SIM Slot

Bottom View



- 17 eSPI & UART Debug
- 18 I2C/SMBus (Opt.)
- 19 DIO



Jumper Settings

13 Panel Inverter/Backlight Power Selection	DPJP2
12V (Default)	1-2 On
5V	2-3 On

14 Panel VDD Power/Panel Backlight Selection	DPJP3
5V	1-2 On
3.3V (Default)	2-3 On



PIN Assignment

1 ► Power Connector (CN4)

Pin	Assignment
1	DC_IN
2	GND

6 ► RTC Battery (J7)

Pin	Assignment
1	RTC Battery +
2	RTC Battery -

11 ► CPU Fan (J6)

Pin	Assignment
1	GND
2	12V
3	TACH
4	PWM

18 ► I2C/SMBUS (Opt.) (J5)

Pin	Assignment
1	3V3SB
2	GND
3	I2C0_CLK
4	I2C0_SDA
5	I2C0_INT

7 ► Front Panel (J11)

Pin	Assignment
1	Power Button
2	GND
3	Reset
4	SUS LED-
5	SUS LED PWR
6	HDD LED-

10 ► USB2.0 (J4)

Pin	Assignment
1	VCC
2	-DATA
3	+DATA
4	GND
5	VCC
6	-DATA
7	+DATA
8	GND

12 ► Front Audio (AUJ1)

Pin	Assignment
1	Mic-L
2	GND
3	Mic-R
4	Line-Out-R
5	Mic-JD
6	GND
7	Line-Out-L
8	Line-JD

19 ► DIO (J1)

Pin	Assignment
1	DIO7
2	DIO6
3	DIO5
4	DIO4
5	DIO3
6	DIO2
7	DIO1
8	DIO0
9	5VSB
10	GND

17 ► eSPI & UART Debug (J8)

Pin	Assignment
1	3V3SB
2	ESPI_RST#
3	ESPI_ALT#
4	ESPI_D0
5	ESPI_D1
6	ESPI_D2
7	ESPI_D3
8	ESPI_CLK
9	ESPI_CS
10	UART_RX
11	UART_TX
12	GND

15 ► LVDS/eDP (Opt.) (DPJ1)

Pin	Assignment	Pin	Assignment
1	LVDS_A_LANE3_P	2	LVDS_B_LANE3_P
3	LVDS_A_LANE3_N	4	LVDS_B_LANE3_N
5	GND	6	GND
7	LVDS_A_LANE2_P	8	LVDS_B_LANE2_P
9	LVDS_A_LANE2_N	10	LVDS_B_LANE2_N
11	GND	12	GND
13	LVDS_A_LANE1_P	14	LVDS_B_LANE1_P / eDP_LANE1_P
15	LVDS_A_LANE1_N	16	LVDS_B_LANE1_N / eDP_LANE1_N
17	GND	18	GND
19	LVDS_A_LANE0_P	20	LVDS_B_LANE0_P / eDP_LANE0_P
21	LVDS_A_LANE0_N	22	LVDS_B_LANE0_N / eDP_LANE0_N
23	GND	24	GND
25	LVDS_A_CLK_P	26	LVDS_B_CLK_P / eDP_AUX_P
27	LVDS_A_CLK_N	28	LVDS_B_CLK_N / eDP_AUX_N
29	GND	30	GND
31	LVDS_DDC_CLK	32	eDP_HPD
33	LVDS_DDC_DATA	34	BL_ON_OFF
35	GND	36	LVDS_3V3 (1A)
37	INV_PWR (5V/12V, 1.5A)	38	DIMMING
39		40	PANEL_PWR (3.3V/5V,1A)

8 ► COM1 (TSJ1)

Pin	RS232	RS422	RS485
1	DCD-	TXD-	Data-
2	SIN	TXD+	Data+
3	SOUT	RXD+	N.C.
4	DTR-	RXD-	N.C.
5	GND	GND	GND
6	DSR-	N.C	N.C.
7	RTS-	N.C	N.C.
8	CTS-	N.C	N.C.
9	RI-	N.C	N.C.

9 ► COM2 (TSJ2)

Pin	RS232	RS422	RS485
1	DCD-	TXD-	Data-
2	SIN	TXD+	Data+
3	SOUT	RXD+	N.C.
4	DTR-	RXD-	N.C.
5	GND	GND	GND
6	DSR-	N.C	N.C.
7	RTS-	N.C	N.C.
8	CTS-	N.C	N.C.
9	RI-	N.C	N.C.



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

