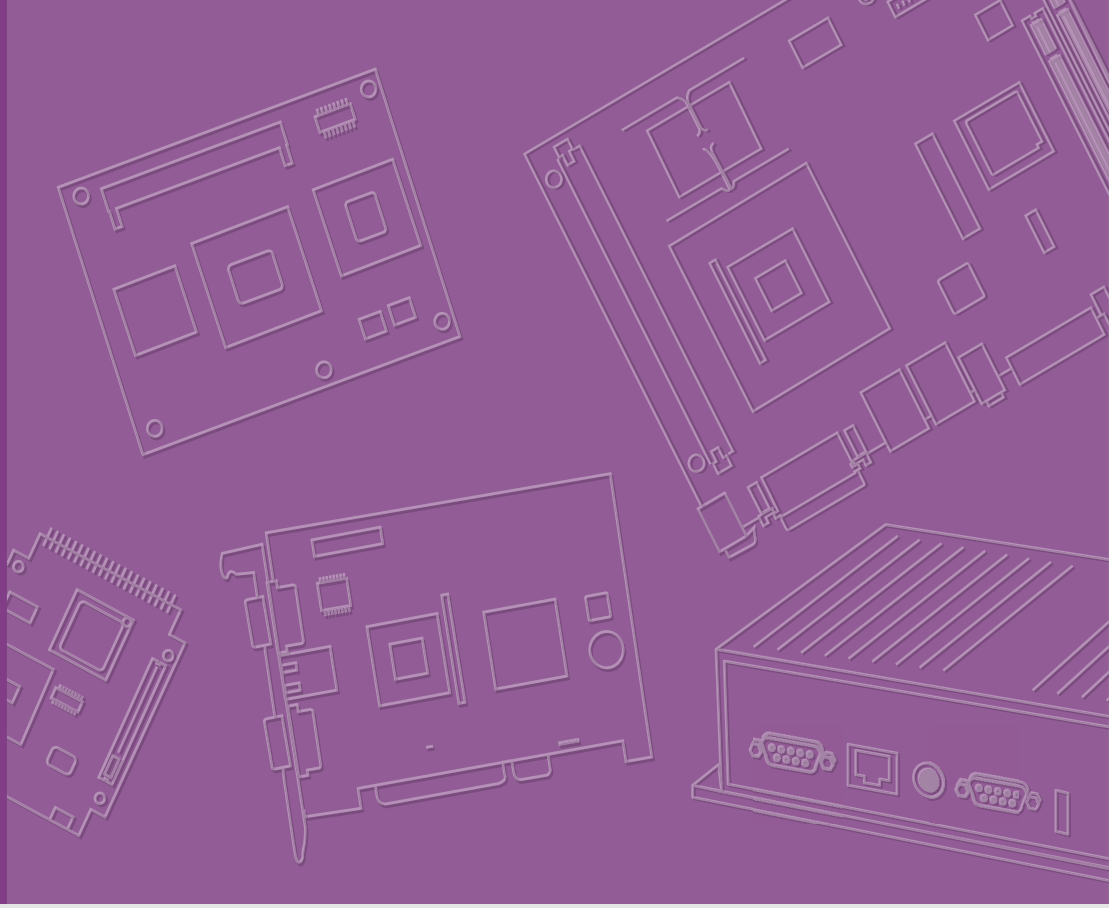


User Manual

SOM-5883

CPU Computer on Module

ADVANTECH

Enabling an Intelligent Planet

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Product Warranty (2 years)

Advantech warrants the original purchaser that each of its products will be free from defects in materials and workmanship for two years from the date of purchase.

This warranty does not apply to any products that have been repaired or altered by persons other than repair personnel authorized by Advantech, or products that have been subject to misuse, abuse, accident, or improper installation. Advantech assumes no liability under the terms of this warranty as a consequence of such events.

Because of Advantech's high quality-control standards and rigorous testing, most customers never need to use our repair service. If an Advantech product is defective, it will be repaired or replaced free of charge during the warranty period. For out-of-warranty repairs, customers will be billed according to the cost of replacement materials, service time, and freight. Please consult your dealer for more details.

If you believe your product to be defective, follow the steps outlined below.

1. Collect all the information about the problem encountered. (For example, CPU speed, Advantech products used, other hardware and software used, etc.) Note anything abnormal and list any onscreen messages displayed when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information readily available.
3. If your product is diagnosed as defective, obtain a return merchandise authorization (RMA) number from your dealer. This allows us to process your return more quickly.
4. Carefully pack the defective product, a completed Repair and Replacement Order Card, and a proof of purchase date (such as a photocopy of your sales receipt) into a shippable container. Products returned without a proof of purchase date are not eligible for warranty service.
5. Write the RMA number clearly on the outside of the package and ship the package prepaid to your dealer.

Part No. 2006588300 Edition 1

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Declaration of Conformity

CE

This product has passed the CE test for environmental specifications when shielded cables are used for external wiring. We recommend the use of shielded cables. This type of cable is available from Advantech. Please contact your local supplier for ordering information.

Test conditions for passing also include the equipment being operated within an industrial enclosure. In order to protect the product from damage caused by electrostatic discharge (ESD) and EMI leakage, we strongly recommend the use of CE-compliant industrial enclosure products.

FCC Class B

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for assistance.

FM

This equipment has passed FM certification. According to the National Fire Protection Association, work sites are categorized into different classes, divisions, and groups based on hazard considerations. This equipment is compliant with the specifications for Class I, Division 2, Groups A, B, C, and D indoor hazards.

Technical Support and Assistance

1. Visit the Advantech website at www.advantech.com/support to obtain the latest product information.
2. Contact your distributor, sales representative, or Advantech's customer service center for technical support if you need additional assistance. Please have the following information ready before calling:
   - Product name and serial number
   - Description of your peripheral attachments
   - Description of your software (operating system, version, application software, etc.)
   - A complete description of the problem
   - The exact wording of any error messages

Warnings, Cautions, and Notes

Warning! *Warnings indicate conditions that if not observed can cause personal injury!*



Caution! *Cautions are included to help prevent hardware damage and data losses. For example,*



"Batteries are at risk of exploding if incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions."

Note! *Notes provide additional, optional information.*



Document Feedback

To assist us with improving this manual, we welcome all comments and constructive criticism. Please send all feedback in writing to support@advantech.com.

Selection Guide w/ P/N

Part No.	CPU	Core	Base Freq.	Max. Boost Freq.	CPU TDP	LLC	PCH	DDR4 SODIMM	SSD	USB4	Thermal solution	Operating Temp.
SOM-5883C7Q-U6A1	Core i7-11850HE	8	2.6GHz	4.7GHz	45W/35W	24MB	QM580E	non-ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883C5Q-U6A1	Core i5-11500HE	6	2.6GHz	4.5GHz	45W/35W	12MB	QM580E	non-ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883C3Q-U4A1	Core i3-11100HE	4	2.4GHz	4.4GHz	45W/35W	8MB	QM580E	non-ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883C3QA-U4A1	Core i3-11100HE	4	2.4GHz	4.4GHz	45W/35W	8MB	QM580E	non-ECC	N/A	Option*	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883CRQ-U6A1	Celeron 6600HE	2	2.6GHz	N/A	35W	8MB	QM580E	non-ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883D8R-U6A1	Xeon W-11865MRE (8C)	8	2.6GHz	4.7GHz	45W/35W	24MB	RM590E	ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883D6R-U6A1	Xeon W-11555MRE (6C)	6	2.6GHz	4.5GHz	45W/35W	12MB	RM590E	ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883D4R-U4A1	Xeon W-11155MRE (4C)	4	2.4GHz	4.4GHz	45W/35W	8MB	RM590E	ECC	N/A	N/A	Active	0 ~ 60 °C; 32 ~ 140 °F
SOM-5883D8RX-U6A1	Xeon W-11865MRE (8C)	8	2.6GHz	4.7GHz	45W/35W	24MB	RM590E	ECC	N/A	N/A	Active	-40 ~ 85 °C;-40 ~ 185 °F
SOM-5883D6RX-U6A1	Xeon W-11555MRE (6C)	6	2.6GHz	4.5GHz	45W/35W	12MB	RM590E	ECC	N/A	N/A	Active	-40 ~ 85 °C;-40 ~ 185 °F
SOM-5883D4RX-U4A1	Xeon W-11155MRE (4C)	4	2.4GHz	4.4GHz	45W/35W	8MB	RM590E	ECC	N/A	N/A	Active	-40 ~ 85 °C;-40 ~ 185 °F
SOM-5883D8RX-S5A1	Xeon W-11865MLE (8C)	8	1.5GHz	4.5GHz	25W	24MB	RM590E	ECC	N/A	N/A	Active	-40 ~ 85 °C;-40 ~ 185 °F

Packing List

Before system installation, check that the items listed below are included and in good condition. If any item does not accord with the list, contact your dealer immediately.

Part No.	Description	Quantity
-	SOM-5883 COM module	1
1970005238T000	Heatspreader of SOM-5883	1

Development Board

Part No.	Description
SOM-DB5830-00A2	COMe Devel. Board COMe R3.0 Type6 pin-out (LVDS) 0 ~ 60 °C; 32 ~ 140 °F
SOM-DB5830A-00A2	COMe Devel. Board COMe R3.0 Type6 pin-out (eDP) 0 ~ 60 °C; 32 ~ 140 °F
SOM-DB5830X-00A2	COMe Devel. Board COMe R3.0 Type6 pin-out (LVDS) -40 ~ 85 °C;-40 ~ 185 °F

Optional Accessory

Part No.	Description
1970005036T001	Semi-Cooler 125 x 95 x 33.2 mm (4.8 x 3.7 x 1.3 in) with 12V Fan

Safety Precaution - Static Electricity

Follow these simple precautions to protect yourself from harm and the products from damage.

- To avoid electrical shock, always disconnect the power from the PC chassis before manual handling. Do not touch any components on the CPU card or other cards while the PC is powered on.
- Disconnect the power before making any configuration changes. A sudden rush of power after connecting a jumper or installing a card may damage sensitive electronic components.

Safety Instructions

1. Read these safety instructions carefully.
2. Retain this user manual for future reference.
3. Disconnect the equipment from all power outlets before cleaning. Use only a damp cloth for cleaning. Do not use liquid or spray detergents.
4. For pluggable equipment, the power outlet socket must be located near the equipment and easily accessible.
5. Protect the equipment from humidity.
6. Place the equipment on a reliable surface during installation. Dropping or letting the equipment fall may cause damage.
7. The openings on the enclosure are for air convection. Protect the equipment from overheating. Do not cover the openings.
8. Ensure that the voltage of the power source is correct before connecting the equipment to a power outlet.
9. Position the power cord away from high-traffic areas. Do not place anything over the power cord.
10. All cautions and warnings on the equipment should be noted.
11. If the equipment is not used for a long time, disconnect it from the power source to avoid damage from transient overvoltage.
12. Never pour liquid into an opening. This may cause fire or electrical shock.
13. Never open the equipment. For safety reasons, the equipment should be opened only by qualified service personnel.
14. If any of the following occurs, have the equipment checked by service personnel:
    - The power cord or plug is damaged.
    - Liquid has penetrated the equipment.
    - The equipment has been exposed to moisture.
    - The equipment is malfunctioning, or does not operate according to the user manual.
    - The equipment has been dropped and damaged.
    - The equipment shows obvious signs of breakage.
15. Do not leave the equipment in an environment with a storage temperature of below -20 °C (-4 °F) or above 60 °C (140 °F) as this may damage the components. The equipment should be kept in a controlled environment.
16. CAUTION: Batteries are at risk of exploding if incorrectly replaced. Replace only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.
17. In accordance with IEC 704-1:1982 specifications, the sound pressure level at the operator's position does not exceed 70 dB (A).

DISCLAIMER: These instructions are provided according to IEC 704-1 standards. Advantech disclaims all responsibility for the accuracy of any statements contained herein.

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Chapter 1

General Information

This chapter details background information on the SOM-5883 CPU Computer on Module.

Sections include:

- Introduction
- Functional Block Diagram
- Product Specifications

1.1 Introduction

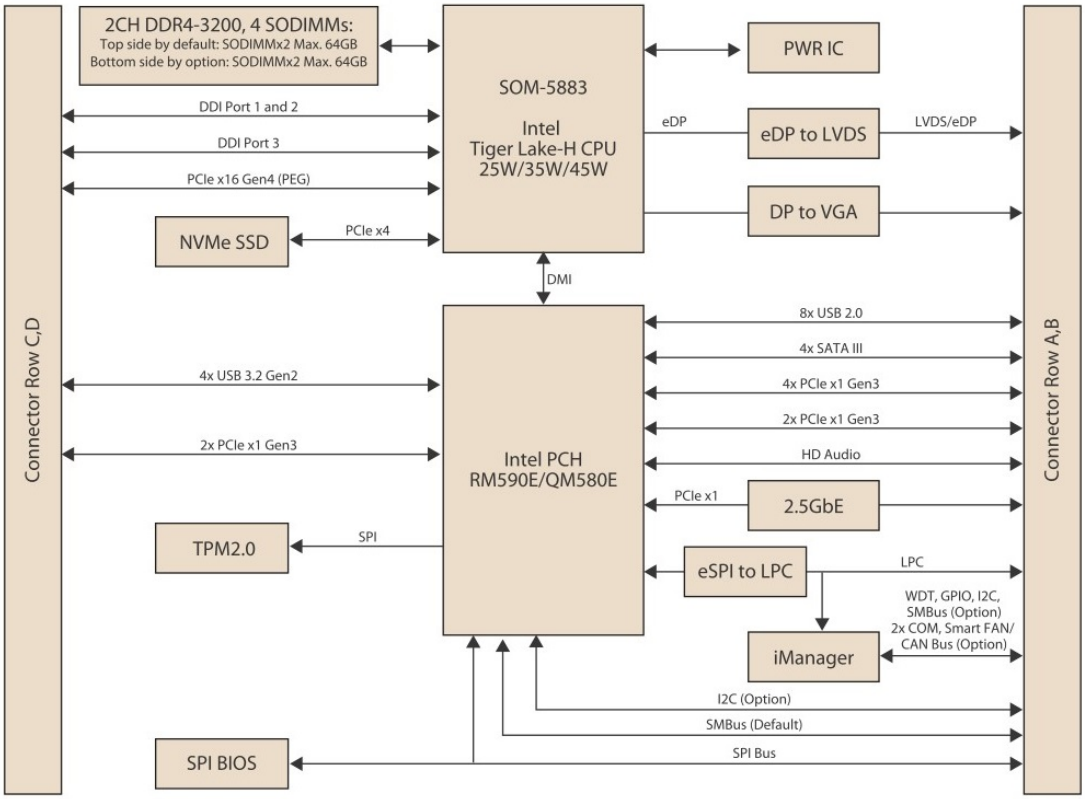
SOM-5883 is a COM Express Type 6 Basic Module with an 11th Gen Intel® Core™ processor. It delivers octa-core computing performance under TDP 45W, while providing Intel Iris Xe graphics, and Advantech's Edge AI Suite software toolkit.

SOM-5883's memory supports up to 128GB DDR4 3200 RAM (1.7 x computing performance growth and 1.5 x the 3D graphics performance when compared to previous generation solutions). It supports numerous super-speed I/O interfaces e.g. PCIe Gen 4 (16GT/s), 2.5Gbase-T, and USB 3.2 Gen 2 (10Gbps). SOM-5883 also supports the use of 4 x 4K independent displays via 3 x DisplayPort 1.4/HDMI 2.1, 1 x optional eDP or LVDS, and 1 x VGA interface. SOM-5883 can be configured with up to 2 x 8K HDR outputs. It also supports optional NVMe SSD, TPM 2.0 on-board design, 8.5 ~ 20V power input, and wide operating temperatures (-40 ~ 85 °C; - 40 ~ 185 °F). It includes a heat spreader & QFCS solution.

Advantech iManager (SUSI 4) satisfies diverse embedded application requirements by providing a multi-level watchdog timer, voltage and temperature monitoring, thermal protection and mitigation through processor throttling, LCD backlight on/off and brightness control, and embedded storage for customized information. By combining Advantech WISE-PaaS/RMM, it can monitor and control devices remotely over the internet for easy maintenance. All Advantech SMARC modules integrate iManager and WISE-PaaS/RMM as an add-on value for diverse customer applications.

Acronyms	
Term	Definition/Meaning
AC'97	Audio CODEC (Coder-Decoder)
ACPI	Advanced Configuration Power Interface – standard to implement power saving modes in PC-AT systems
BIOS	Basic Input Output System – firmware in PC-AT system that is used to initialize system components before handing control over to the operating system
CAN	Controller-area network (CAN or CAN-bus) is a vehicle bus standard designed to allow microcontrollers to communicate with each other within a vehicle without a host computer
DDI	Digital Display Interface – containing DisplayPort, HDMI/DVI, and SDVO
EAPI	Embedded Application Programmable Interface Software interface for COM Express® specific industrial function ■ System information ■ Watchdog timer ■ I2C Bus ■ Flat-panel brightness control ■ User storage area ■ GPIO
GbE	Gigabit Ethernet
GPIO	General purpose input output
HDA	Intel High Definition Audio (HD Audio) refers to the specification released by Intel in 2004 for delivering high definition audio that is capable of playing back more channels at higher quality than AC'97
I^2C	Inter Integrated Circuit – 2 wire (clock and data) signaling scheme allowing communication between integrated circuit, primarily used to read and load register values
ME	Management Engine
PC-AT	"Personal Computer – Advanced Technology" – an IBM trademark term used to refer to Intel based personal computer in 1990s
PEG	PCI Express Graphics
RTC	Real Time Clock – battery backed circuit in PC-AT systems that keeps system time and date as well as certain system setup parameters
SPD	Serial Presence Detect – refers to serial EEPROM on DRAMs that has DRAM Module configuration information
TPM	Trusted Platform Module, chip to enhance the security features of a computer system
UEFI	Unified Extensible Firmware Interface
WDT	Watchdog Timer

1.2 Functional Block Diagram



1.3 Product Specification

1.3.1 Compliance

- PICMG COM.0 (COM Express) Revision 3.0
- Basic Size - 125 x 95 mm (4.9 x 3.7 in)
- Pin-out Type 6 compatible

1.3.2 Feature List

Feature Type	Connector Row	Feature	Type 6 Define		SOM-5883
			Max.	Min.	
Display	A-B	LVDS Channel A (18/24-bit)	1	0	1
	A-B	LVDS Channel B (18/24-bit)	1	0	1
	A-B	eDP (muxed on LVDS Channel A)	1	0	1
	A-B	VGA	1	0	1
Expansion	A-B	PCI Express x1	6	1	6
	A-B	LPC	1	1	1
Serial	A-B	SMBus	1	1	1
	A-B	I2C Bus	1	1	1
	A-B	Serial Port	2	0	2
	A-B	CAN BUS (muxed on SER1)	1	0	1
I/O	A-B	LAN Port 0 (Gigabit Ethernet)	1	1	1
	A-B	SATA	4	1	4
	A-B	USB 2.0	8	4	8
	A-B	USB Client	1	0	0
	A-B	HD Audio	1	0	1
	A-B	SPI Bus	2	1	1
	A-B	General Purpose I/O (GPIO)	8	8	8
	A-B	SDIO (muxed on GPIO)	1	0	0
	A-B	Watchdog Timer Output	1	0	1
	A-B	Speaker Out	1	1	1
	A-B	External BIOS ROM Support	2	0	1
	A-B	Power Button Support	1	1	1
	A-B	Power Good	1	1	1
	A-B	VCC_5V_SBY Contacts	4	4	4
	A-B	Sleep	1	0	1
	A-B	Thermal Protection	1	0	1
	A-B	Lid Input	1	0	1
	A-B	Battery Low Alarm	1	0	1
	A-B	Suspend/Wake Signals	3	0	3
	A-B	Fan PWM/Tachometer	2	0	2
	A-B	Trusted Platform Modules	1	0	1
Display	C-D	Digital Display Interfaces 1 - 3	3	0	3
I/O	C-D	PEG (PCI Express x16)	1	0	1
	C-D	PCI Express x1	2	0	2
	C-D	USB 3.0	4	0	4
	C-D	Rapid Shutdown	1	0	1

1.3.3 Processor System

CPU	Std. Freq.	Max. Turbo Freq.	Core	Cache (MB)	TDP(W)
i7-11850HE	2.6 GHz	4.7 GHz	8	24	45W/35W
i5-11500HE	2.6 GHz	4.5 GHz	6	12	45W/35W
i3-11100HE	2.4 GHz	4.4 GHz	4	8	45W/35W
6600HE	2.6 GHz	-	2	8	35W
W-11865MRE	2.6 GHz	4.7 GHz	8	24	45W/35W
W-11555MRE	2.6 GHz	4.5 GHz	6	12	45W/35W
W-11155MRE	2.4 GHz	4.4 GHz	4	8	45W/35W
W-11865MLE	1.5 GHz	4.5 GHz	8	24	25W

1.3.4 Memory

There are a total of 4 memory sockets on SOM-5883, top side 2pcs by default and bottom side 2pcs optional. This solution supports max 128GB capacity (utilizing ECC memory modules with specific SKU.) with 260pin SO-DIMM sockets (dual-channel).

1.3.5 Graphics/Audio

Graphics Core: 12th Gen Intel® Iris Xe Graphics Core supports DX12.1, OGL4.6, OCL3.0, and MPEG2, HEVC/H265, AVC/VP9 HW decode/encode/transcode acceleration.

CPU	Graphic Core	Base Freq.	Max Freq.
i7-11850HE	12th Gen Iris® Xe Graphics	350	1350
i5-11500HE	12th Gen Iris® Xe Graphics	350	1350
i3-11100HE	12th Gen Iris® Xe Graphics	350	1350
6600HE	12th Gen Iris® Xe Graphics	350	1350
W-11865MRE	12th Gen Iris® Xe Graphics	350	1350
W-11555MRE	12th Gen Iris® Xe Graphics	350	1350
W-11155MRE	12th Gen Iris® Xe Graphics	350	1250
W-11865MLE	12th Gen Iris® Xe Graphics	350	1350

- Dual display:
  - VGA+LVDS
  - VGA+DDI1
  - LVDS+DDI2
  - LVDS+DDI3
- Triple display:
  - VGA+DDI1+LVDS
  - DDI1+DDI2+DDI3
- Quad display:
  - LVDS+DDI1+DDI2+DDI3

1.3.6 Expansion Interfaces

PCIe x1

PCI Express x1: Supports default 8 x ports PCIe x1 compliant to PCIe Gen 3 (8.0 GT/s) specifications, configurable to PCIe x4 or PCIe x2. Several configurable combinations may need BIOS modification. Please contact the Advantech sales or FAE for more details.

Type 6		**Row A,B**						**Row C,D**	
		P0	P1	P2	P3	P4	P5	P6	P7
Default	Config	X1	X1	X1	X1	X1	X1	X1	X1
Option 1		X2		X1	X1	X2		X1	X1
Option 2		X2		X2		X2		X2	
Option 3		X4				X4			

1.3.7 LPC

Supports Low Pin Count (LPC) 1.1 specifications, without DMA or bus mastering. Enables connection to Super I/O, embedded controller, or TPM. 25MHz LPC clock.

1.3.8 Serial Bus

1.3.8.1 SMBus

Supports SMBus 2.0 specifications.

1.3.8.2 I2C Bus

Supports I²C bus 7-bit and 10-bit address modes. Support standard mode up to 100 Kb/s, fast mode up to 400 Kb/s.

1.3.9 I/O

1.3.9.1 Gigabit Ethernet

Ethernet: Intel® I225 Gigabit LAN supports 10/100/1000 Mbps & 2.5 Gbps Speed.

1.3.9.2 SATA

Supports 4 x ports SATA Gen3 (6.0 Gb/s), backward compliant to SATA Gen2 (3.0 Gb/s) and Gen1 (1.5 Gb/s). The maximum data rate is 600 MB/s. Supports AHCI 1.3.1 mode (does not support IDE mode).

1.3.9.3 USB 3.2/USB 2.0

COM-Express supports USB 3.0 but SOM-5883 supports 4 x USB 3.2 Gen2 (10 Gbps) ports and 8 x USB 2.0 (480 Mbps) ports which are reverse compatible to USB1.x. For USB 3.2, product supports LPM (U0, U1, U2, and U3) for power efficiency.

Notice: To meet USB 3.2 Gen2 performance, Advantech strongly recommends using a certificated cable.

1.3.9.4 USB 3.2

Type 6	P0	P1	P2	P3
SoC	P0	P1	P2	P3
Type 6	OC_01		OC_23	

SoC USB_OC#	OC_0	OC_2

1.3.9.5 USB 2.0

Type 6	P0	P1	P2	P3
SoC	P0	P1	P2	P3
Type 6	OC_01		OC_23	
SoC USB_OC#	OC_0		OC_2	

1.3.9.6 SPI Bus

Supports BIOS flash only. SPI clock can be 20MHz, with capacity up to 128Mb.

1.3.9.7 GPIO

8 x programmable general purpose input or output (GPIO).

1.3.9.8 Watchdog Timer

Supports multi-level watchdog time-out output. Provides 1-65535 level, from a 100ms to 109.22 minute intervals.

1.3.9.9 Serial Port

2 x 2-wire serial port (Tx/Rx) supports 16550 UART compliance.

- Programmable FIFO or character mode
- 16-byte FIFO buffer on transmitter and receiver in FIFO mode
- Programmable serial-interface characteristics: 5, 6, 7, or 8-bit character
- Even, odd, or no parity bit selectable
- 1, 1.5, or 2 stop bit selectable
- Baud rate up to 115.2K

1.3.9.10 TPM

Supports TPM 2.0 module by default.

1.3.9.11 Smart Fan

Supports 2 x Fan PWM control signals and 2 x tachometer input for fan speed detection. Provides 1 x on module with connector and the other to the carrier board following PICMG COM Express R3.0 specifications.

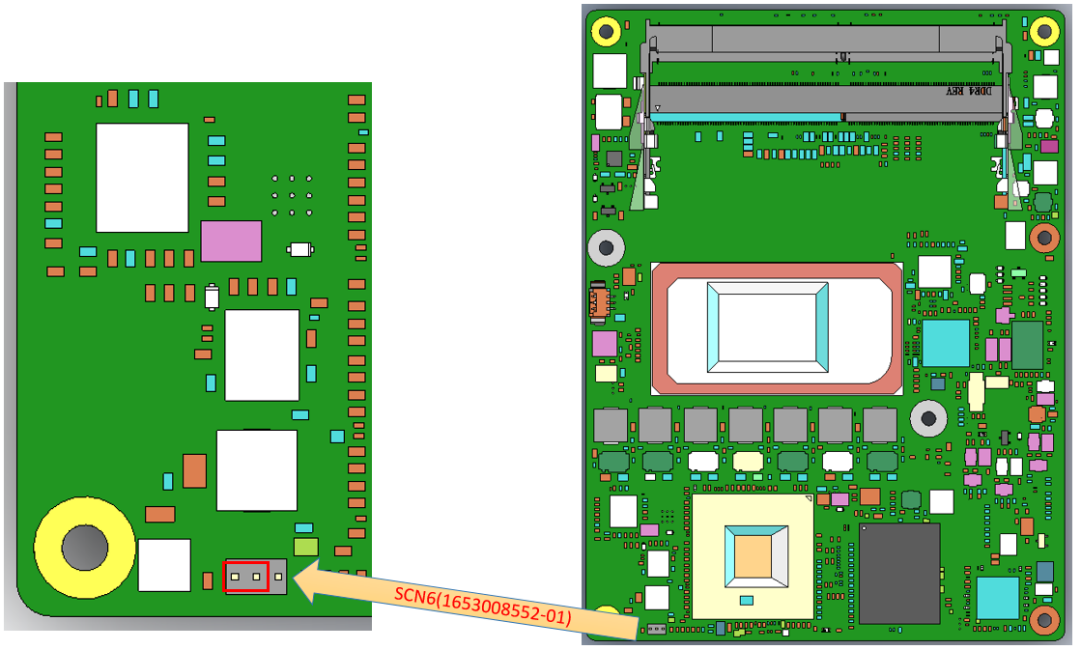
1.3.9.12 BIOS

The BIOS chip is placed on module by default. Users can place BIOS chip on the carrier board with the appropriate design and jumper setting in BIOS_DIS#[1:0].

BIOS_DIS#0	BIOS_DIS#1	Boot up destination/function
Open	Open	Boot from Module's SPI BIOS
Open	GND	SPI_CS0# to Carrier Board, SPI_CS1# to Module
GND	GND	SPI_CS0# to Module, SPI_CS1# to Carrier Board

Note: If system COMS is cleared, Advantech strongly suggests going to the BIOS setup menu and loading default settings on the first boot up.

The standard module has no jumper at SCN6, so BIOS settings are kept without a RTC coin battery. If you need to restore the BIOS to default settings, follow the steps below:



1. Remove the coin battery.
2. Put jumper on SCN6 pin1-2.
3. Turn on power supply.
4. The system will boot up a few times.
5. The BIOS will load default settings successfully.

1.3.10 Power Management

1.3.10.1 Power Supply

Supports both ATX and AT power modes. VSB is for suspended power and is optional if not required by standby (suspend-to-RAM) support. The RTC battery is optional if datekeeping/timekeeping is not required.

- VCC: 8.5V (9V-5%) – 20V (19V+5%)
- VSB: 5V +/- 5% (Suspend power)
- RTC Battery Power: 2.0V – 3.3V

1.3.10.2 PWROK

Power-good from the main power supply. A high value indicates the power level is good. This signal can be used to postpone module startup to allow carrier-based FPGAs or other configurable devices' time to be programmed.

1.3.10.3 Power Sequence

According to PICMG COM Express COM.0 R 3.0 specifications.

1.3.10.4 Wake Event

Various wake event support allows users to apply different scenarios.

- Wake-on-LAN(WOL): Wake to S0 from S3/S4/S5
- USB Wake: Wake to S0 from S3
- PCIe Device Wake: depends on user inquiry and may need customized BIOS
- LPC Wake: depends on user inquiry and may need customized BIOS

1.3.10.5 Advantech S5 ECO Mode (Deep Sleep Mode)

Advantech iManager provides additional features allowing the system to enter a very low suspended power mode – S5 ECO mode. In this mode, the module will cut all power, including suspended and active power to the chipset, and keep an on-module controller active. Only power under 50MW will be consumed, meaning user battery packs can last longer. While this mode is enabled in the BIOS, the system (or module) only allows power button boot instead of other methods such as WOL.

1.3.11 Environment

1.3.11.1 Temperature

- Operating: 0 ~ 60 °C (32 ~ 140 °F)
- Storage: -40 ~ 85 °C (-40 ~ 185 °F)

1.3.11.2 Humidity

- Operating: 40 °C @ 95% relative humidity, non-condensing
- Storage: 60 °C @ 95% relative humidity, non-condensing

1.3.11.3 Vibrations

IEC60068-2-64: Random vibration test under operation mode, 3.5 Grms.

1.3.11.4 Drop Test (Shock)

Federal Standard 101 Method 5007 test procedure with standard packing.

1.3.11.5 EMC

CE EN55022 Class B and FCC Certifications: validated with standard development boards in Advantech chassis.

1.3.12 MTBF

Please refer Advantech SOM-5883 Refresh Series Reliability Prediction report on the website: Link: http://com.advantech.com

1.3.13 OS Support

The mission of Advantech Embedded Software Services is to "Enhance quality of life with Advantech platforms and Microsoft Windows Embedded technology." We enable Windows Embedded software products on Advantech platforms to more effectively support the embedded computing community. Customers are freed from the hassle of dealing with multiple vendors (hardware suppliers, system integrators, embedded OS distributors) for projects. Our goal is to make Windows Embedded software solutions easily and widely available to the embedded computing community.

To install drivers, please connect to the website http://support.advantech.com.tw to download the setup file.

1.3.14 Advantech iManager

Supports APIs for GPIO, smart fan control, multi-stage watchdog timer, temperature sensor, and hardware monitoring. Follows PICMG EAPI 1.0 specifications with backward compatibility.

1.3.15 Power Consumption

Power Consumption Table (Watt.)						
VCC=12V, VSB=5V	Active Power Domain			Suspend Power Domain		Mechanical off
Power State	S0 Max. Load	S0 Burn-in	S0 Idle	S5	S5 Deep Sleep	RTC (uA)
SOM-5883C7Q-U6A1	138.823W	70.192W	16.3W	1.714W	0.342W	4.18μA

Hardware Configurations:

1. MB: SOM-5883C7Q-U6A1
2. DRAM: 32GB DDR4 3200MHz x 4pcs. (2pcs top side by default, 2pcs bottom side by option.)
3. Carrier board: SOM-DB5830-00A2, SOM-DB5830X-00A2, SOM-DB5830A-00A2
4. Middle Board: EA30

Test Condition:

1. Test temperature: room temperature (about 25 °C)
2. Test voltage: rated voltage DC +12.0V
3. Test loading:
   - Maximum load mode: According to Intel thermal/power test tools.
   - Burn-in mode: Burn-in test V8.1 Pro (1023) for 64 bit Windows. (CPU, RAM, 2D&3D Graphics and Disk with 100%)
   - Idle mode: DUT power management off and not running any program.
4. OS: Windows 10 Enterprise

1.3.16 Performance

To compare performance or benchmark data with other modules, please refer to “Advantech COM Performance & Power Consumption Table.”

1.3.17 Pin Description

Advantech provides useful checklists for schematic design and layout routing. The schematic checklist will specify details about each pin’s electrical properties and how to connect it in different scenarios. The layout checklist will specify the layout constraints and recommendations for trace length, impedance, and other necessary information during design.

Please contact your regional Advantech branch office to acquire design documents and further advanced support.

Chapter 2

Mechanical Information

This chapter details mechanical information for the SOM-5883 CPU Computer on Module.

Sections include:

- Board Information
- Mechanical Drawings
- Assembly Drawings

2.1 Board Information

The figures below demonstrate the main chips on SOM-5883 Computer-on-Module.

Be aware of these positions while designing your customer's carrier board to avoid mechanical damage and to improve thermal dissipation performance.

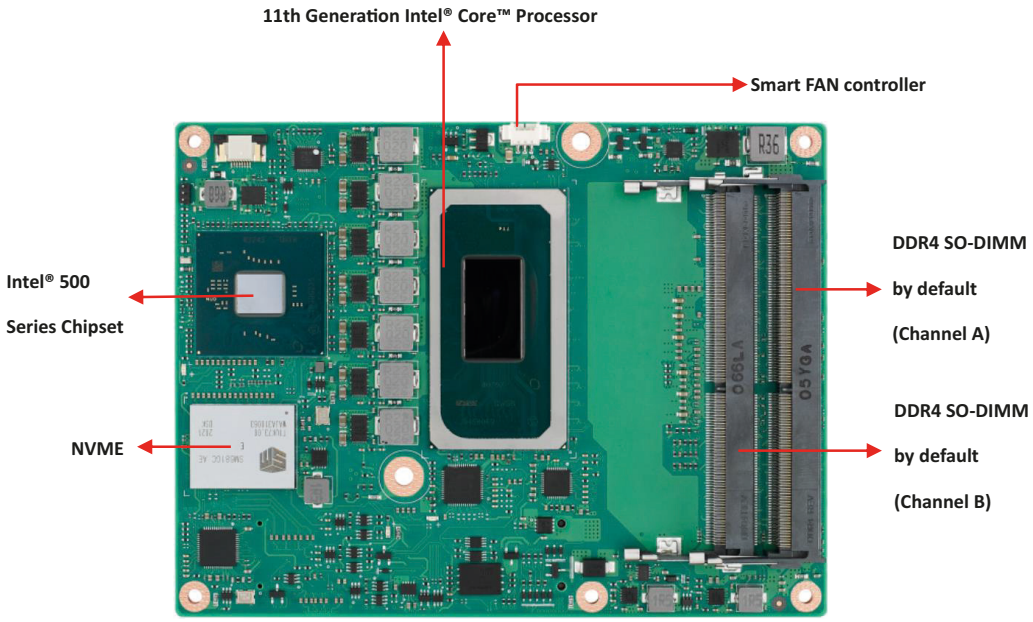


Figure 2.1 Board Chips – Front

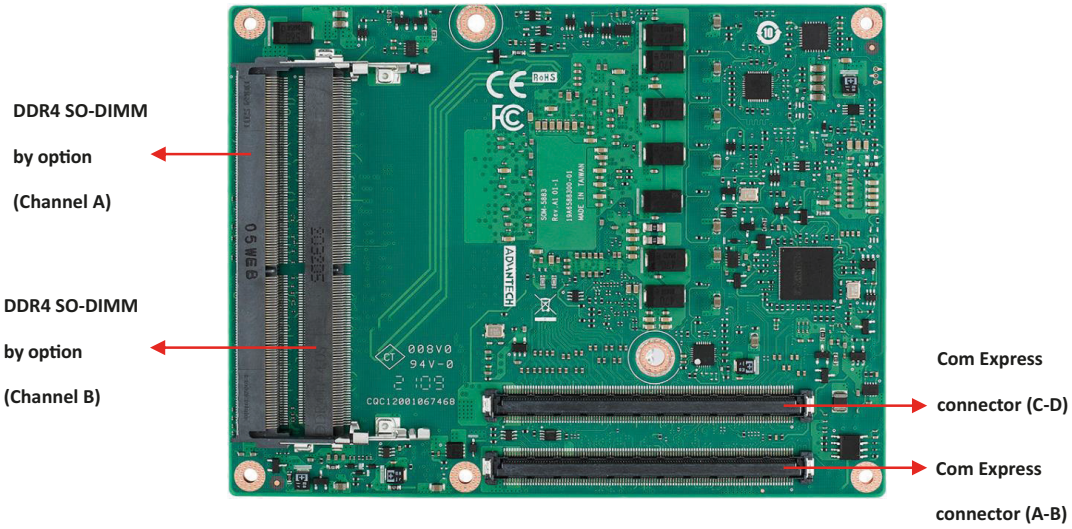


Figure 2.2 Board Chips – Rear

2.2 Mechanical Diagram

For more details regarding 2D/3D models, please visit the Advantech COM support service website http://com.advantech.com.

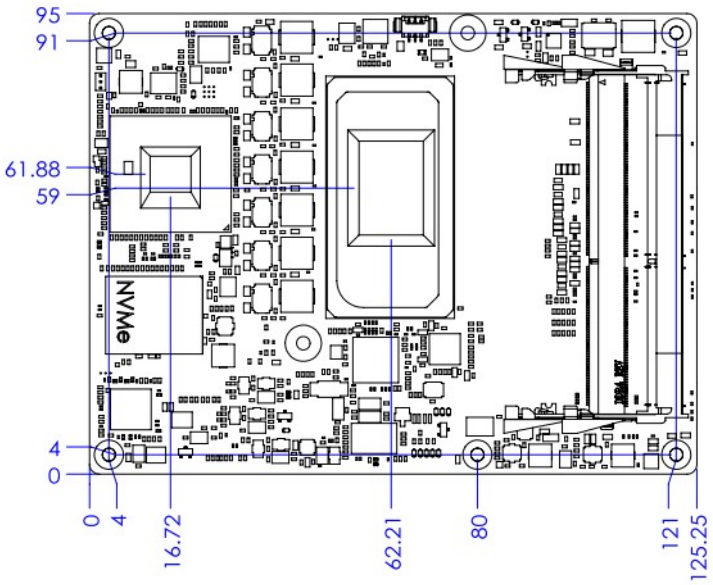


Figure 2.3 Board Mechanical Diagram – Front

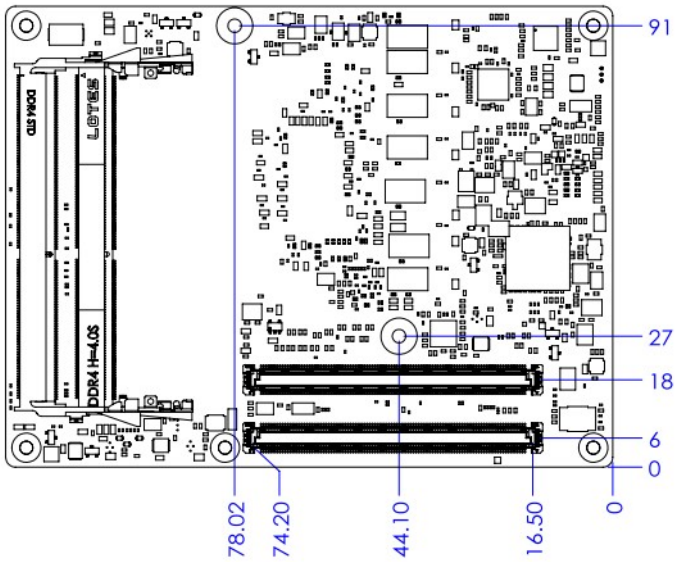


Figure 2.4 Board Mechanical Diagram – Rear

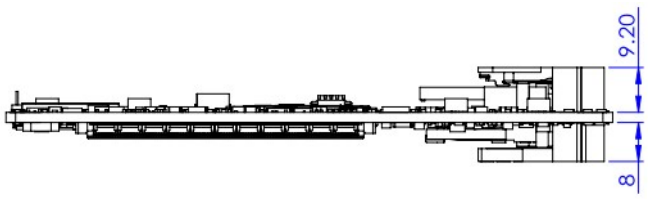


Figure 2.5 Board Mechanical Diagram – Side 1

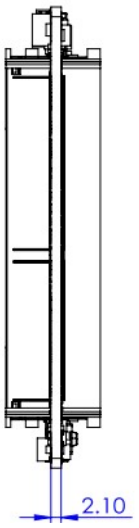


Figure 2.6 Board Mechanical Diagram – Side 2

2.3 Assembly Diagram

These figures demonstrate the order of assembly needed when attaching the thermal module and COM module to the carrier board.

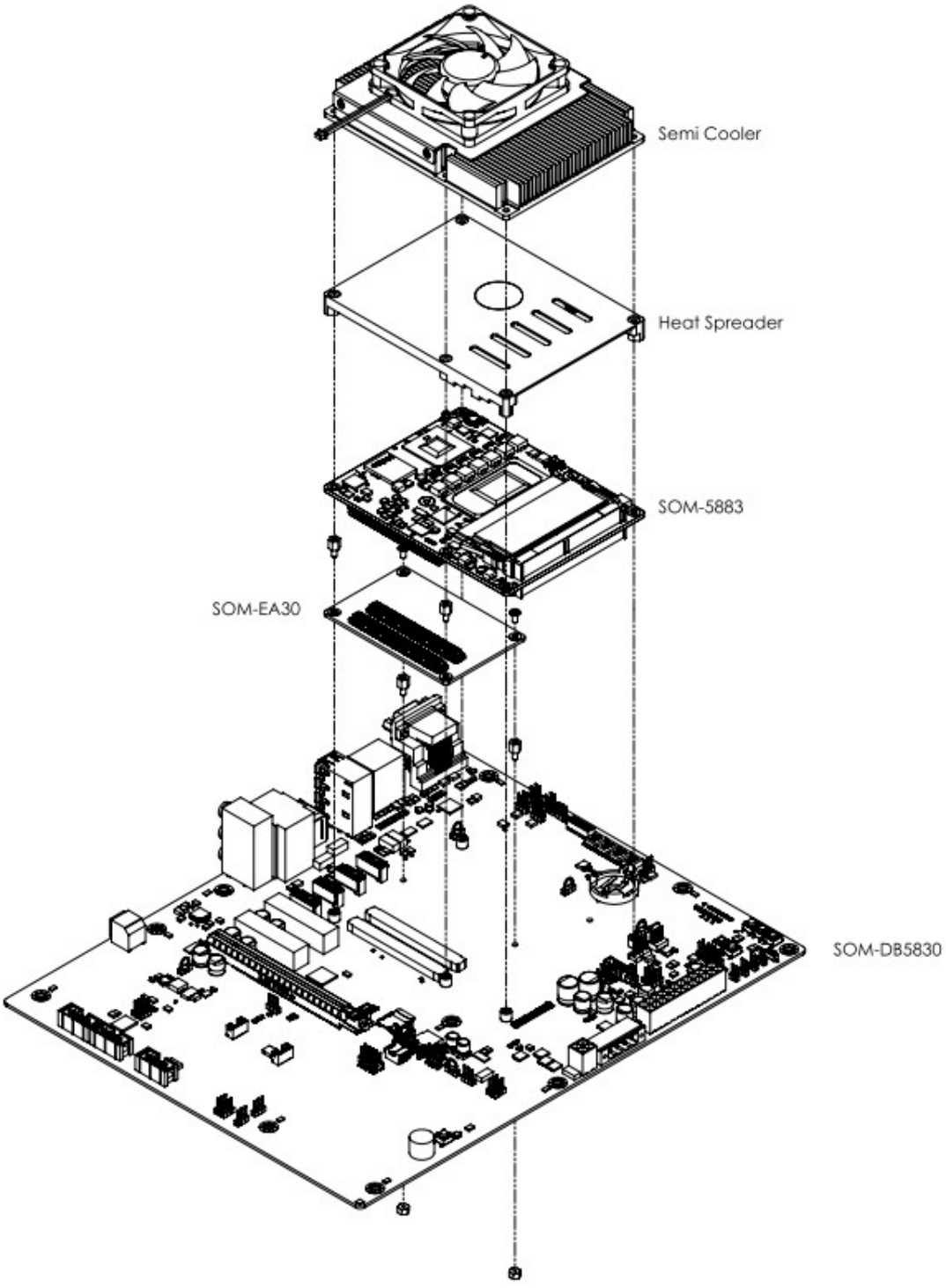


Figure 2.7 Assembly Diagram

There are 5 x reserved screw holes for SOM-5883 used to assemble the heat spreader.

2.4 Assembly Diagram

Consider the CPU and chip height tolerance when designing your thermal solution.

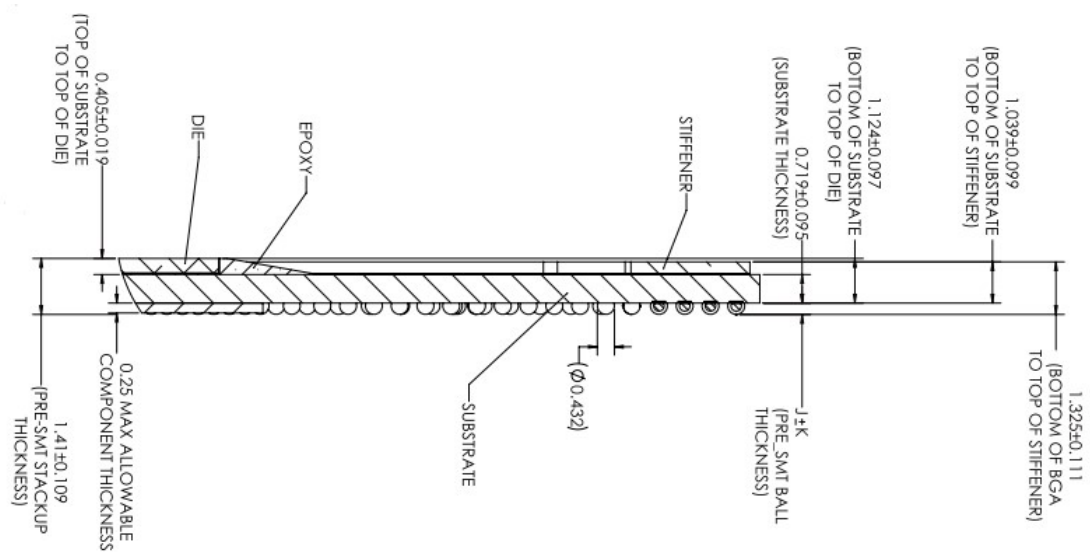


Figure 2.8 CPU Height and Tolerance

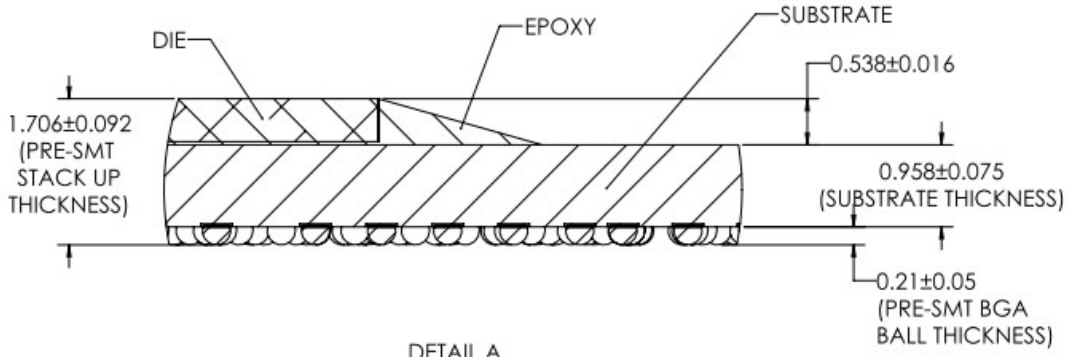


Figure 2.9 PCH Height and Tolerance

Chapter 3

AMI BIOS

This chapter details BIOS setup information for the SOM-5883 CPU computer-on module.

Sections include:

- Introduction
- Entering Setup
- Hot/Operation Key
- Exit BIOS Setup Utility

3.1 Introduction

AMI BIOS has been integrated into many motherboards for over a decade. The AMI BIOS Setup Utility enables users to modify the BIOS settings and control various system features. This chapter describes the basic navigation of the BIOS Setup Utility.

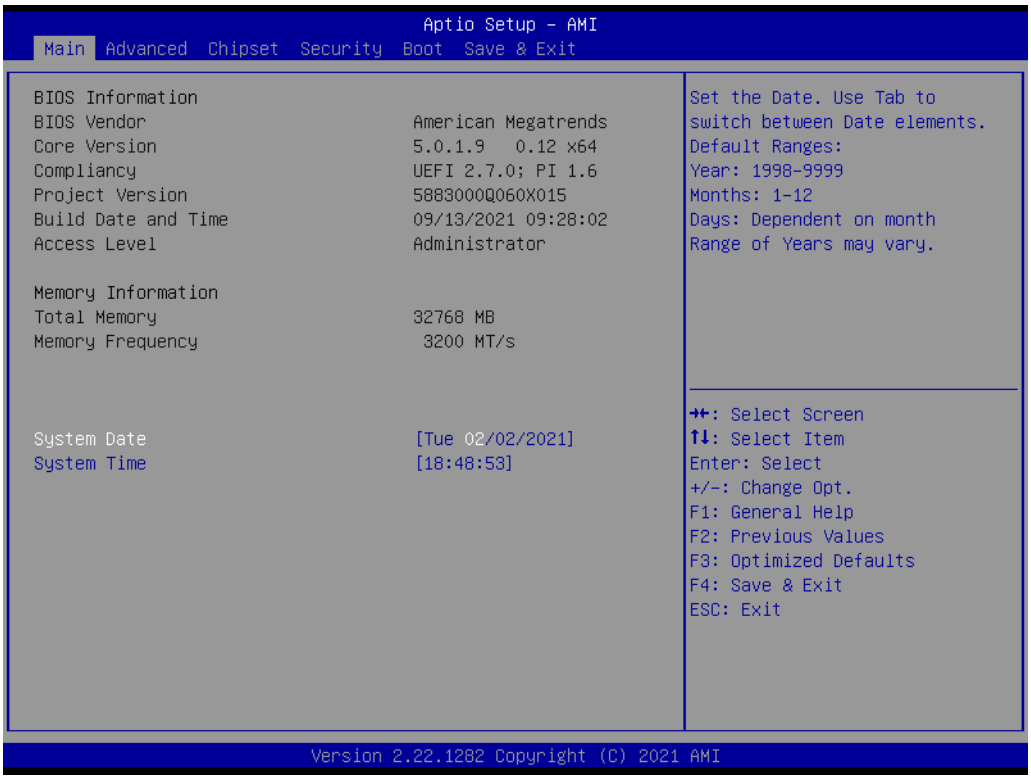


Figure 3.1 Setup Program Initial Screen

AMI's BIOS ROM has a built-in setup program that allows users to modify the basic system configuration. This information is stored in flash ROM so it retains the Setup information when the power is turned off.

3.2 Entering Setup

Turn on the computer and then press <DEL> or <ESC> to enter the Setup menu.

3.3 Main Setup

When users first enter the BIOS Setup Utility, users will see the Main setup screen.

Users can always return to the Main setup screen by selecting the Main tab. There are two Main Setup options. They are described in this section. The Main BIOS Setup screen is shown below.

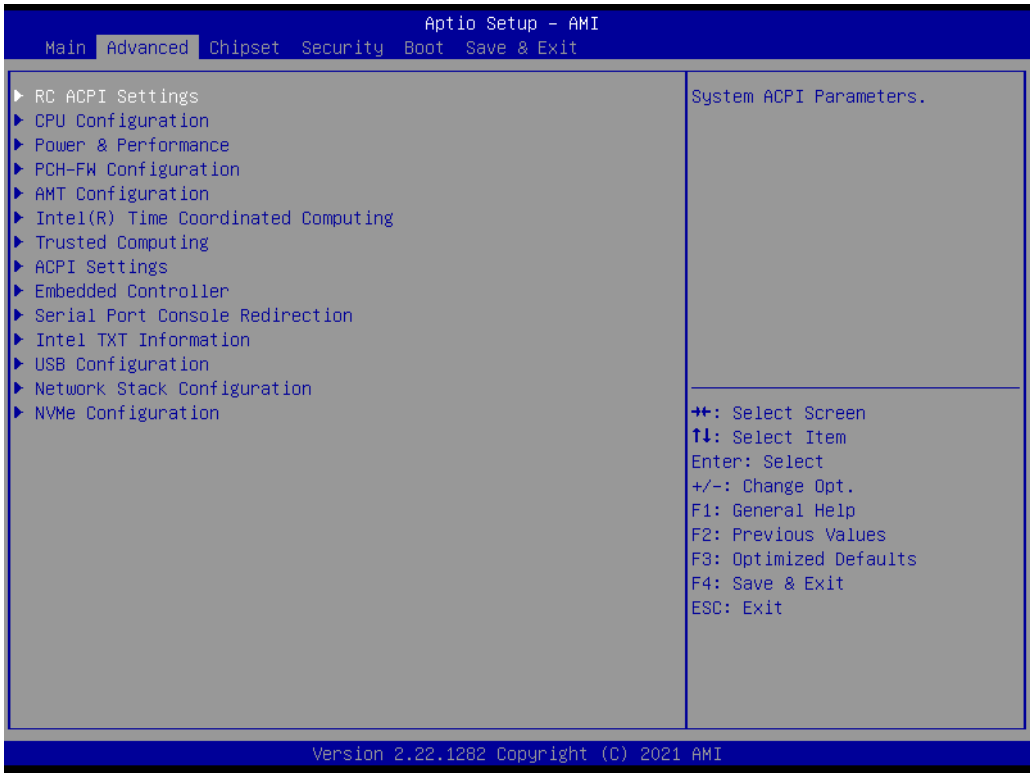


Figure 3.2 Main Setup Screen

The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can. The right frame displays the key legend.

Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

- **System time/System date**
  Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.4 Advanced BIOS Features Setup

Select the Advanced tab from the SOM-5883 setup screen to enter the Advanced BIOS Setup screen. Users can select any item in the left frame of the screen, such as CPU Configuration, to go to the sub menu for that item. Users can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screens are shown below. The sub menus are described on the following pages.

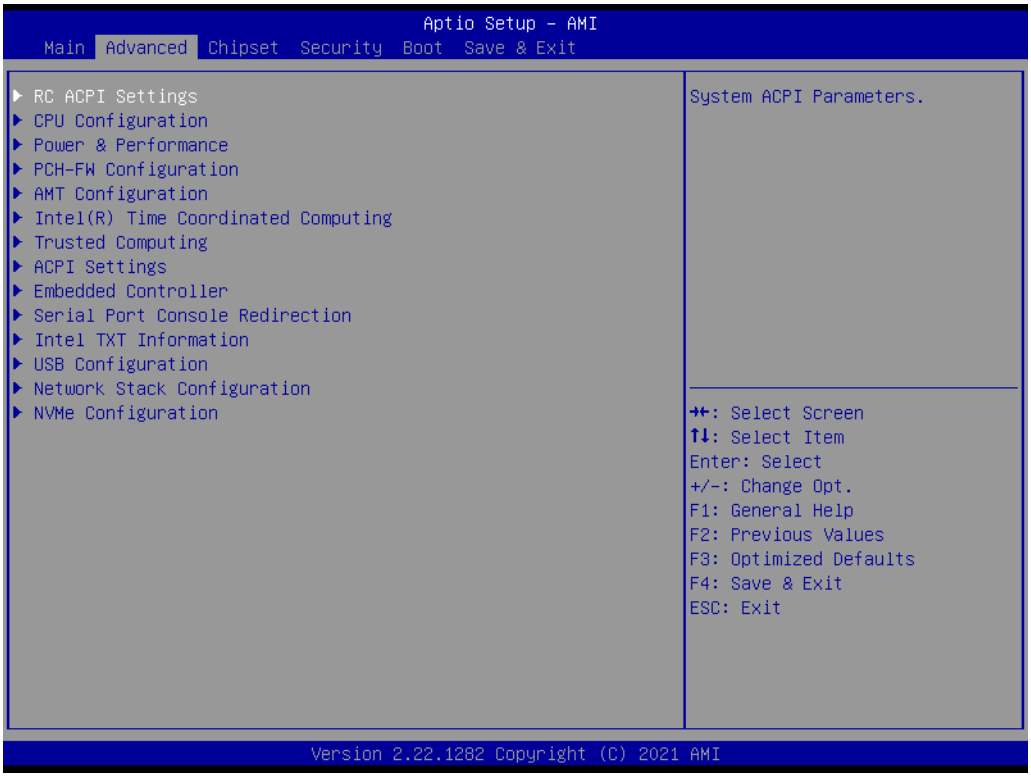


Figure 3.3 Advanced BIOS Features Setup Screen

- **RC ACPI Settings**
  System ACPI Parameters.
- **CPU Configuration**
  CPU Configuration Parameters.
- **Power & Performance**
  Power & Performance Options.
- **PCH-FW Configuration**
  Configure Management Engine Technology Parameters.
- **AMT Configuration**
  Configure Intel(R) Active Management Technology Parameters.
- **Intel(R) Time Coordinated Computing**
  Intel(R) Time Coordinated Computing (Intel(R) TCC) options.
- **Trusted Computing**
  Trusted Computing Settings.
- **ACPI Settings**
  ACPI Sleep State.
- **Embedded Controller**
  Embedded Controller Parameters.
- **Serial Port Console Redirection**
  Console Redirection Settings.

- **Intel TXT Information**
  Display Intel TXT information.
- **USB Configuration**
  USB Configuration Parameters.
- **Network Stack Configuration**
  Network Stack Settings.
- **NVMe Configuration**
  NVMe controller and Drive information.

3.4.1 RC ACPI Settings



Figure 3.4 RC ACPI Settings

- **Low Power S0 Idle Capability**
  This variable determines if we enable ACPI Lower Power S0 Idle Capability (Mutually exclusive with Smart connect). While this is enabled, it also disable 8254 timer for SLP_S0 support.

3.4.2 CPU Configuration

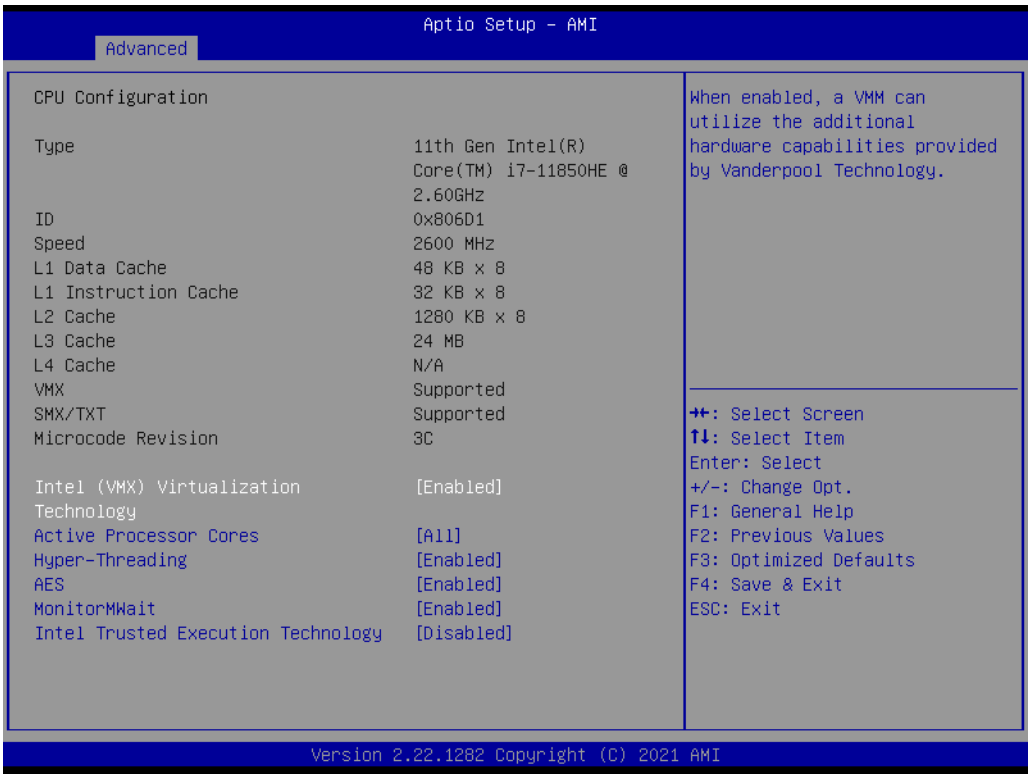


Figure 3.5 CPU Configuration

- **Intel (VMX) Virtualization Technology**
  When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
- **Active Processor Cores**
  Number of cores to enable in each processor package.
- **Hyper-Threading**
  Enable or Disable Hyper-Threading Technology.
- **AES**
  Enable/Disable AES (Advanced Encryption Standard).
- **MonitorMwait**
  Enable/Disable Monitor Mwait.
- **Intel Trusted Execution Technology**
  Enables utilization of additional hardware capabilities provided by Intel (R) Trusted Execution Technology.

3.4.3 Power & Performance

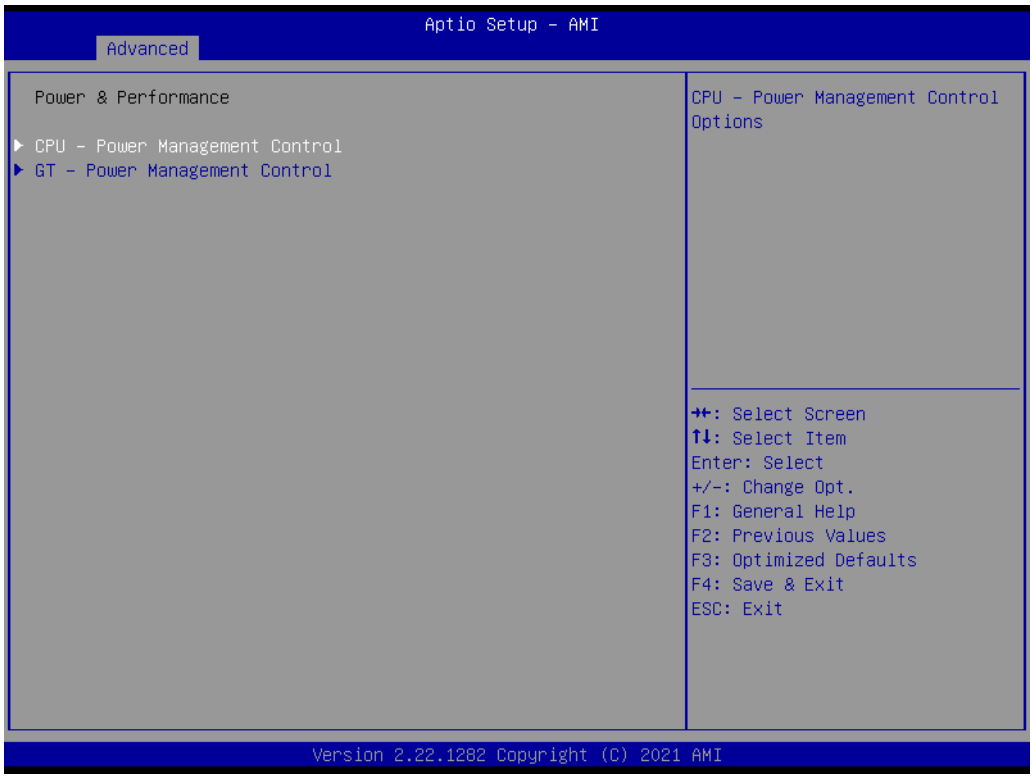


Figure 3.6 Power & Performance

- **CPU - Power Management Control**
  CPU - Power Management Control Options.
- **GT - Power Management Control**
  GT - Power Management Control Options.

3.4.3.1 CPU - Power Management Control

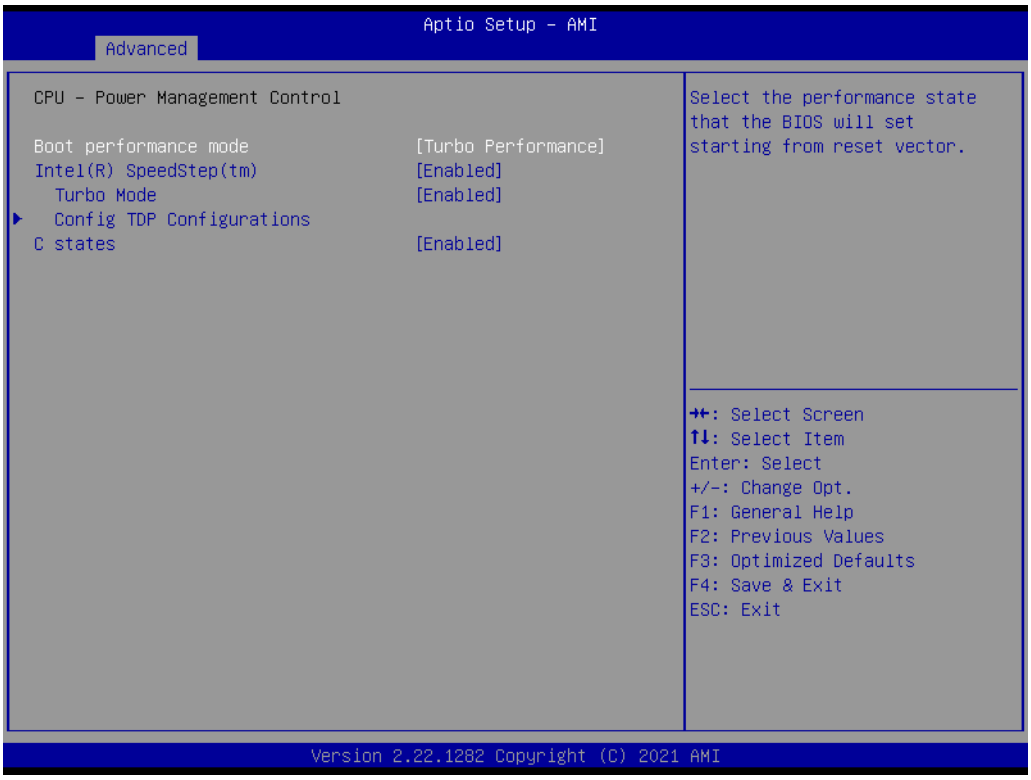


Figure 3.7 Power & Performance

- **Boot performance mode**
  Select the performance state that the BIOS will set starting from reset vector.
- **Intel(R) SpeedStep(tm)**
  Allows more than two frequency ranges to be supported.
- **Turbo Mode**
  Enable/Disable processor Turbo Mode (requires EMTTM enabled too). AUTO means enabled.
- **Config TDP Configurations**
  Config TDP Configurations.
- **C states**
  Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.

Config TDP Configurations

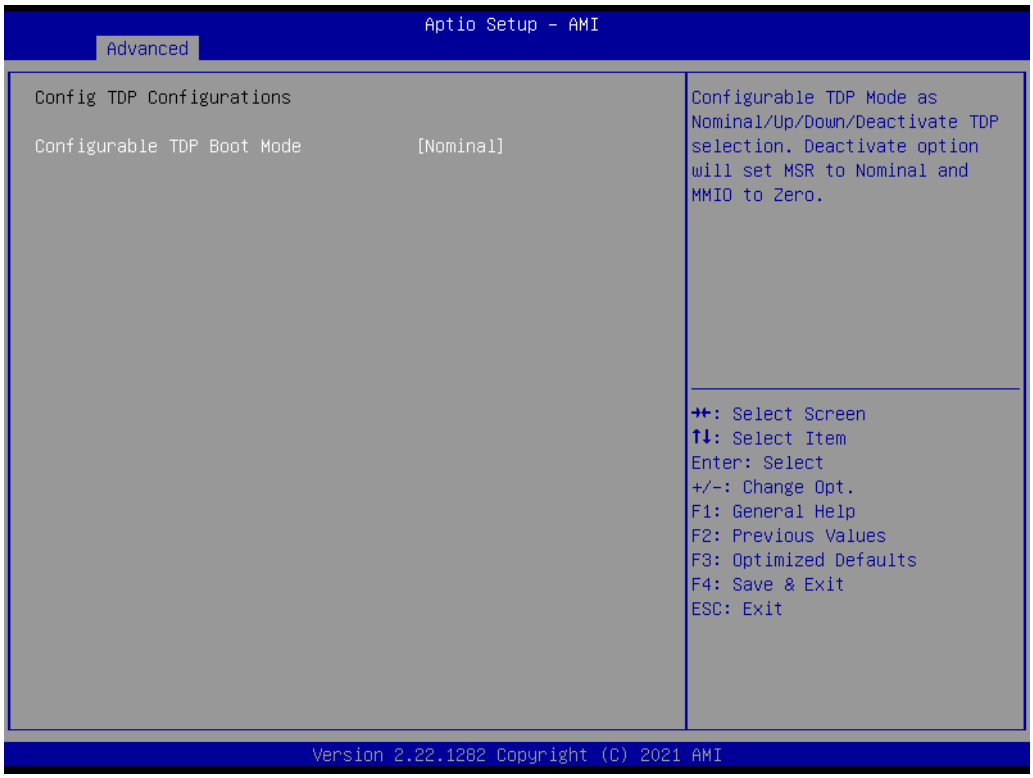


Figure 3.8 Config TDP Configurations

- **Configurable TDP Boot Mode**
  Configurable TDP Mode as Nominal/Up/Down/Deactivate TDP selection. Deactivate option will set MSR to Nominal and MMIO to Zero.

3.4.3.2 GT - Power Management Control

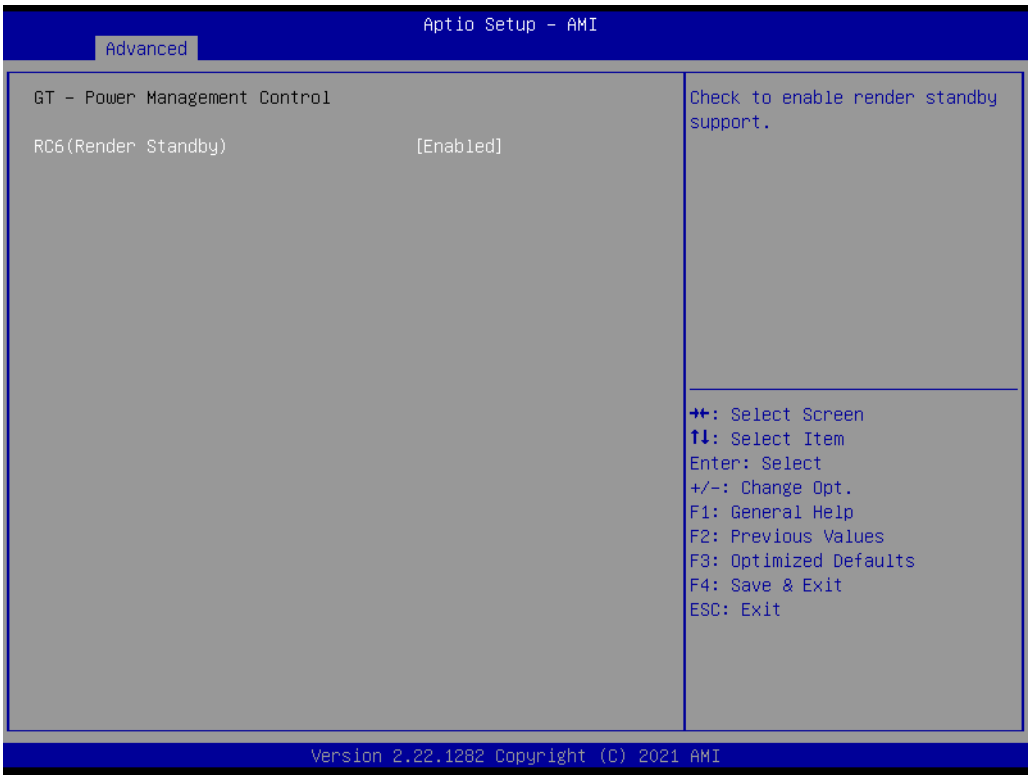


Figure 3.9 GT - Power Management Control

- **RC6 (Render Standby)**
  Check to enable render standby support.

3.4.4 PCH-FW Configuration

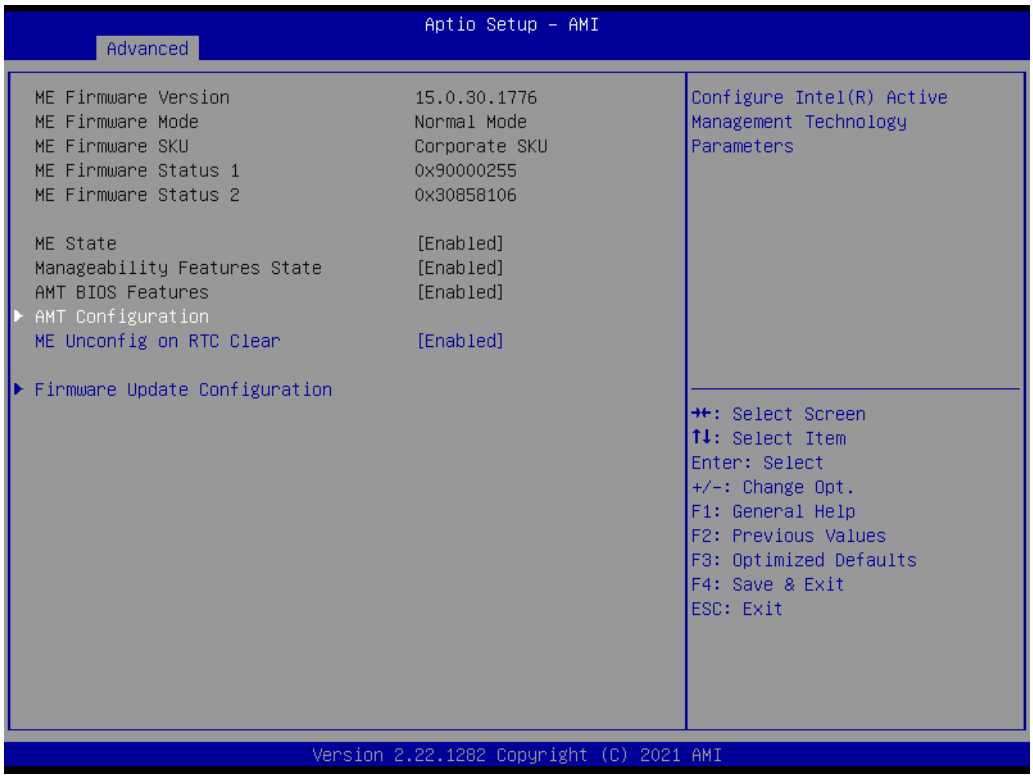


Figure 3.10 PCH-FW Configuration

- **AMT Configuration**
  Configure Intel(R) Active Management Technology Parameters.
- **ME Unconfig on RTC Clear**
  When Disabled ME will nor unconfigured on RTC clear.
- **Firmware Update Configuration**
  Configure Management Engine Technology Parameters.

3.4.4.1 AMT Configuration



Figure 3.11 AMT Configuration

- **OEM Flags Settings**
  Configure OEM Flags.

OEM Flags Settings

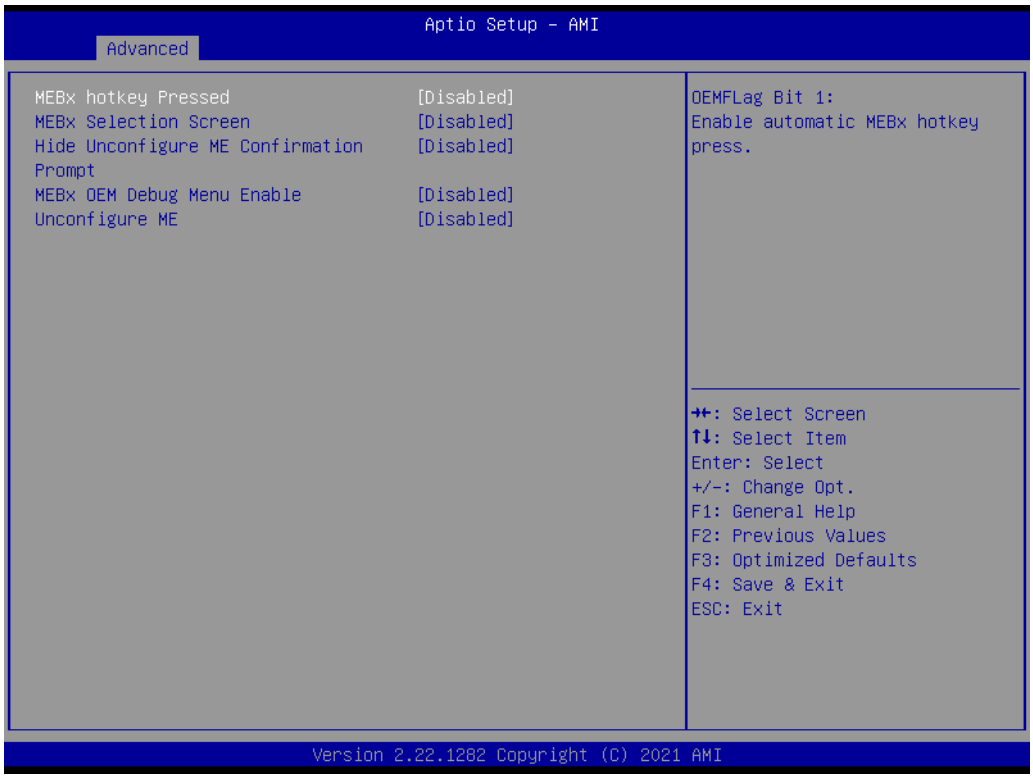


Figure 3.12 OEM Flags Settings

- **MEBx hotkey Pressed**
  OEMFLag Bit 1: Enable automatic MEBx hotkey press.
- **MEBx Selection Screen**
  OEMFLag Bit 2: Enable MEBx selection screen with 2 options:
  Press 1 to enter ME Configuration Screens.
  Press 2 to initiate a remote connection.
  Note: Network Access must be activated from MEBx Setup for this screen to be displayed.
- **Hide Unconfigure ME Confirmation Prompt**
  OEMFlag Bit 6: Hide Unconfigure ME confirmation prompt when attempting ME unconfiguration.
- **MEBx OEM Debug Menu Enable**
  OEMFlag Bit 14: Enable OEM debug menu in MEBx.
- **Unconfigure ME**
  OEMFlag Bit 15: Unconfigure ME with resetting MEBx password to default.

3.4.4.2 Firmware Update Configuration



Figure 3.13 Firmware Update Configuration

- **Me FW Image Re-Flash**
  Enable/Disable Me FW Image Re-Flash function.

3.4.5 Intel(R) Time Coordinated Computing

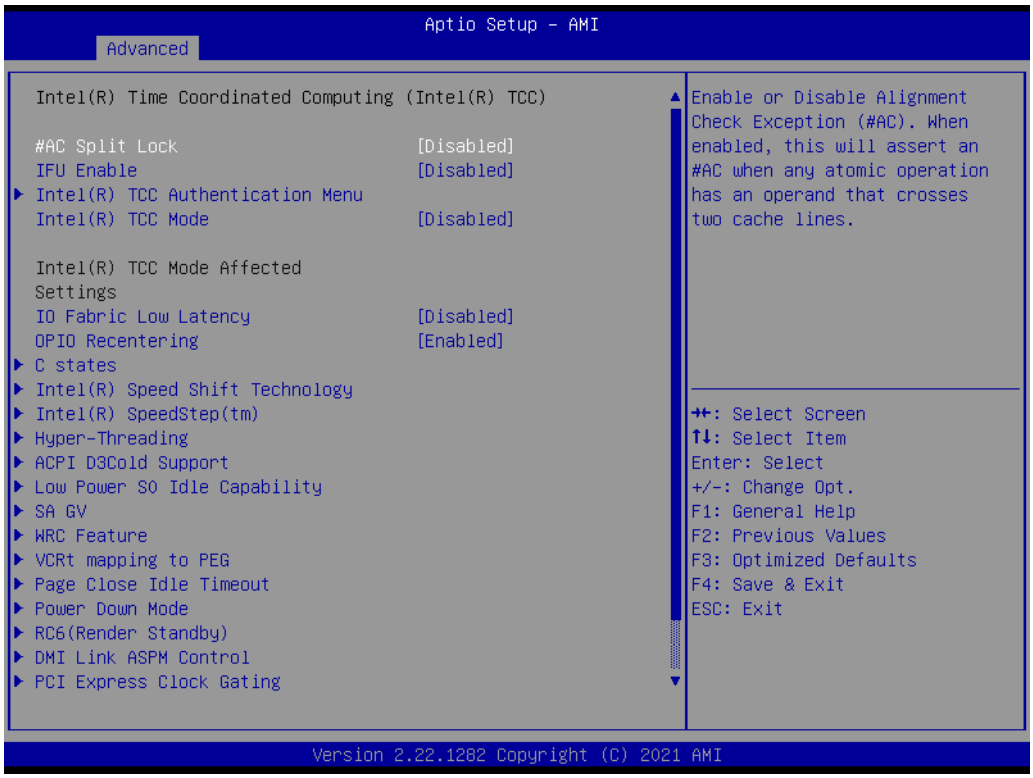


Figure 3.14 Intel(R) Time Coordinated Computing

- **#AC Split Lock**
  Enable or Disable Alignment Check Exception (#AC). When enabled, this will assert an #AC when any atomic operation has an operand that crosses two cache lines.
- **IFU Enable**
  Enable or Disable Instruction Fetch Unit(IFU). When enabled, Instructions will be prefetch to the cache.
- **Intel(R) TCC Authentication Menu**
  Intel(R) TCC Authentication Menu options.
- **Intel(R) TCC Mode**
  Enable or Disable Intel(R) TCC Mode. When enabled, this will modify system settings to improve real-time performance. The full list of settings and their current state are displayed below when Intel(R) TCC mode is enabled.
- **IO Fabric Low Latency**
  Enable or Disable IO Fabric Low Latency. This will turn off some power management in the PCH IO fabrics. This option provides the most aggressive IO Fabric performance setting. S3 state is NOT supported.
- **OPIO Recentering**
  Enable or Disable Opio Recentering to improve Pcie latency.
- **C states**
  Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.
- **Intel(R) Speed Shift Technology**
  Enable/Disable Intel(R) Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.

- **Intel(R) SpeedStep(tm)**
  Allows more than two frequency ranges to be supported.
- **Hyper-Threading**
  Enable or Disable Hyper-Threading Technology.
- **ACPI D3Cold Support**
  Enable/Disable ACPI D3Cold(RTD3) Support.
- **Low power SO Idle Capability**
  This variable determines if we enable ACPI lower power S0 idle capability (Mutually exclusive with smart connect) While this is enabled, it also disable 8254 timer for SLP_S0 support.
- **SA GV**
  System agent geyserville. Can disable, fix to a specific point, or enable frequency switching.
- **Page Close Idle Timeout**
  Page close idle timeout control.
- **Power Down Mode**
  CKE Power Down Mode Control.
- **RC6(Render standby)**
  Check to enable render standby support.
- **DMI Link ASPM Control**
  The control of Active State Power Management of the DMI Link.
- **PCI Express Clock Gating**
  PCI Express Clock Gating Enable/Disable for each root port.
- **Legacy IO Low Latency**
  Set to enable low latency of legacy IO. Some systems require lower IO latency irrespective of power. This is a tradeoff between power and IO latency.
- **CPU PCI Express Configuration**
- **PCH PCI Express Configuration**

3.4.6 Trusted Computing

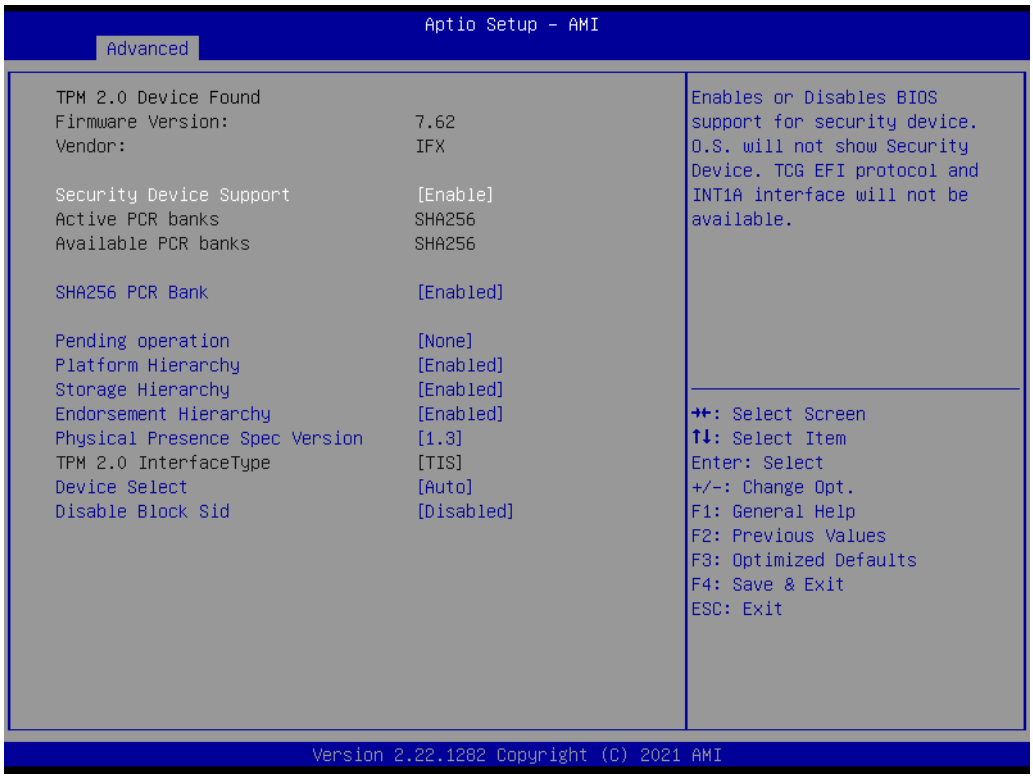


Figure 3.15 Trusted Computing

- **SHA256 PCR Bank**
  Enable or Disable SHA256 PCR Bank.
- **Pending operation**
  Pending operation.
- **Platform Hierarchy**
  Enable or Disable Platform Hierarchy.
- **Storage Hierarchy**
  Enable or Disable Storage Hierarchy.
- **Endorsement Hierarchy**
  Enable or Disable Endorsement Hierarchy.
- **Physical Presence Spec Version**
  Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.
- **Device Select**
  TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated.
- **Disable Block Sid**
  Override to allow SID authentication in TCG Storage device.

3.4.7 ACPI Settings

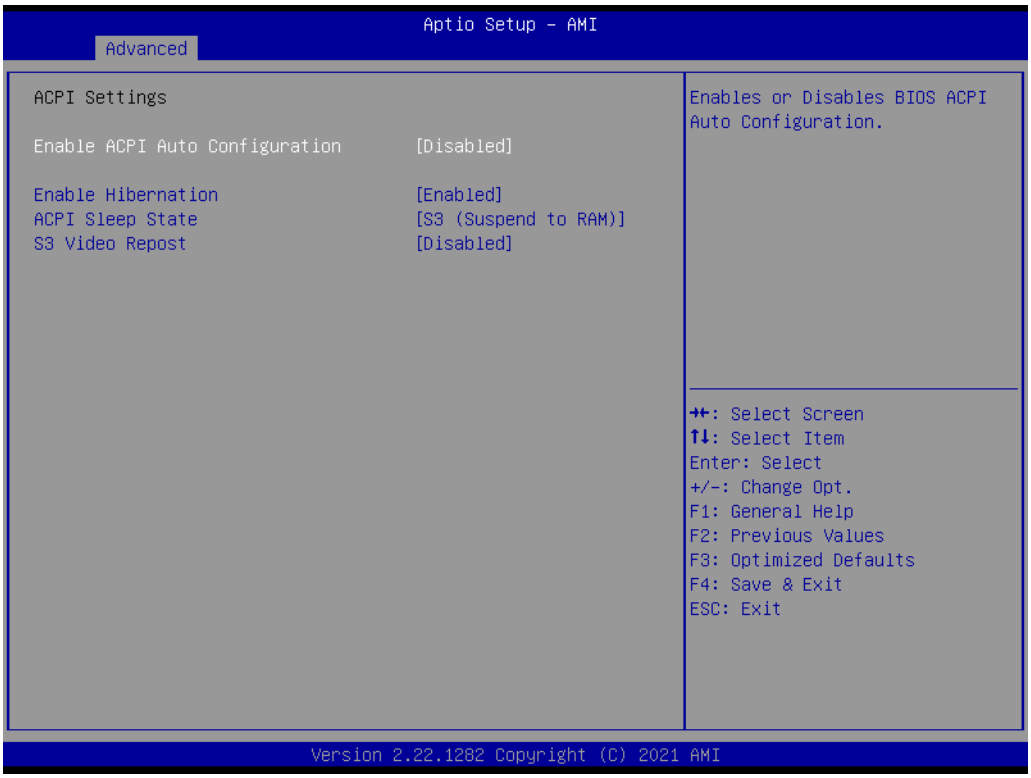


Figure 3.16 ACPI Settings

- **Enable ACPI Auto Configuration**
  Enables or Disables BIOS ACPI Auto Configuration.
- **Enable Hibernation**
  Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
- **ACPI Sleep State**
  Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.
- **S3 Video Repost**
  Enable or Disable S3 Video Repost.

3.4.8 Embedded Controller

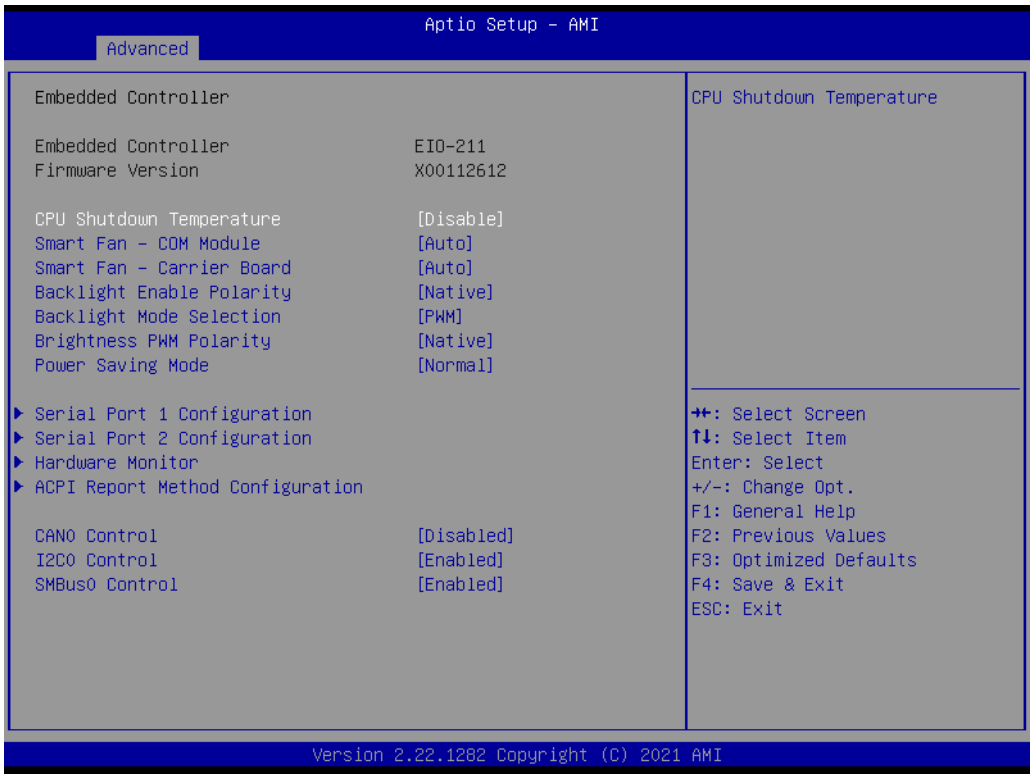


Figure 3.17 Embedded Controller

- **CPU Shutdown Temperature**
  CPU Shutdown Temperature.
- **Smart Fan - COM Module**
  Control COM Module Smart FAN function.
- **Smart Fan - Carrier Board**
  Control Carrier Board Smart FAN function. Get value from EC and only set value when Save Changes.
- **Backlight Enable Polarity**
  Switch Backlight Enable Polarity for Native or Invert.
- **Backlight Mode Selection**
  Switch Backlight Control to PWM or DC mode.
- **Brightness PWM Polarity**
  Backlight Control Brightness PWM Polarity for Native or Invert.
- **Power Saving Mode**
  Select Power Saving Mode.
- **Serial Port 1 Configuration**
  Set Parameters of Serial Port 1 (COMA).
- **Serial Port 2 Configuration**
  Set Parameters of Serial Port 2 (COMB).
- **Hardware Monitor**
  Monitor hardware status.
- **ACPI Report Method Configuration**
  Select ACPI Reporting Method for EC Devices.
- **CAN0 Control**
  Enable/Disable CAN0 controller on RDC-IS200.

- **I2C0 Control**
  Enable/Disable I2C0 controller on RDC-IS200.
- **SMBus0 Control**
  Enable/Disable SMBus0 controller on RDC-IS200.

3.4.8.1 Hardware Monitor

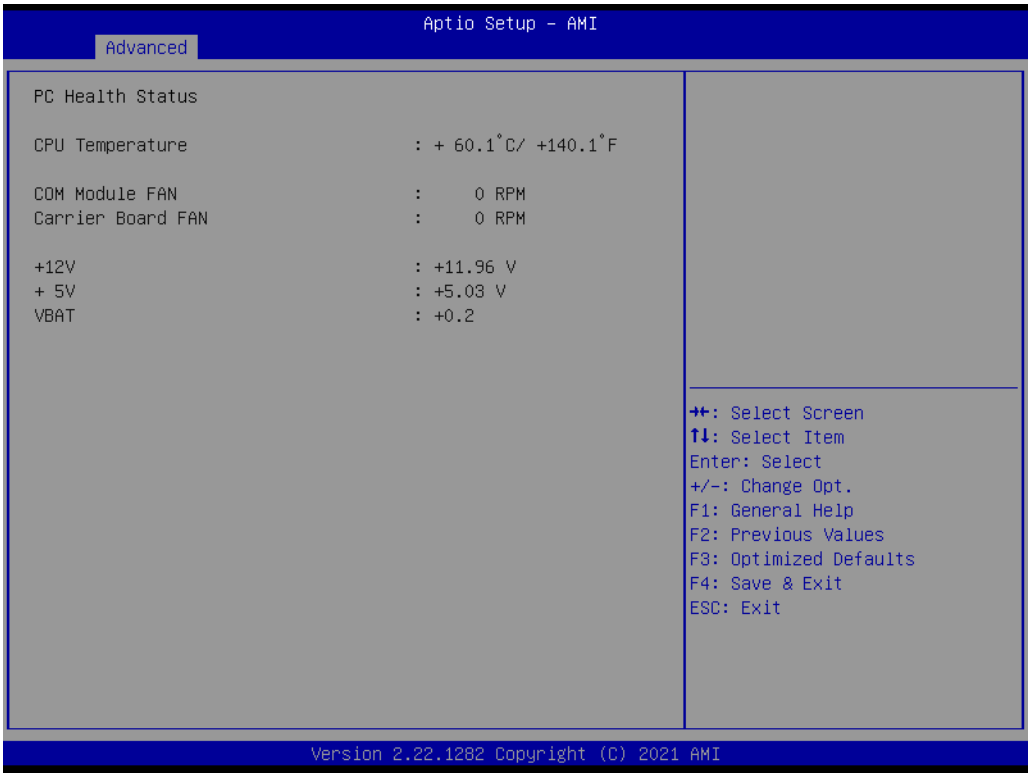


Figure 3.18 Hardware Monitor

3.4.8.2 Serial Port 1 Configuration

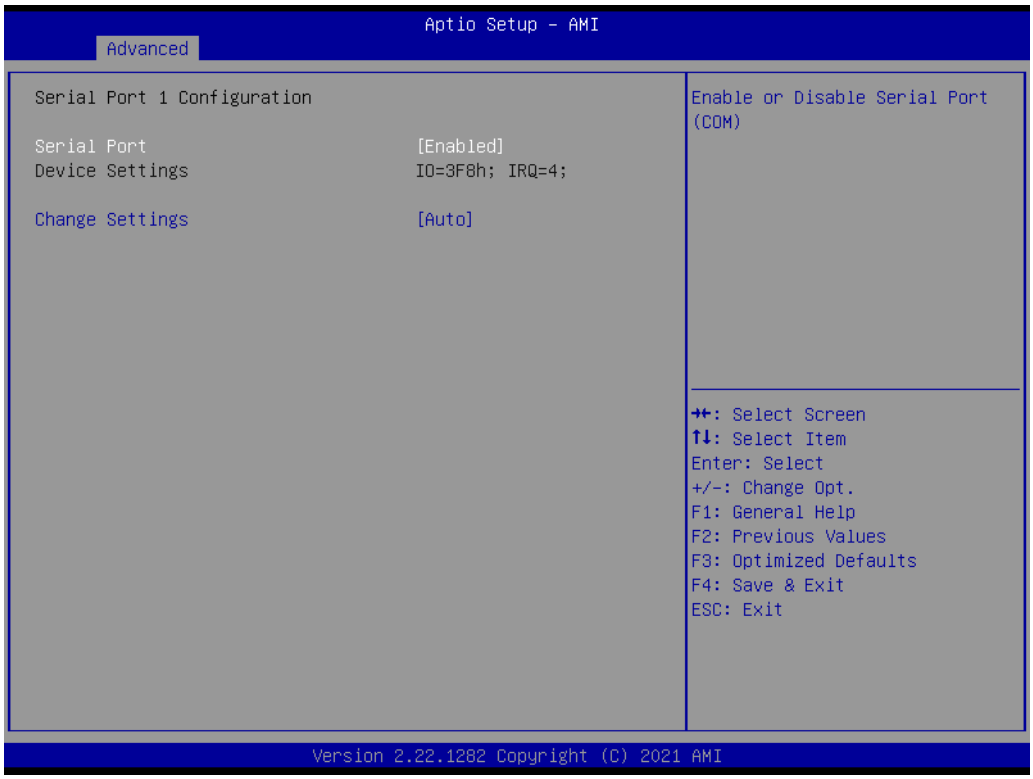


Figure 3.19 Serial Port 1 Configuration

- **Serial Port**
  Enable or Disable Serial Port (COM).
- **Change Settings**
  Select an optimal settings for Super IO Device.

3.4.8.3 ACPI Report Method Configuration

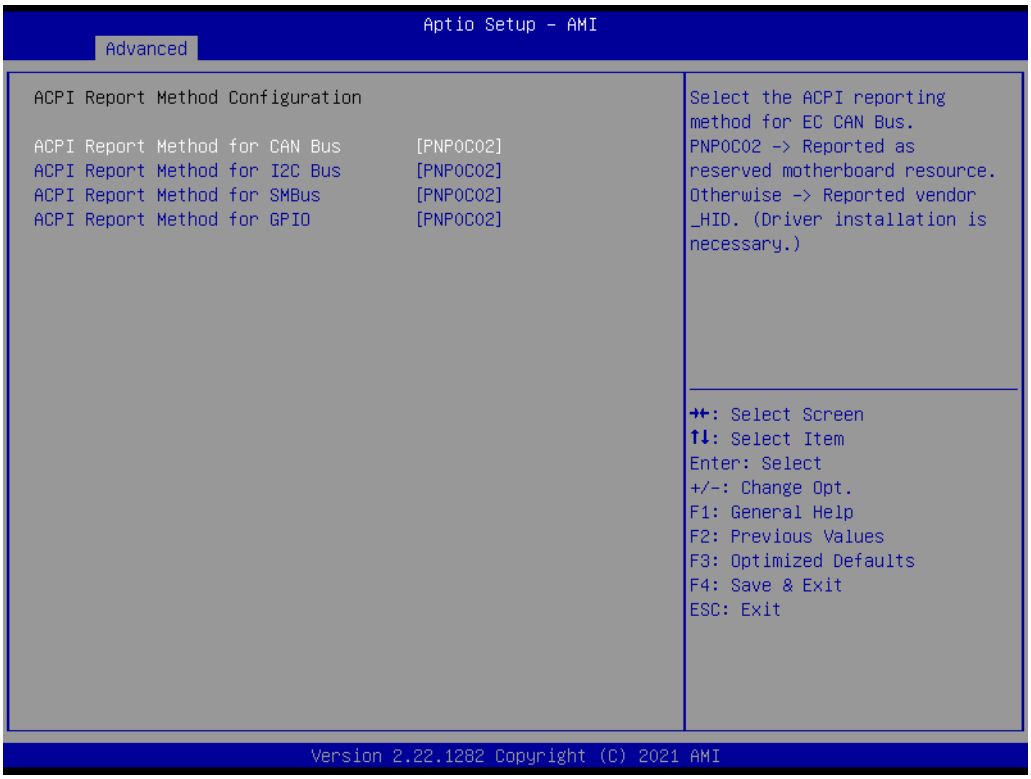


Figure 3.20 ACPI Report Method Configuration

- **CPI Report Method for CAN Bus**
  Select the ACPI reporting method for EC CAN Bus.
  PNP0C02 -> Reported as reserved motherboard resource.
  Otherwise -> Reported vendor _HID. (Driver installation is necessary.)
- **ACPI Report Method for I2C Bus**
  Select the ACPI reporting method for EC I2C Bus.
  PNP0C02 -> Reported as reserved motherboard resource.
  Otherwise -> Reported vendor _HID. (Driver installation is necessary.)
- **ACPI Report Method for SMBus**
  Select the ACPI reporting method for EC SMBus.
  PNP0C02 -> Reported as reserved motherboard resource.
  Otherwise -> Reported vendor _HID. (Driver installation is necessary.)
- **ACPI Report Method for GPIO**
  Select the ACPI reporting method for EC GPIO.
  PNP0C02 -> Reported as reserved motherboard resource.
  Otherwise -> Reported vendor _HID. (Driver installation is necessary.)

3.4.9 Serial Port Console Redirection

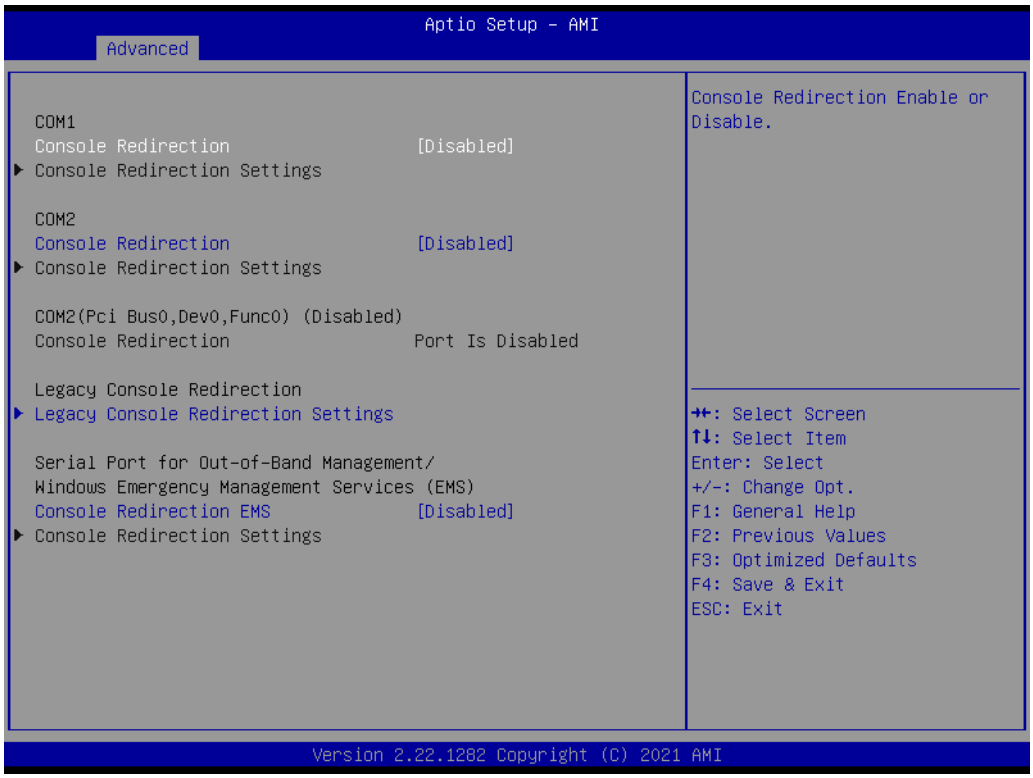


Figure 3.21 Serial Port Console Redirection

- **COM1 Console Redirection**
  Console Redirection Enable or Disable.
- **Console Redirection Settings**
  The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.
- **COM2 Console Redirection**
  Console Redirection Enable or Disable.
- **Console Redirection Settings**
  The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.
- **Legacy Console Redirection**
  Legacy Console Redirection Settings.
- **Console Redirection EMS**
  Console Redirection Enable or Disable.
- **Console Redirection Settings**
  The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

3.4.9.1 COM1 Console Redirection Settings

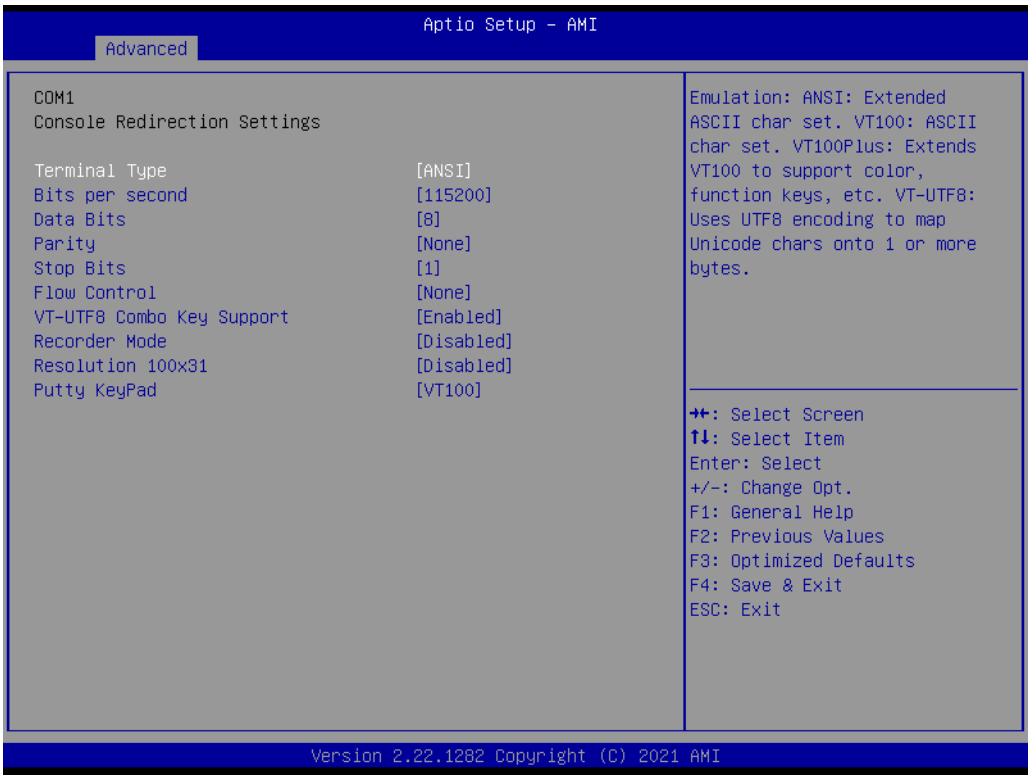


Figure 3.22 COM1 Console Redirection Settings

- **Terminal Type**
  Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100Plus: Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
- **Bits per second**
  Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
- **Data Bits**
  Data Bits.
- **Parity**
  A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
- **Stop Bits**
  Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
- **Flow Control**
  Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
- **VT-UTF8 Combo Key Support**
  Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.

- **Recorder Mode**
  With this mode enabled only text will be sent. This is to capture Terminal data.
- **Resolution 100x31**
  Enables or disables extended terminal resolution.
- **Putty KeyPad**
  Select FunctionKey and KeyPad on Putty.

3.4.9.2 Legacy Console Redirection Settings



Figure 3.23 Legacy Console Redirection Settings

- **Redirection COM Port**
  Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.
- **Resolution**
  On Legacy OS, the Number of Rows and Columns supported redirection.
- **Redirect After POST**
  When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

3.4.9.3 Console Redirection Settings

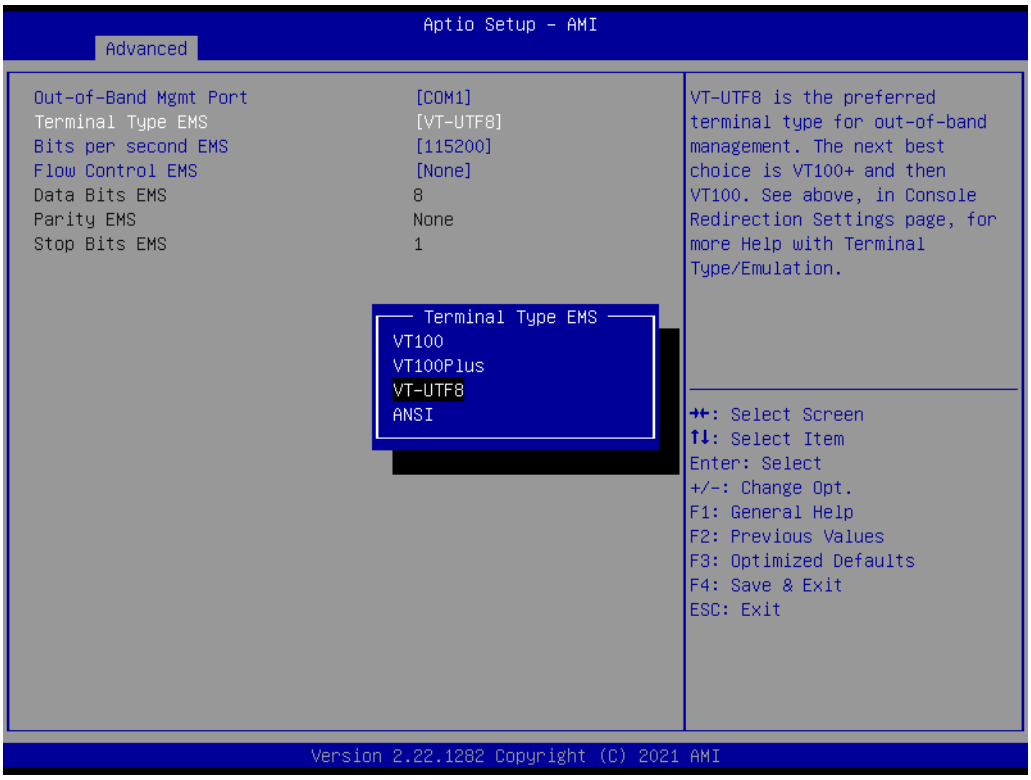


Figure 3.24 Console Redirection Settings

- **Out-of-Band Mgmt Port**
  Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.
- **Terminal Type**
  VT-UTF8 is the preferred terminal type for out-of-band management. The next best choice is VT100+ and then VT100. See above, in Console Redirection Settings page, for more Help with Terminal Type/Emulation.
- **Bits per second**
  Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
- **Flow Control EMS**
  Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
- **Data Bits EMS**
- **Parity**
  A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
- **Stop Bits EMS**
  Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

3.4.10 Intel TXT Information

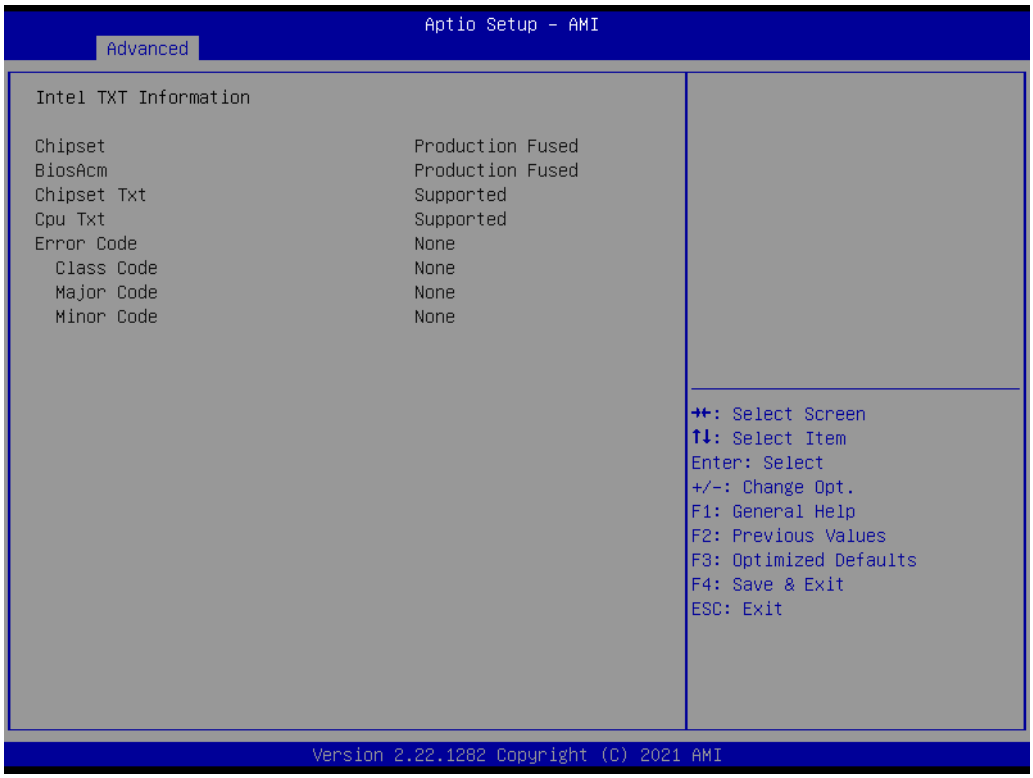


Figure 3.25 Intel TXT Information

3.4.11 USB Configuration

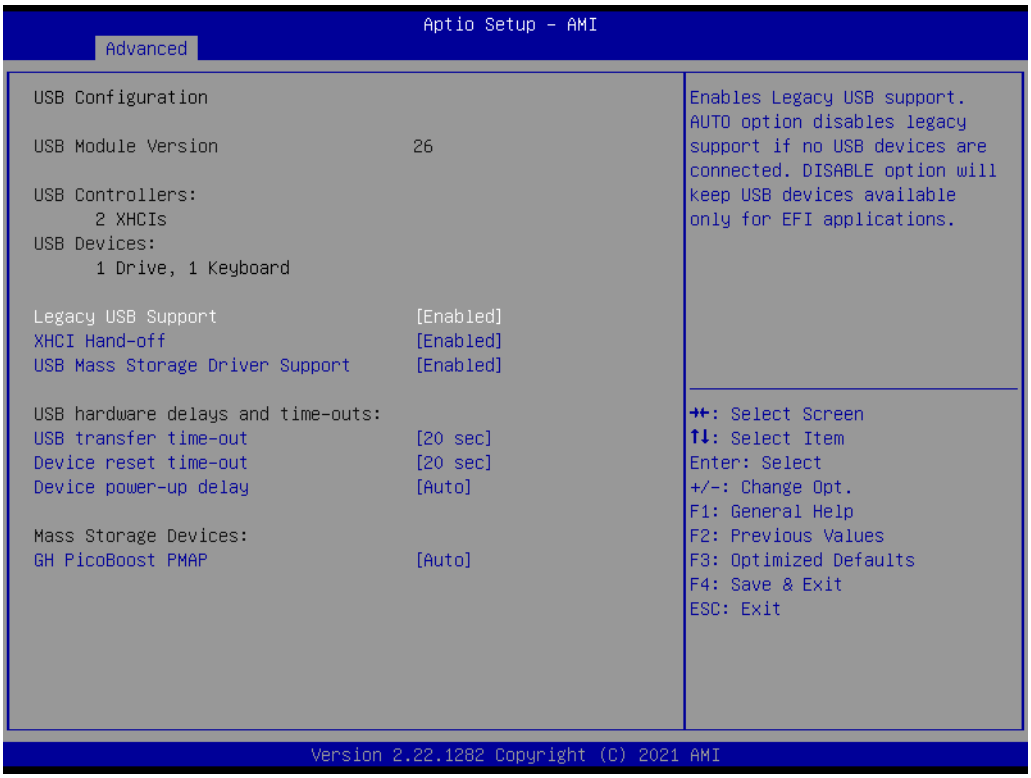


Figure 3.26 USB Configuration

- **Legacy USB Support**
  Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.
- **XHCI Hand-off**
  This is a workaround for OS without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
- **USB Mass Storage Driver Support**
  Enable/Disable USB Mass Storage Driver Support.
- **USB transfer time-out**
  The time-out value for Control, Bulk, and Interrupt transfers.
- **Device reset time-out**
  USB mass storage device Start Unit command time-out.
- **Device power-up delay**
  Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor.
- **GH PicoBoost PMAP**

3.4.12 Network Stack Configuration

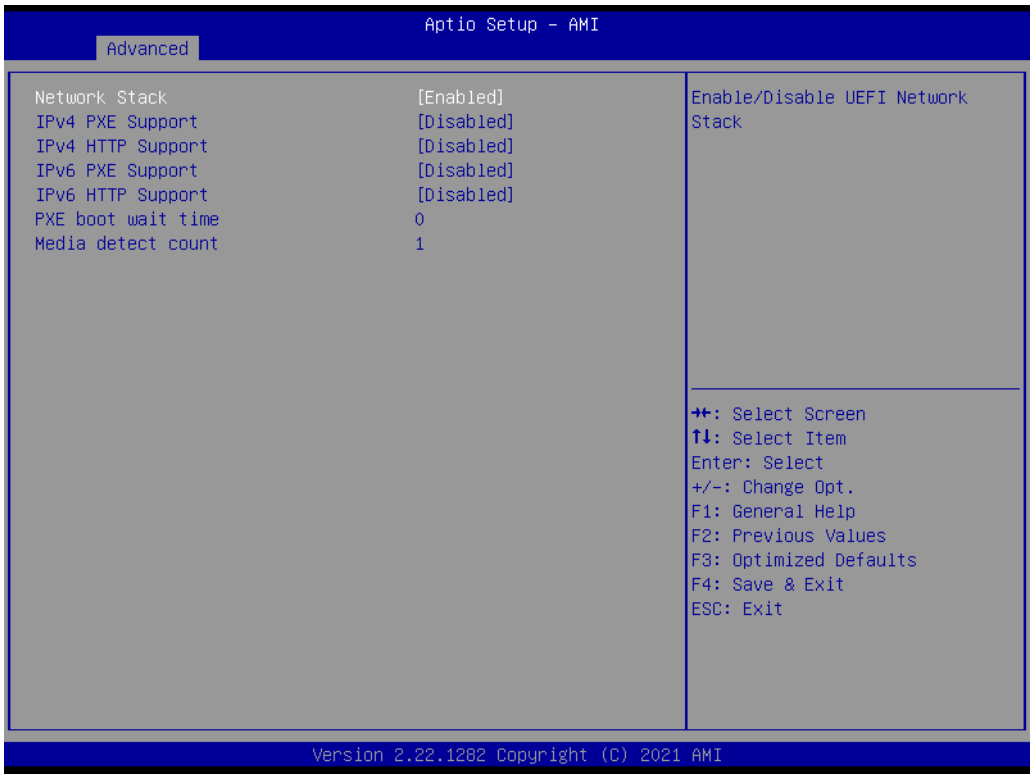


Figure 3.27 Network Stack Configuration

- **Network Stack**
  Enable/Disable UEFI Network Stack.
- **IPv4 PXE support**
  Enable/Disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.
- **IPv4 HTTP Support**
  Enable/Disable IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.
- **IPv6 PXE Support**
  Enable/Disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.
- **IPv6 HTTP Support**
  Enable/Disable IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.
- **PXE boot wait time**
  Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.
- **Media detect count**
  Number of times presence of media will be checked. Use either +/- or numeric keys to set the value.

3.4.13 NVME Configuration

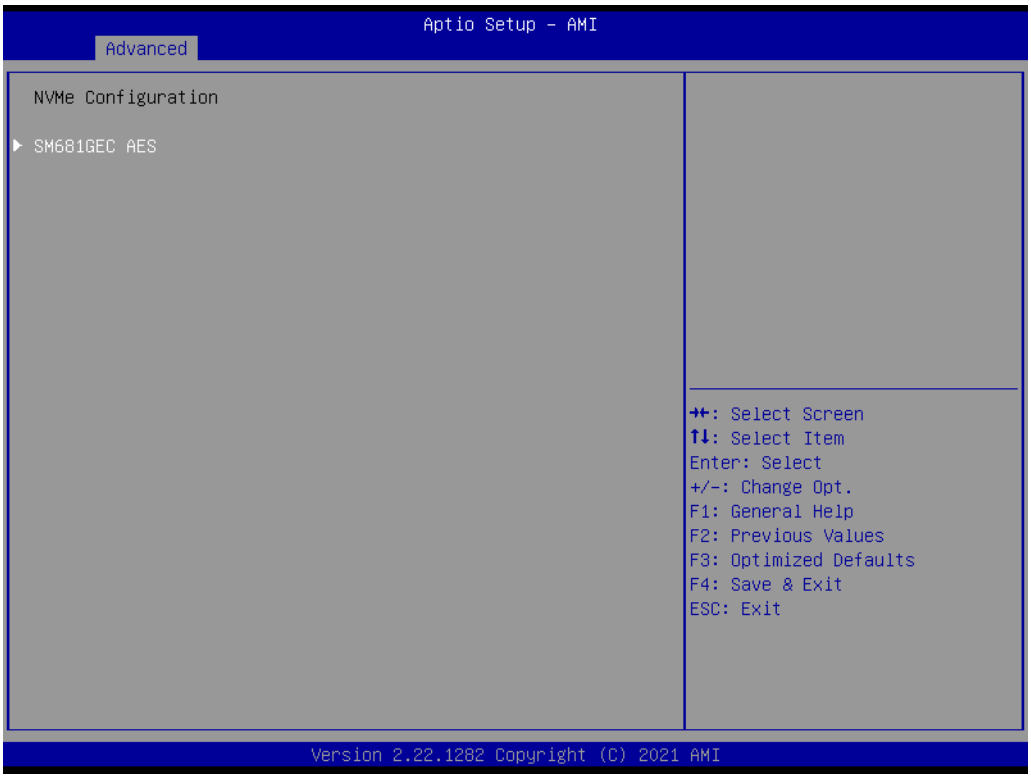


Figure 3.28 NVME Configuration

- **SM681GEC AES** (Note: This is optional component.)

3.4.13.1 SM681GEC AES

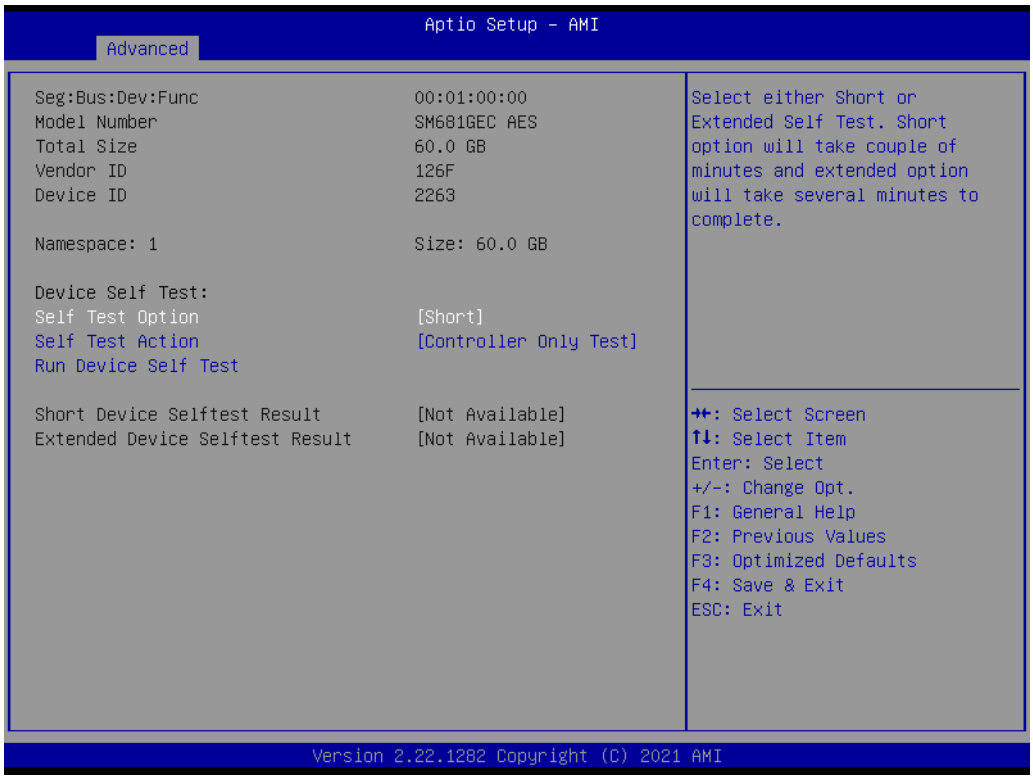


Figure 3.29 SM681GEC AES

- **Self Test Option**
  Select either Short or Extended Self Test. Short option will take couple of minutes and extended option will take several minutes to complete.
- **Self Test Action**
  Select either to test Contoller alone or Controller and NameSpace. Selecting Controller and Namespace option will take lot longer to complete the test.
- **Run Device Self Test**
  Perform device self test for the corresponding Option and Action selected by user.Pressing 'Esc' key will abort the test. Result shown below is the recent result logged in the device.

3.5 Chipset Setup



Figure 3.30 Chipset Setup

- **System Agent (SA)Configuration**
  System Agent Parameters.
- **PCH-I0 Configuration**
  PCH parameters.

3.5.1 System Agent (SA) Configuration

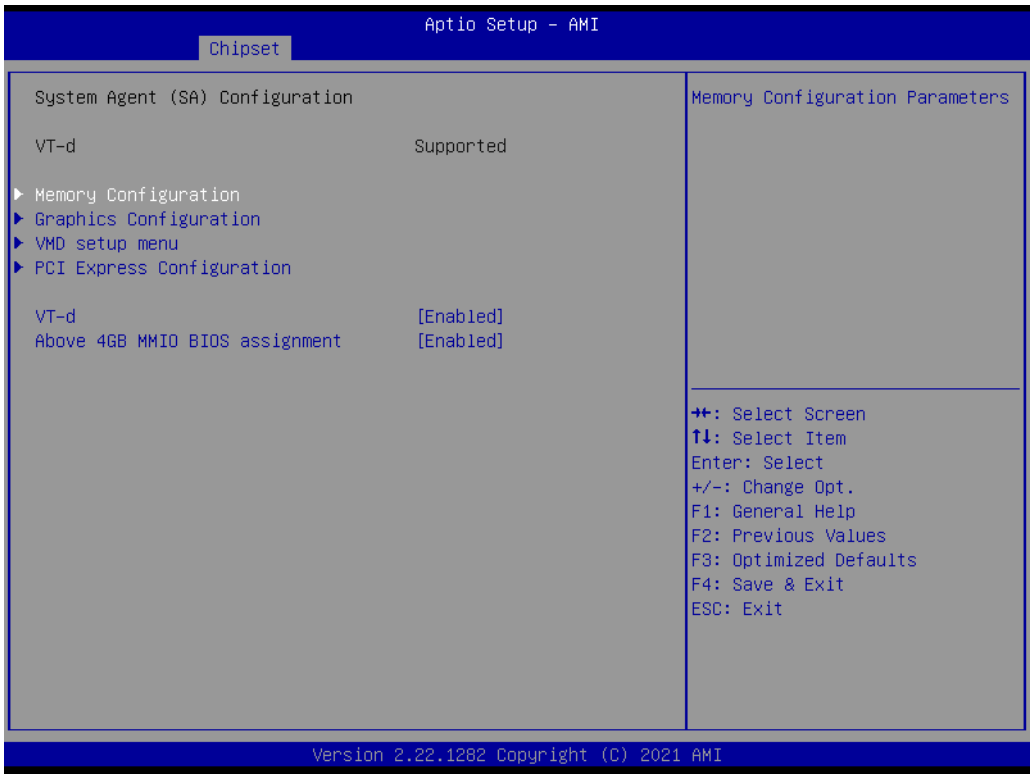


Figure 3.31 System Agent (SA) Configuration

- **Memory Configuration**
  Memory Configuration Parameters.
- **Graphic Configuration**
- **VMD setup menu**
  VMD Configuration.
- **VT-d**
  VT-d capability.
- **Above 4GB MMIO BIOS assignment**
  Enable/Disable above 4GB memory mapped IO BIOS assignment. This is enabled automatically when aperture size is set to 2048MB.

3.5.1.1 Memory Configuration

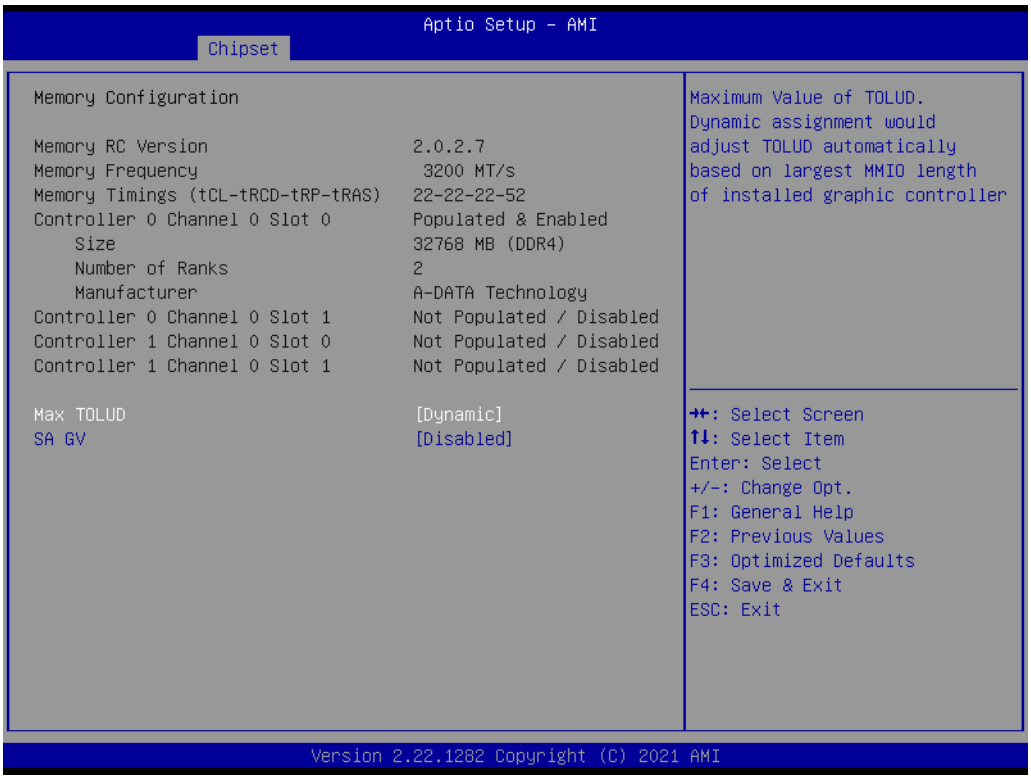


Figure 3.32 Memory Configuration

- **Max TOLUD**
  Maximum value of TOLUD. Dynamic assignment would adjust TOLUD automatically based on the largest MMIO length of installed graphic controller.
- **SA GV**
  System Agent Geyserville. Can disable, fix to a specific point, or enable frequency switching.

3.5.1.2 Graphics Configuration

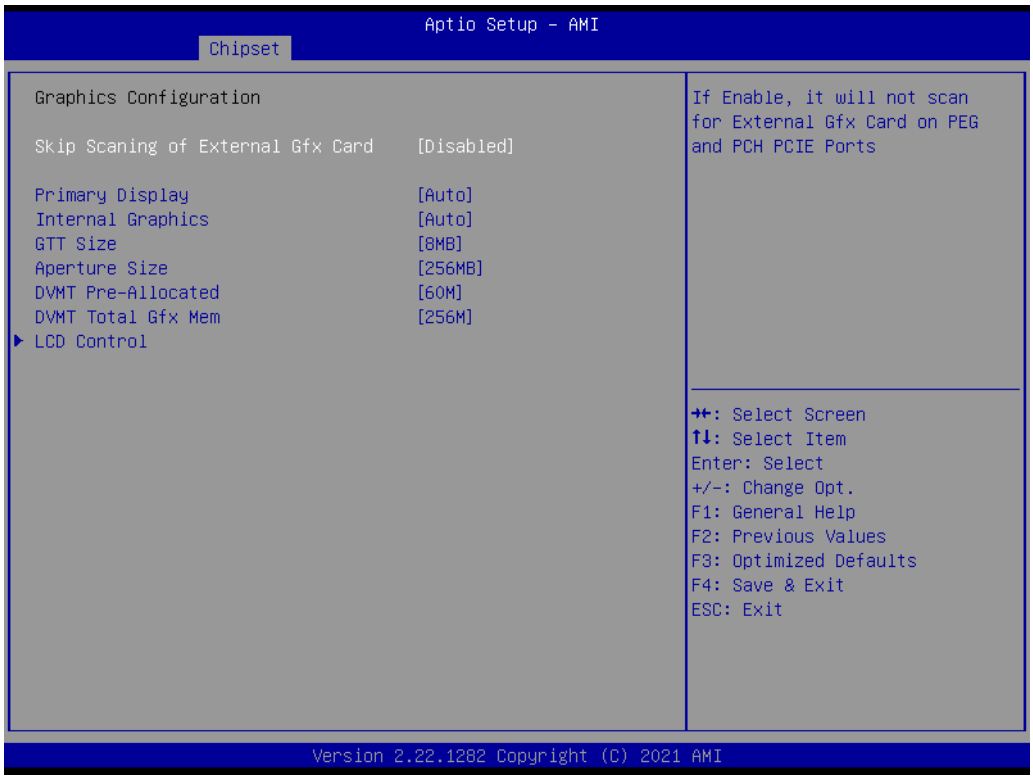


Figure 3.33 Graphics Configuration

- **Skip Scaning of External Gfx Card**
  If Enable, it will not scan for External Gfx Card on PEG and PCH PCIE Ports.
- **Primary Display**
  Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select HG for Hybrid Gfx.
- **Internal Graphics**
  Keep IGFX enabled base on the setup options.
- **GTT Size**
  Select the GTT size.
- **Aperture Size**
  Select the aperture size. Note: Above 4GB MMIO BIOS assignment is atomically enabled when selecting 2048MB aperture. To use this feature, please disable CSM support.
- **DVMT Pre-Allocated**
  Select DVMT5.0 pre-allocated(fixed) Graphics Memory size is used by the internal graphics device.
- **DVMT Total Gfx Mem**
  Select DVMT5.0 total graphic memory size is used by the internal graphic device.
- **LCD Control**

LCD Control



Figure 3.34 LCD Control

- **LCD Panel Type**
  Select LCD panel used by Internal Graphics Device by selecting the appropriate setup item.

3.5.1.3 VMD Setup Menu

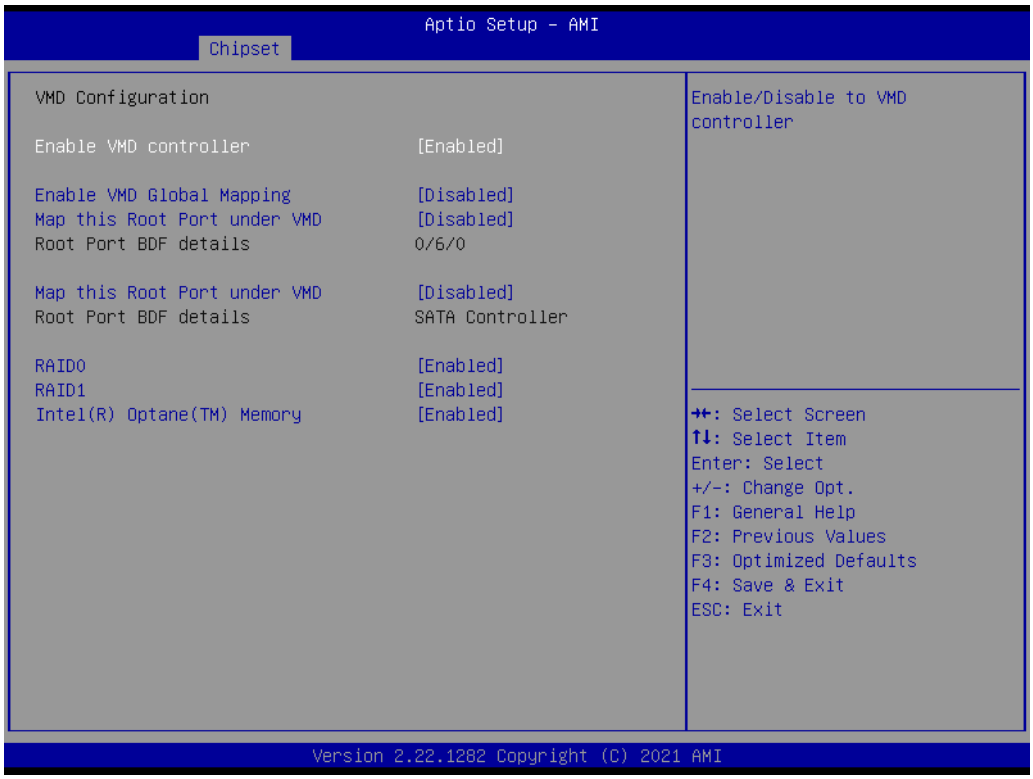


Figure 3.35 VMD Setup Menu

- **Enable VMD controller**
  Enable/Disable to VMD controller.
- **Enable VMD Global Mapping**
  Enable/Disable to VMD Global Mapping.
- **Map this Root Port under VMD**
- **RAID0**
  Enable/Disable RAID0 feature.
- **RAID1**
  Enable/Disable RAID1 feature.
- **Intel(R) Optane(TM) Memory**
  Enable/Disable System Acceleration with Intel(R) Optane(TM) Memory feature.

3.5.1.4 PCI Express Configuration

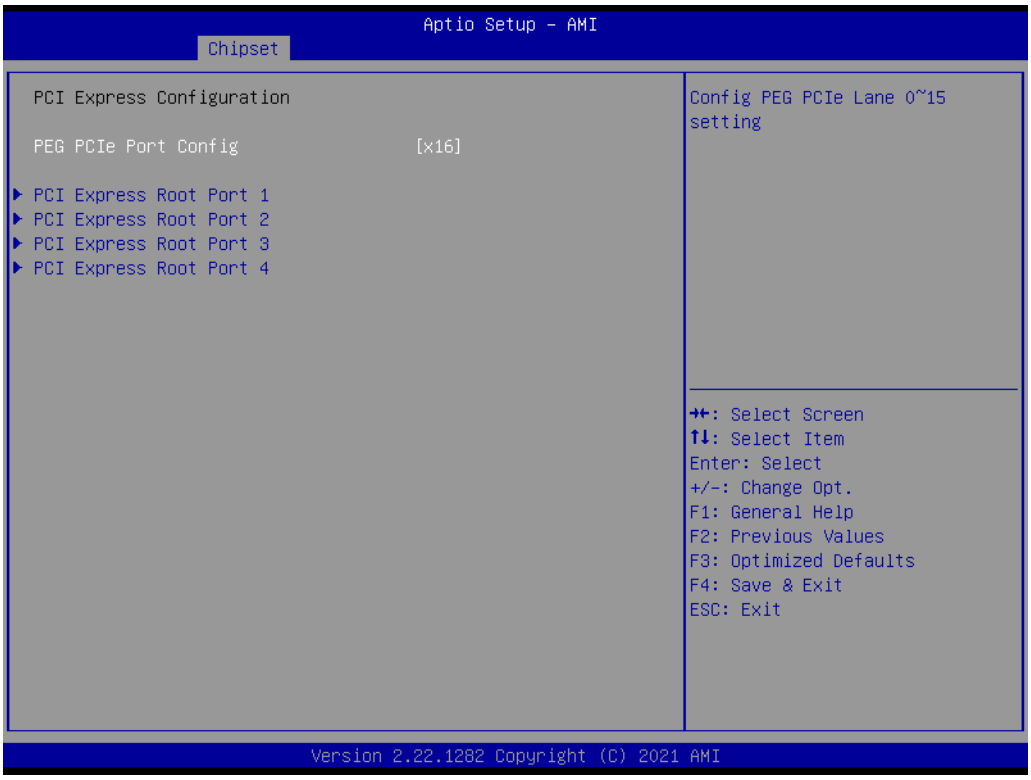


Figure 3.36 PCI Express Configuration

- **PEG PCIe Port Config**
  Config PEG PCIe Lane 0~15 setting.
- **PCI Express Root Port 1**
- **PCI Express Root Port 2**
- **PCI Express Root Port 3**
- **PCI Express Root Port 4**

PCI Express Root Port 1

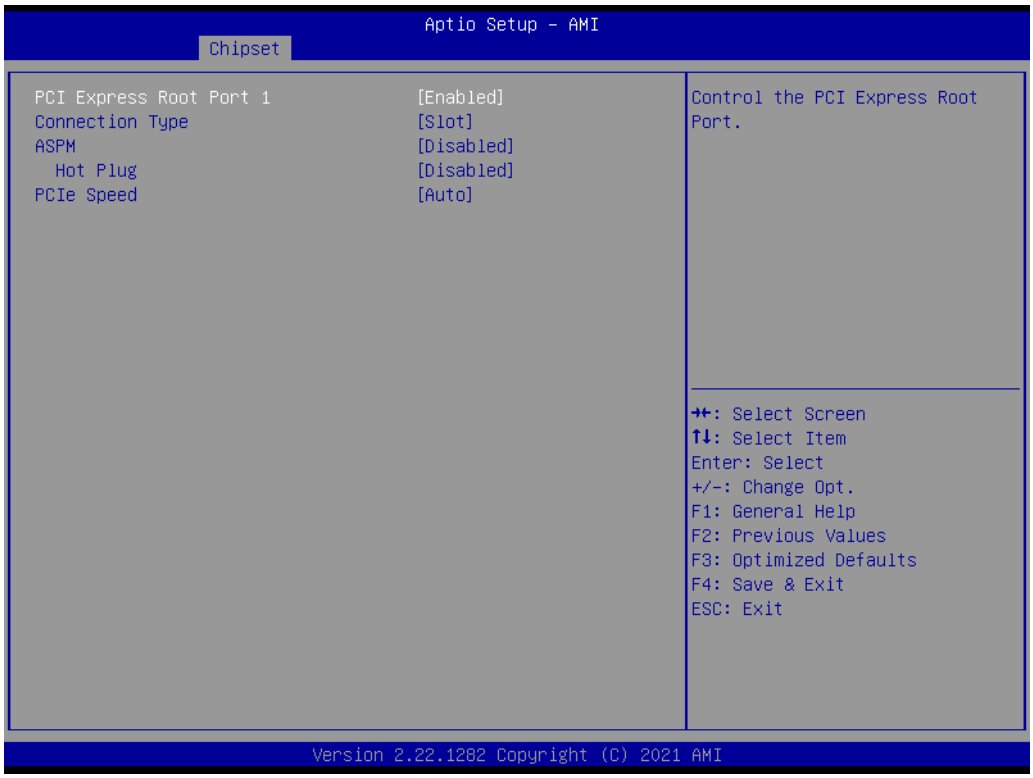


Figure 3.37 PCI Express Root Port 1

- **PCI Express Root Port 1**
  Control the PCI Express Root Port.
- **Connection Type**
  Built-In: a built-in device is connected to this root port. Slot Implemented bit will be clear. Slot: this root port connects to user-accessible slot. Slot Implemented bit will be set.
- **ASPM**
  PCI Express Active State Power Management settings.
- **Hot Plug**
  PCI Express Hot Plug Enable/Disable.
- **PCIe Speed**
  Configure PCIe Speed.

3.5.2 PCH-IO Configuration

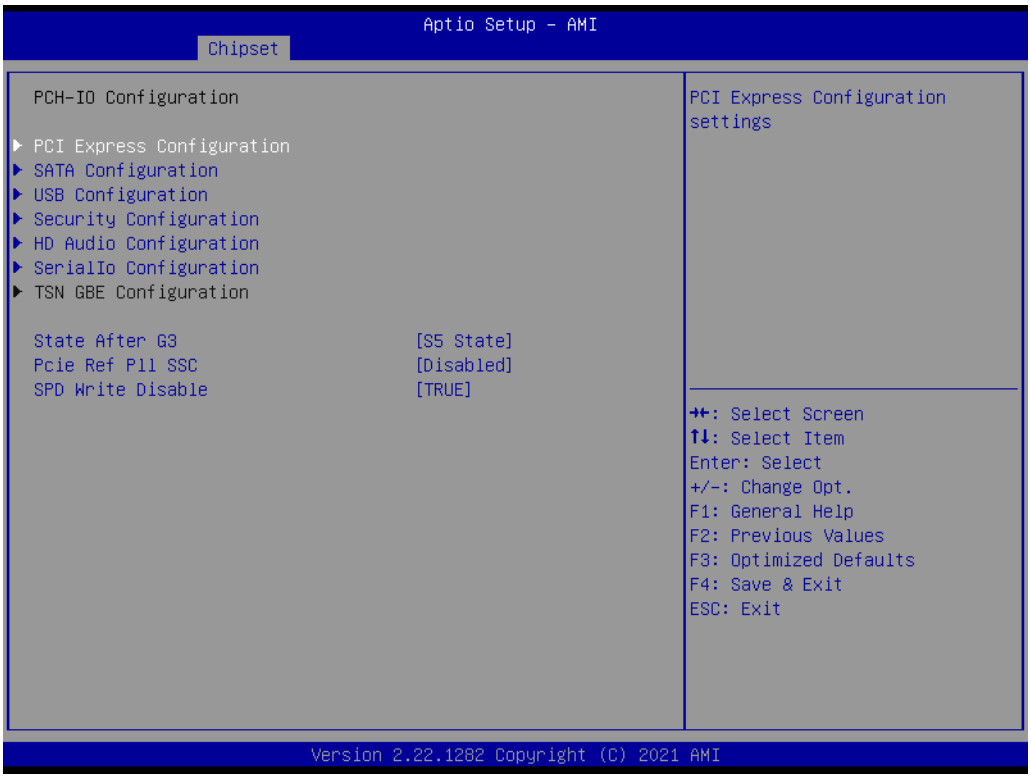


Figure 3.38 PCH-IO Configuration

- **PCI Express Configuration**
  PCI Express Configuration settings.
- **SATA Configuration**
  SATA device option settings.
- **USB Configuration**
  USB Configuration settings.
- **Security Configuration**
  Security Configuration settings.
- **HD Audio Configuration**
  HD audio subsystem configuration settings.
- **Seriallo Configuration**
  Seriallo configuration settings.
- **State After G3**
  Specify what state to go to when power is re-applied after a power failure (G3 state).
- **Pcie Ref Pll SSC**
  Pcie Ref Pll SSC Percentatge. AUTO - Keep hw default, no BIOS override. Range is 0.0%-0.5%.
- **SPD Write Disable**
  Enable/Disable setting SPD Write Disable. For security recommendations, SPD write disable bit must be set.

3.5.2.1 PCI Express Configuration

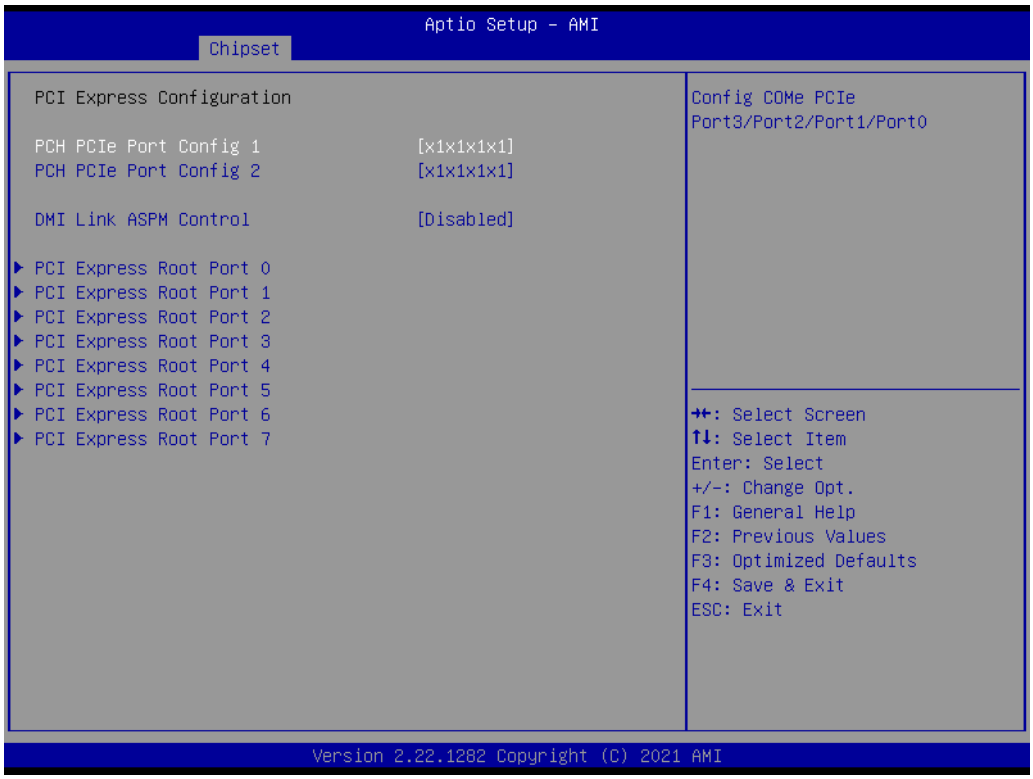


Figure 3.39 PCI Express Configuration

- **PCH PCIe Port Config 1**
  Config COMe PCIe Port3/Port2/Port1/Port0.
- **PCH PCIe Port Config 2**
  Config COMe PCIe Port7/Port6/Port5/Port4.
- **DMI Link ASPM Control**
  The control of Active State Power Management of the DMI Link.
- **PCI Express Root Port 0**
- **PCI Express Root Port 1**
- **PCI Express Root Port 2**
- **PCI Express Root Port 3**
- **PCI Express Root Port 4**
- **PCI Express Root Port 5**
- **PCI Express Root Port 6**
- **PCI Express Root Port 7**

PCI Express Root Port 0

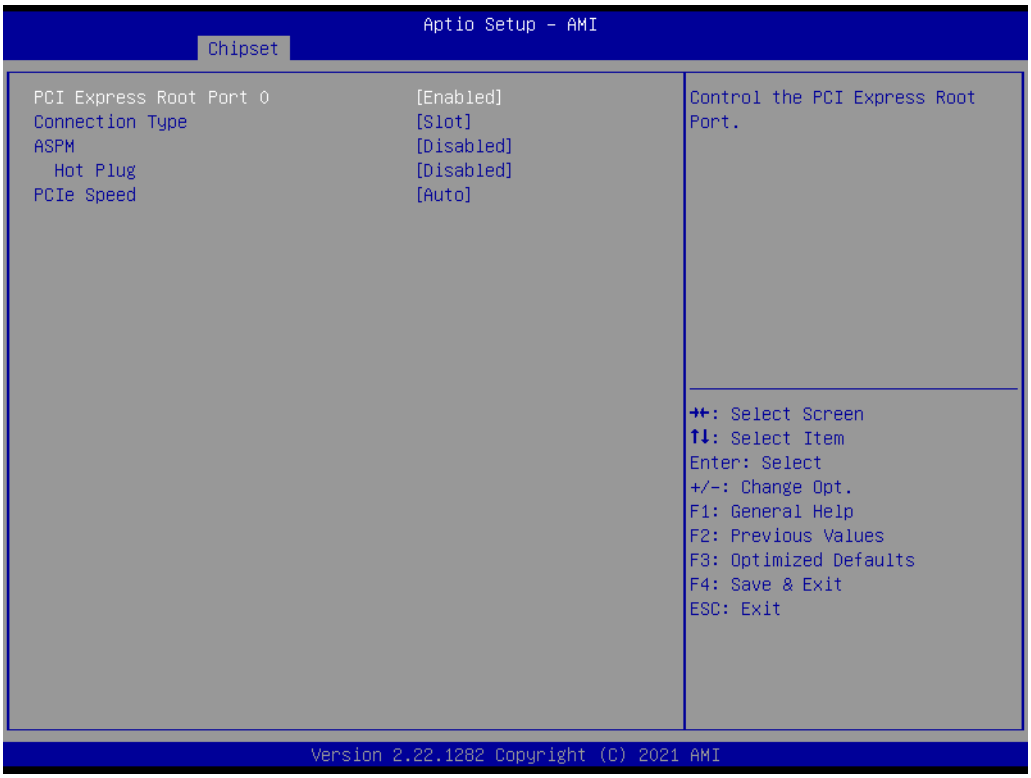


Figure 3.40 PCI Express Root Port 0

- **PCI Express Root Port 0**
  Control the PCI Express Root Port.
- **Connection Type**
  Built-In: a built-in device is connected to this root port. Slot Implemented bit will be clear. Slot: this root port connects to user-accessible slot. Slot Implemented bit will be set.
- **ASPM**
  PCI Express Active State Power Management settings.
- **Hot plug**
  PCI Express hot plug enable/disable.
- **PCIe Speed**
  Configure PCIe Speed.

3.5.2.2 SATA Configuration

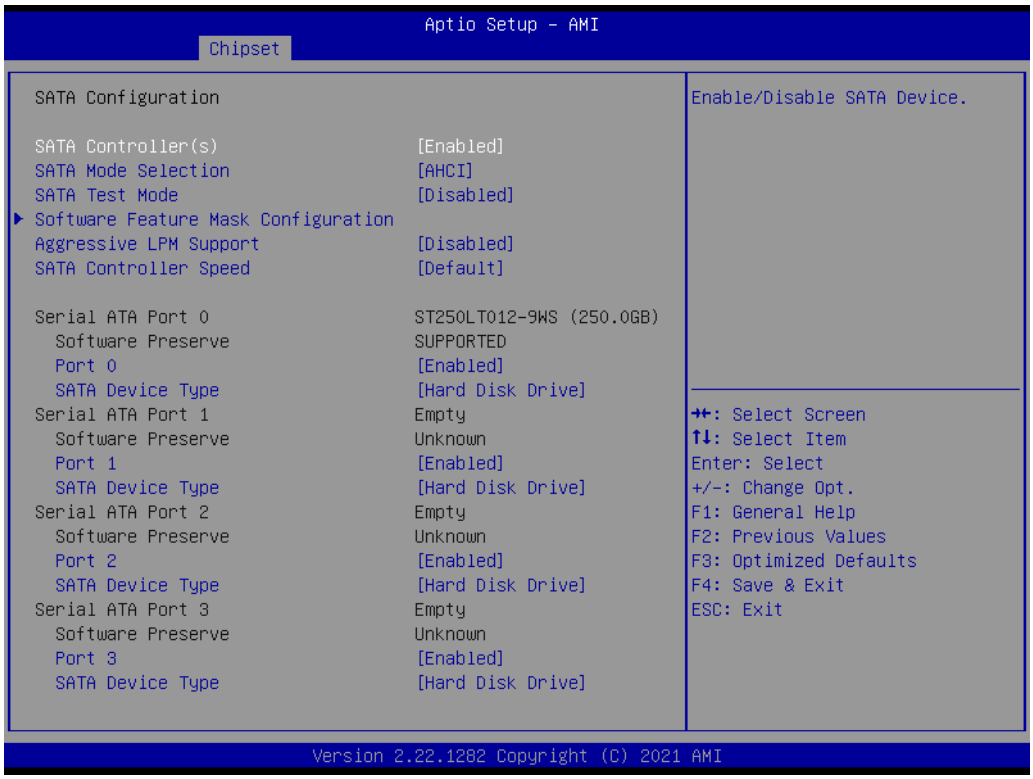


Figure 3.41 SATA Drives

- **SATA Controller(s)**
  Enable/Disable SATA Device.
- **SATA Mode Selection**
  Determines how SATA controller(s) operate.
- **SATA Test Mode**
  Test Mode Enable/Disable (Loop Back).
- **Software Feature Mask Configuration**
  RST Legacy OROM/RST UEFI driver will refer to the SWFM configuration to enable/disable the storage features.
- **Aggressive LPM Support**
  Enable PCH to aggressively enter link power state.
- **SATA Controller Speed**
  Indicates the maximum speed the SATA controller can support.
- **Port 0**
  Enable or Disable SATA Port.
- **SATA Device Type**
  Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.

3.5.2.3 USB Configuration

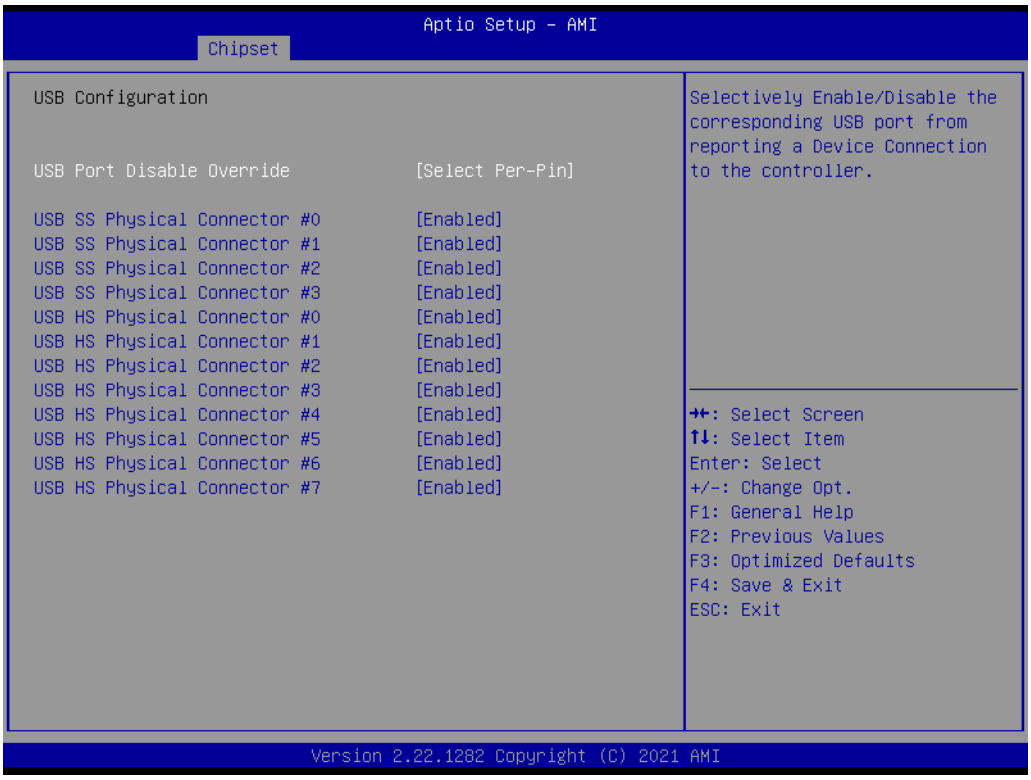


Figure 3.42 USB Configuration

- **USB Port Disable Override**
  Selectively Enable/Disable the corresponding USB port from reporting a Device Connection to the controller.
- **USB SS Physical Connector #0**
  Enable/Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS.

3.5.2.4 Security Configuration

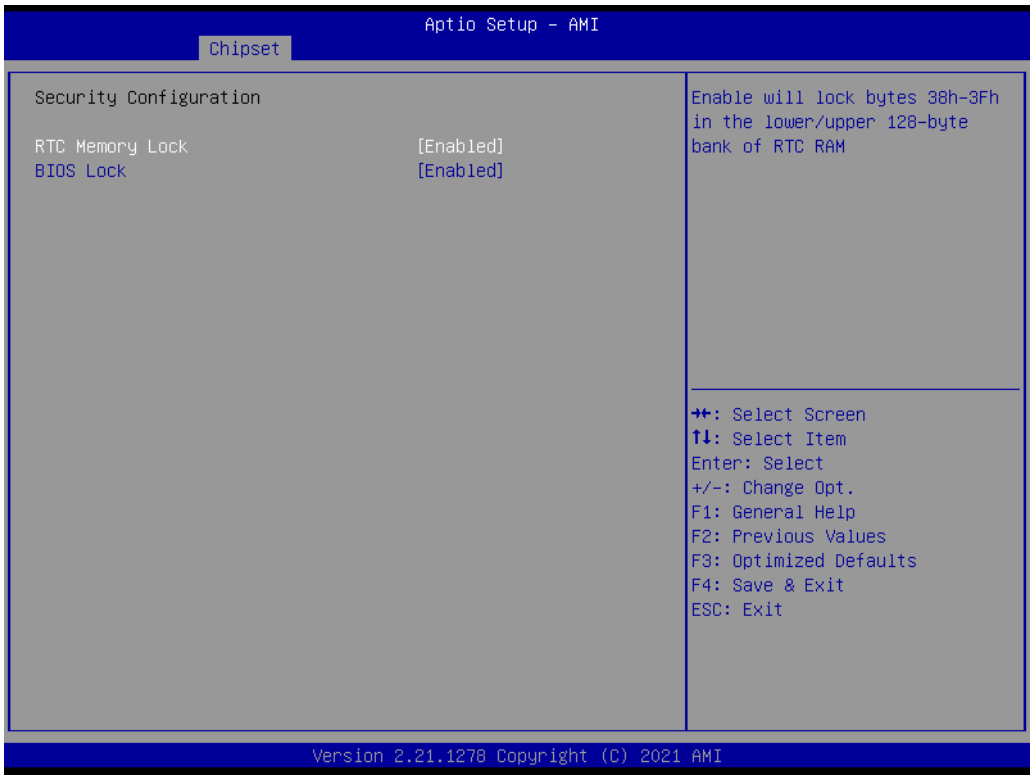


Figure 3.43 Security Configuration

- **RTC Memory Lock**
  Enable will lock bytes 38h-3Fh in the lower/upper 126 –byte bank of RTC RAM.
- **BIOS Lock**
  Enable/Disable the PCH BIOS lock enable feature. Required to be enabled to ensure SMM protection of flash.

3.5.2.5 HD Audio Subsystem Configuration Settings

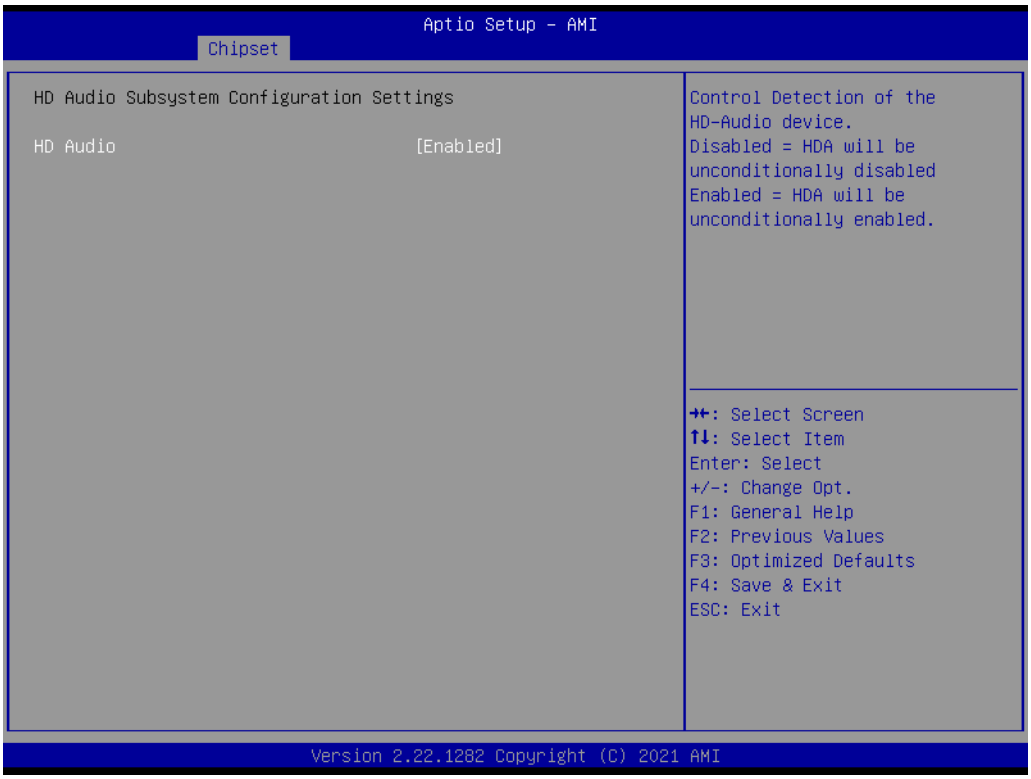


Figure 3.44 HD Audio Subsystem Configuration Settings

- **HD Audio**
  Control Detection of the HD-Audio device. Disabled=HDA will be unconditionally disabled. Enabled=HDA will be unconditionally enabled.

3.5.2.6 Seriallo Configuration

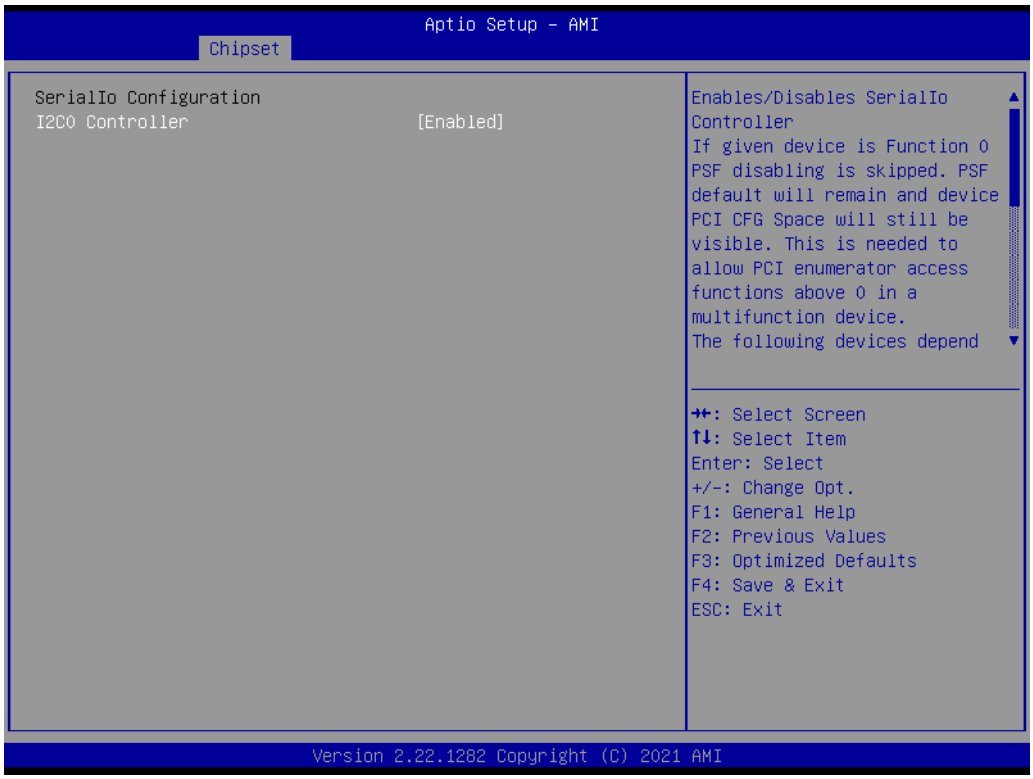


Figure 3.45 Seriallo Configuration

- **I2C0 Controller**
  Enables/Disables Seriallo Controller.
  If given device is Function 0 PSF disabling is skipped. PSF default will remain and device PCI CFG Space will still be visible. This is needed to allow PCI enumerator access functions above 0 in a multifunction device.
  The following devices depend on each other:
  I2C0 and I2C1,2,3
  UART0 and UART1,SPI0,1
  UART2 and I2C4,5

  UART 0 (00:30:00) cannot be disabled when:
  1. Child device is enabled like CNVi Bluetooth (_SB.PC00.UA00.BTH0)
  UART 0 (00:30:00) cannot be enabled when:
  1. I2S Audio codec is enabled (_SB.PC00.I2C0.HDAC)

3.6 Security Chipset

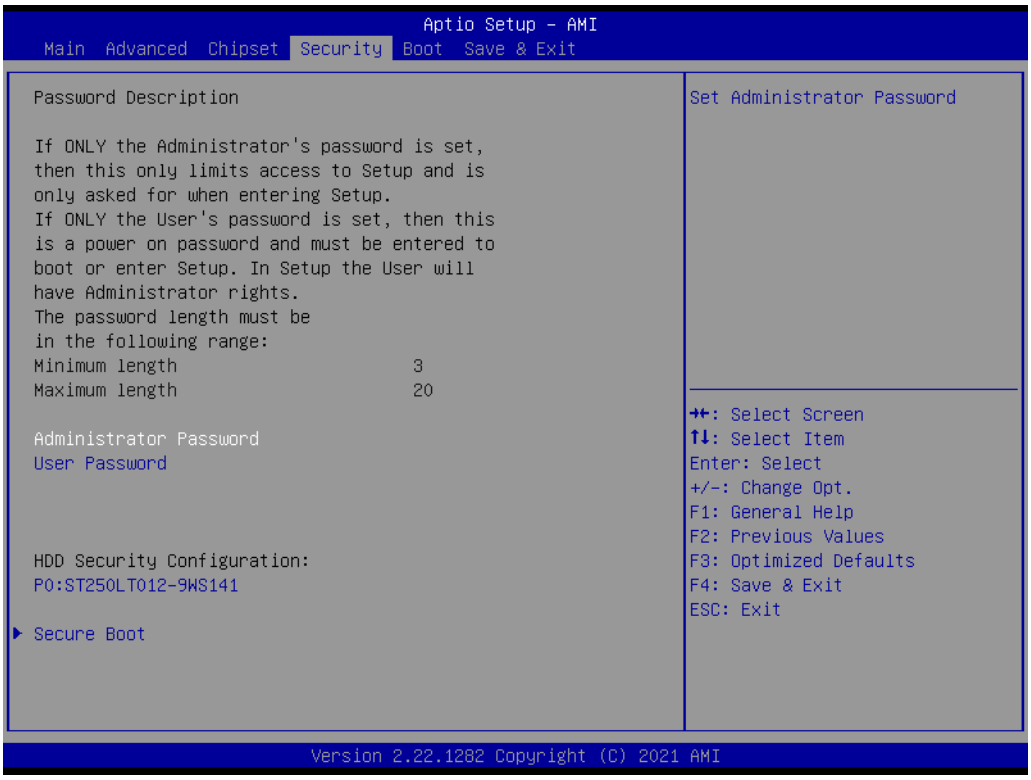


Figure 3.46 Security Chipset

- **Administrator Password**
  Set Setup Administrator Password.
- **User Password**
  Set User Password.
- **Secure Boot**
  Secure Boot Configuration.

3.6.1 Secure Boot

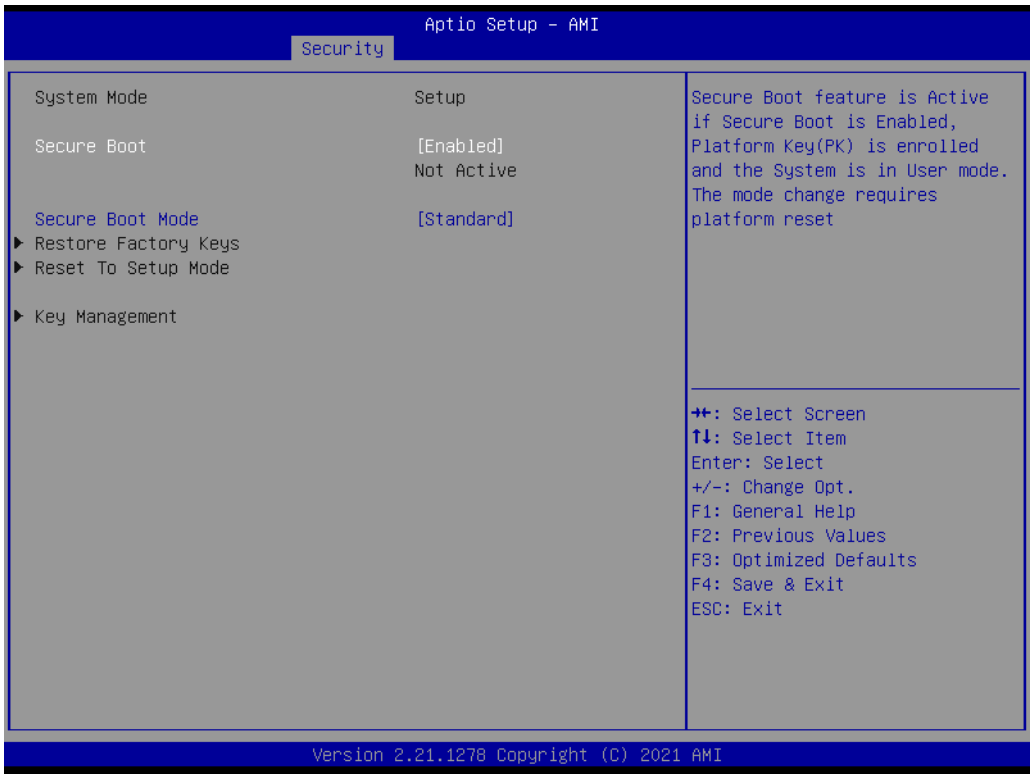


Figure 3.47 Secure Boot

- **Secure Boot**
  Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset.
- **Secure Boot Mode**
  Secure Boot mode options:
  Standard or Custom.
  In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.

3.6.2 Boot Setup

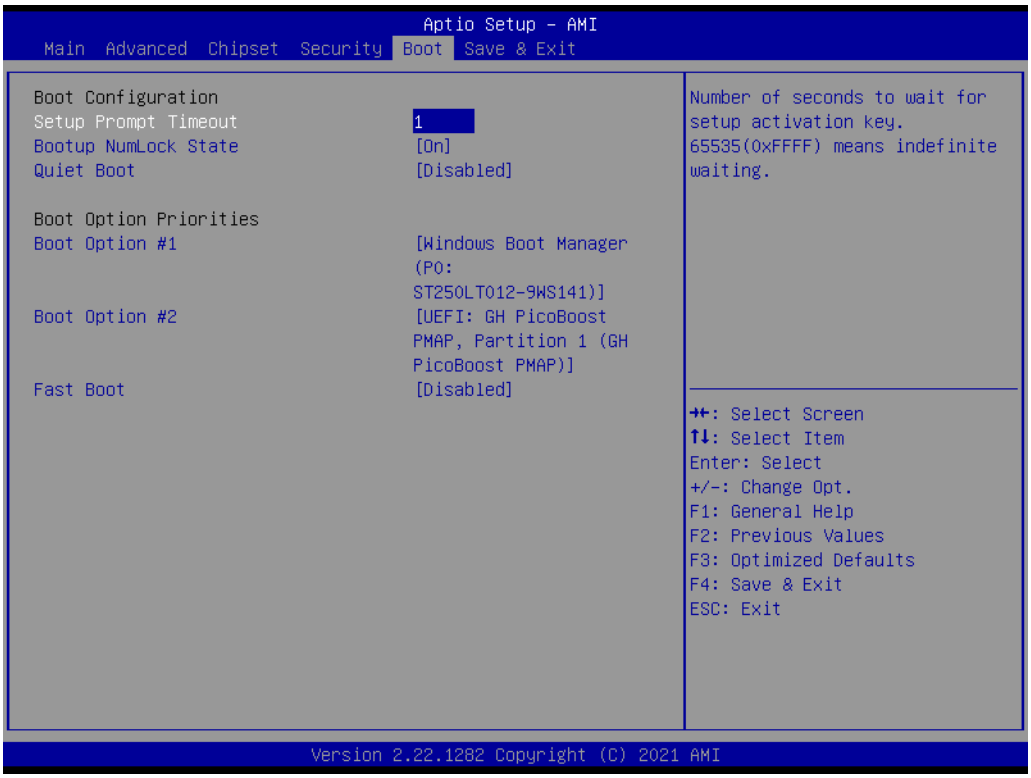


Figure 3.48 Boot Setup

- **Setup Prompt Timeout**
  Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
- **Bootup NumLock State**
  Select the keyboard NumLock state.
- **Quiet Boot**
  Enables or disables Quiet Boot option.
- **Boot Option #1**
  Sets the system boot order.
- **Fast Boot**
  Enable or Disable FastBoot features. Most probes are skipped to reduce time cost during boot.

3.7 Save & Exit

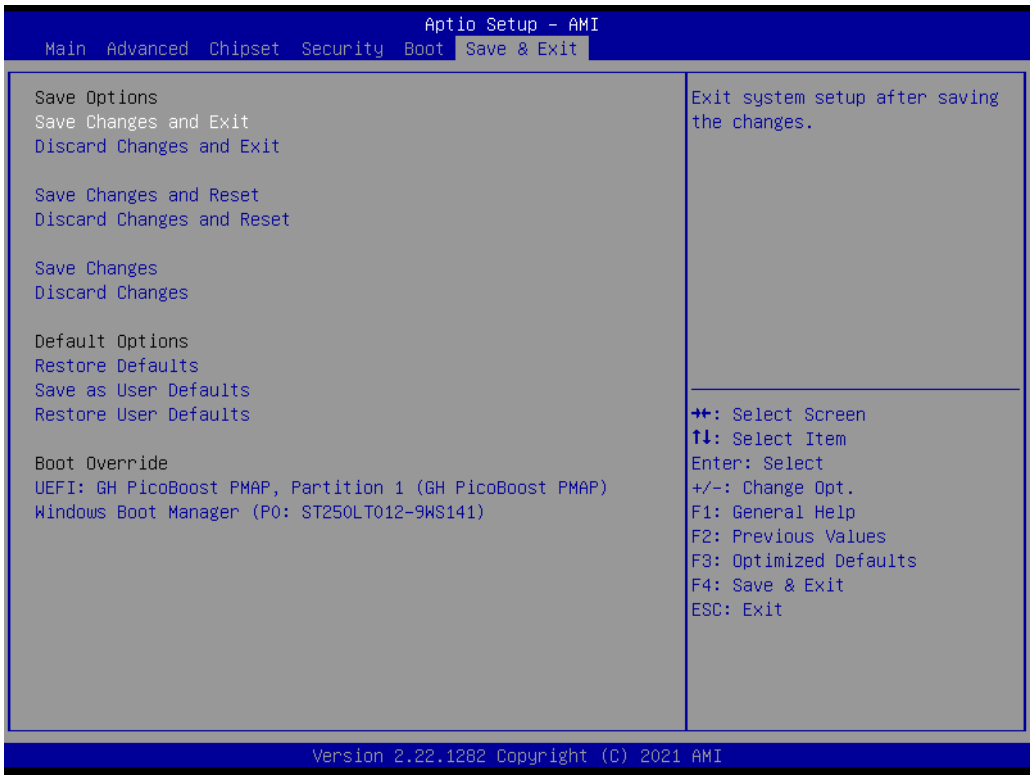


Figure 3.49 Save & Exit

- **Save Changes and Exit**
  Exit system setup after saving the changes.
- **Discard Changes and Exit**
  Exit system setup without saving any changes.
- **Save Changes and Reset**
  Reset the system after saving the changes.
- **Discard Changes and Reset**
  Reset system setup without saving any changes.
- **Save Changes**
  Save Changes done so far to any of the setup options.
- **Discard Changes**
  (005B) Discard Changes done so far to any of the setup options.
- **Restore Defaults**
  Restore/Load Default values for all the setup options.
- **Save as User Defaults**
  Save the changes done so far as User Defaults.
- **Restore User Defaults**
  Restore the User Defaults to all the setup options.
- **Boot Override**

Chapter 4

S/W Introduction & Installation

- S/W Introduction
- Driver Installation
- Advantech iManager

4.1 S/W Introduction

The mission of Advantech Embedded Software Services is to "Enhance quality of life with Advantech platforms and Microsoft Windows embedded technology". We enable Windows Embedded software products on Advantech platforms to more effectively support the embedded computing community. Customers are freed from the hassle of dealing with multiple vendors (Hardware suppliers, System integrators, Embedded OS distributors) for projects. Our goal is to make Windows Embedded Software solutions easily and widely available to the embedded computing community.

4.2 Driver Installation

The Intel Chipset Software Installation (CSI) utility installs the Windows INF files that explain how chipset components are configured to the operating system.

4.2.1 Windows Driver Setup

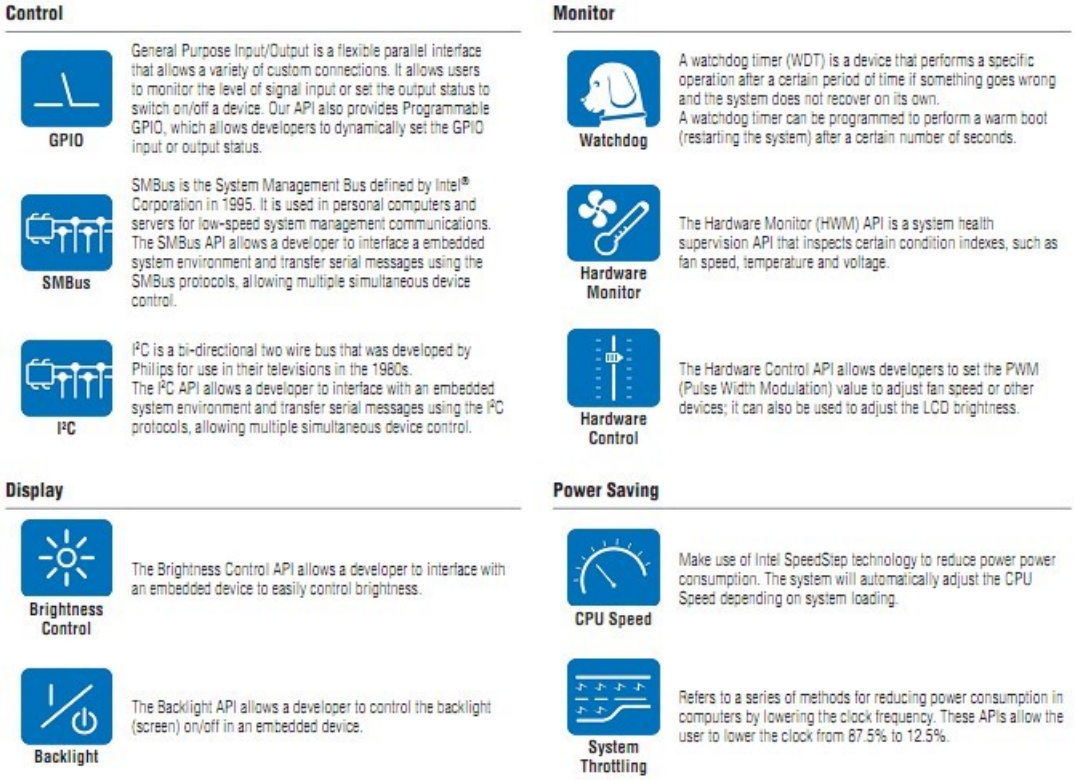
To install the drivers on a windows-based operation system, please connect to internet and browse the website http://support.advantech.com.tw. Next, download the drivers that you want to install. Then follow the Driver Setup instructions to complete the installation.

4.2.2 Other OS

Linux Ubuntu

4.3 Advantech iManager

Advantech's platforms come equipped with iManager, a micro controller that provides embedded features for system integrators. Embedded features have been moved from the OS/BIOS level to the board level, to increase reliability and simplify integration. iManager runs whether the operating system is running or not; it can count the boot times and running hours of the device, monitor device health, and provide an advanced watchdog to handle errors just as they happen. iManager also comes with a secure & encrypted EEPROM for storing important security key or other customer define information. All the embedded functions are configured through API and provide corresponding utilities to demonstrate. These APIs comply with PICMG EAPI (Embedded Application Programmable Interface) specification and unify in the same structures. It makes these embedded features easier to integrate, speed up developing schedule, and provide the customer's software continuity while upgrade hardware. More detail of how to use the APIs and utilities, please refer to Advantech iManager2.0 Software API User Manual.



Appendix A

Pin Assignment

This appendix gives you the information about the hardware pin assignment of the SOM-5883 CPU System on Module.

Sections include:

- SOM-5883 Type 6 Pin Assignment

A.1 SOM-5883 Pin Assignment

This section gives SOM-5883 pin assignment on COM Express connector which compliant with COMR.0 R3.0 Type 6 pin-out definitions. More details about how to use these pins and get design reference, please contact to Advantech for design guide, checklist, reference schematic, and other hardware/software support.

SOM-5883 Row A,B			
A1	GND (FIXED)	B1	GND (FIXED)
A2	GBE0_MDI3-	B2	GBE0_ACT#
A3	GBE0_MDI3+	B3	LPC_FRAME#
A4	GBE0_LINK100#	B4	LPC_AD0
A5	GBE0_LINK1000#	B5	LPC_AD1
A6	GBE0_MDI2-	B6	LPC_AD2
A7	GBE0_MDI2+	B7	LPC_AD3
A8	GBE0_LINK#	B8	N/A
A9	GBE0_MDI1-	B9	N/A
A10	GBE0_MDI1+	B10	LPC_CLK
A11	GND (FIXED)	B11	GND (FIXED)
A12	GBE0_MDI0-	B12	PWRBTN#
A13	GBE0_MDI0+	B13	SMB_CK
A14	N/A	B14	SMB_DAT
A15	SUS_S3#	B15	SMB_ALERT#
A16	SATA0_TX+	B16	SATA1_TX+
A17	SATA0_TX-	B17	SATA1_TX-
A18	SUS_S4#	B18	SUS_STAT#
A19	SATA0_RX+	B19	SATA1_RX+
A20	SATA0_RX-	B20	SATA1_RX-
A21	GND (FIXED)	B21	GND (FIXED)
A22	SATA2_TX+	B22	SATA3_TX+
A23	SATA2_TX-	B23	SATA3_TX-
A24	SUS_S5#	B24	PWR_OK
A25	SATA2_RX+	B25	SATA3_RX+
A26	SATA2_RX-	B26	SATA3_RX-
A27	BATLOW#	B27	WDT
A28	(S)ATA_ACT#	B28	N/A
A29	AC/HDA_SYNC	B29	AC/HDA_SDIN1
A30	AC/HDA_RST#	B30	AC/HDA_SDIN0
A31	GND (FIXED)	B31	GND (FIXED)
A32	AC/HDA_BITCLK	B32	SPKR
A33	AC/HDA_SDOUT	B33	I2C_CK
A34	BIOS_DIS0#	B34	I2C_DAT
A35	THRMTRIP#	B35	THRM#
A36	USB6-	B36	USB7-
A37	USB6+	B37	USB7+
A38	USB_6_7_OC#	B38	USB_4_5_OC#
A39	USB4-	B39	USB5-
A40	USB4+	B40	USB5+

A41	GND (FIXED)	B41	GND (FIXED)
A42	USB2-	B42	USB3-
A43	USB2+	B43	USB3+
A44	USB_2_3_OC#	B44	USB_0_1_OC#
A45	USB0-	B45	USB1-
A46	USB0+	B46	USB1+
A47	VCC_RTC	B47	N/A
A48	RSVD	B48	N/A
A49	N/A	B49	SYS_RESET#
A50	LPC_SERIRQ	B50	CB_RESET#
A51	GND (FIXED)	B51	GND (FIXED)
A52	PCIE_TX5+	B52	PCIE_RX5+
A53	PCIE_TX5-	B53	PCIE_RX5-
A54	GPI0	B54	GPO1
A55	PCIE_TX4+	B55	PCIE_RX4+
A56	PCIE_TX4-	B56	PCIE_RX4-
A57	GND	B57	GPO2
A58	PCIE_TX3+	B58	PCIE_RX3+
A59	PCIE_TX3-	B59	PCIE_RX3-
A60	GND (FIXED)	B60	GND (FIXED)
A61	PCIE_TX2+	B61	PCIE_RX2+
A62	PCIE_TX2-	B62	PCIE_RX2-
A63	GPI1	B63	GPO3
A64	PCIE_TX1+	B64	PCIE_RX1+
A65	PCIE_TX1-	B65	PCIE_RX1-
A66	GND	B66	WAKE0#
A67	GPI2	B67	WAKE1#
A68	PCIE_TX0+	B68	PCIE_RX0+
A69	PCIE_TX0-	B69	PCIE_RX0-
A70	GND (FIXED)	B70	GND (FIXED)
A71	LVDS_A0+	B71	LVDS_B0+
A72	LVDS_A0-	B72	LVDS_B0-
A73	LVDS_A1+	B73	LVDS_B1+
A74	LVDS_A1-	B74	LVDS_B1-
A75	LVDS_A2+	B75	LVDS_B2+
A76	LVDS_A2-	B76	LVDS_B2-
A77	LVDS_VDD_EN	B77	LVDS_B3+
A78	LVDS_A3+	B78	LVDS_B3-
A79	LVDS_A3-	B79	LVDS_BKLT_EN
A80	GND (FIXED)	B80	GND (FIXED)
A81	LVDS_A_CK+	B81	LVDS_B_CK+
A82	LVDS_A_CK-	B82	LVDS_B_CK-
A83	LVDS_I2C_CK	B83	LVDS_BKLT_CTRL
A84	LVDS_I2C_DAT	B84	VCC_5V_SBY
A85	GPI3	B85	VCC_5V_SBY
A86	RSVD(*Note)	B86	VCC_5V_SBY
A87	eDP_HPD	B87	VCC_5V_SBY
A88	PCIE_CLK_REF+	B88	BIOS_DIS1#

A89	PCIE_CLK_REF-	B89	VGA_RED
A90	GND (FIXED)	B90	GND (FIXED)
A91	SPI_POWER	B91	VGA_GRN
A92	SPI_MISO	B92	VGA_BLU
A93	GPO0	B93	VGA_HSYNC
A94	SPI_CLK	B94	VGA_VSYNC
A95	SPI_MOSI	B95	VGA_I2C_CK
A96	TPM_PP	B96	VGA_I2C_DAT
A97	TYPE10#	B97	SPI_CS#
A98	SER0_TX	B98	RSVD
A99	SER0_RX	B99	RSVD
A100	GND (FIXED)	B100	GND (FIXED)
A101	SER1_TX	B101	FAN_PWMOUT
A102	SER1_RX	B102	FAN_TACHIN
A103	LID#	B103	SLEEP#
A104	VCC_12V		
B104	VCC_12V		
A105	VCC_12V		
B105	VCC_12V		
A106	VCC_12V		
B106	VCC_12V		
A107	VCC_12V		
B107	VCC_12V		
A108	VCC_12V		
B108	VCC_12V		
A109	VCC_12V		
B109	VCC_12V		
A110	GND (FIXED)	B110	GND (FIXED)
	SOM-5883 Row C,D		
C1	GND (FIXED)	D1	GND (FIXED)
C2	GND	D2	GND
C3	USB_SSRX0-	D3	USB_SSTX0-
C4	USB_SSRX0+	D4	USB_SSTX0+
C5	GND	D5	GND
C6	USB_SSRX1-	D6	USB_SSTX1-
C7	USB_SSRX1+	D7	USB_SSTX1+
C8	GND	D8	GND
C9	USB_SSRX2-	D9	USB_SSTX2-
C10	USB_SSRX2+	D10	USB_SSTX2+
C11	GND (FIXED)	D11	GND (FIXED)
C12	USB_SSRX3-	D12	USB_SSTX3-
C13	USB_SSRX3+	D13	USB_SSTX3+
C14	GND	D14	GND
C15	N/A(*Note)	D15	DDI1_CTRLCLK_AUX+
C16	N/A(*Note)	D16	DDI1_CTRLDATA_AUX-
C17	RSVD(*Note)	D17	RSVD (*Note)
C18	RSVD(*Note)	D18	RSVD (*Note)
C19	PCIE_RX6+	D19	PCIE_TX6+

C20	PCIE_RX6-	D20	PCIE_TX6-
C21	GND (FIXED)	D21	GND (FIXED)
C22	PCIE_RX7+	D22	PCIE_TX7+
C23	PCIE_RX7-	D23	PCIE_TX7-
C24	DDI1_HPD	D24	RSVD
C25	N/A(*Note)	D25	RSVD
C26	N/A(*Note)	D26	DDI1_PAIR0+
C27	RSVD (*Note)	D27	DDI1_PAIR0-
C28	RSVD (*Note)	D28	RSVD
C29	N/A(*Note)	D29	DDI1_PAIR1+
C30	N/A(*Note)	D30	DDI1_PAIR1-
C31	GND (FIXED)	D31	GND (FIXED)
C32	DDI2_CTRLCLK_AUX+	D32	DDI1_PAIR2+
C33	DDI2_CTRLDATA_AUX-	D33	DDI1_PAIR2-
C34	DDI2_DDC_AUX_SEL	D34	DDI1_DDC_AUX_SEL
C35	RSVD	D35	RSVD
C36	DDI3_CTRLCLK_AUX+	D36	DDI1_PAIR3+
C37	DDI3_CTRLDATA_AUX-	D37	DDI1_PAIR3-
C38	DDI3_DDC_AUX_SEL	D38	RSVD
C39	DDI3_PAIR0+	D39	DDI2_PAIR0+
C40	DDI3_PAIR0-	D40	DDI2_PAIR0-
C41	GND (FIXED)	D41	GND (FIXED)
C42	DDI3_PAIR1+	D42	DDI2_PAIR1+
C43	DDI3_PAIR1-	D43	DDI2_PAIR1-
C44	DDI3_HPD	D44	DDI2_HPD
C45	RSVD	D45	RSVD
C46	DDI3_PAIR2+	D46	DDI2_PAIR2+
C47	DDI3_PAIR2-	D47	DDI2_PAIR2-
C48	RSVD	D48	RSVD
C49	DDI3_PAIR3+	D49	DDI2_PAIR3+
C50	DDI3_PAIR3-	D50	DDI2_PAIR3-
C51	GND (FIXED)	D51	GND (FIXED)
C52	PEG_RX0+	D52	PEG_TX0+
C53	PEG_RX0-	D53	PEG_TX0-
C54	N/A	D54	PEG_LANE_RV#
C55	PEG_RX1+	D55	PEG_TX1+
C56	PEG_RX1-	D56	PEG_TX1-
C57	N/A	D57	GND (FIXED)
C58	PEG_RX2+	D58	PEG_TX2+
C59	PEG_RX2-	D59	PEG_TX2-
C60	GND (FIXED)	D60	GND (FIXED)
C61	PEG_RX3+	D61	PEG_TX3+
C62	PEG_RX3-	D62	PEG_TX3-
C63	RSVD	D63	RSVD (*Note)
C64	RSVD	D64	RSVD (*Note)
C65	PEG_RX4+	D65	PEG_TX4+
C66	PEG_RX4-	D66	PEG_TX4-
C67	RAPID_SHUTDOWN	D67	GND

C68	PEG_RX5+	D68	PEG_TX5+
C69	PEG_RX5-	D69	PEG_TX5-
C70	GND (FIXED)	D70	GND (FIXED)
C71	PEG_RX6+	D71	PEG_TX6+
C72	PEG_RX6-	D72	PEG_TX6-
C73	GND	D73	GND
C74	PEG_RX7+	D74	PEG_TX7+
C75	PEG_RX7-	D75	PEG_TX7-
C76	GND	D76	GND
C77	RSVD	D77	RSVD
C78	PEG_RX8+	D78	PEG_TX8+
C79	PEG_RX8-	D79	PEG_TX8-
C80	GND (FIXED)	D80	GND (FIXED)
C81	PEG_RX9+	D81	PEG_TX9+
C82	PEG_RX9-	D82	PEG_TX9-
C83	RSVD	D83	RSVD
C84	GND	D84	GND
C85	PEG_RX10+	D85	PEG_TX10+
C86	PEG_RX10-	D86	PEG_TX10-
C87	GND	D87	GND
C88	PEG_RX11+	D88	PEG_TX11+
C89	PEG_RX11-	D89	PEG_TX11-
C90	GND (FIXED)	D90	GND (FIXED)
C91	PEG_RX12+	D91	PEG_TX12+
C92	PEG_RX12-	D92	PEG_TX12-
C93	GND	D93	GND
C94	PEG_RX13+	D94	PEG_TX13+
C95	PEG_RX13-	D95	PEG_TX13-
C96	GND	D96	GND
C97	RSVD	D97	RSVD
C98	PEG_RX14+	D98	PEG_TX14+
C99	PEG_RX14-	D99	PEG_TX14-
C100	GND (FIXED)	D100	GND (FIXED)
C101	PEG_RX15+	D101	PEG_TX15+
C102	PEG_RX15-	D102	PEG_TX15-
C103	GND	D103	GND
C104	VCC_12V	D104	VCC_12V
C105	VCC_12V	D105	VCC_12V
C106	VCC_12V	D106	VCC_12V
C107	VCC_12V	D107	VCC_12V
C108	VCC_12V	D108	VCC_12V
C109	VCC_12V	D109	VCC_12V
C110	GND (FIXED)	D110	GND (FIXED)

*Note:

1. A86 could be an optional pin reserved for CB_I2C_ALERT#. Please contact FAE for details.
2. C15 could be an optional pin reserved for SML0_CLK. Please contact FAE for details.

3. C16 could be an optional pin reserved for SML0_DATA. Please contact FAE for details.
4. C17 could be an optional pin reserved for SML0ALERT#. Please contact FAE for details.
5. C18 could be an optional pin reserved for PMC_ALERT#. Please contact FAE for details.
6. C25 could be an optional pin reserved for AUXDDI1_TBT_AUX+. Please contact FAE for details.
7. C26 could be an optional pin reserved for AUXDDI1_TBT_AUX-. Please contact FAE for details.
8. C27 could be an optional pin reserved for SML1_CLK. Please contact FAE for details.
9. C28 could be an optional pin reserved for SML1_DATA. Please contact FAE for details.
10. C29 could be an optional pin reserved for AUXDDI2_TBT_AUX+. Please contact FAE for details.
11. C30 could be an optional pin reserved for AUXDDI2_TBT_AUX-. Please contact FAE for details.
12. D17 could be an optional pin reserved for DDI1_CTRLCLK. Please contact FAE for details.
13. D18 could be an optional pin reserved for DDI1_CTRLDATA. Please contact FAE for details.
14. D63 could be an optional pin reserved for DDI2_CTRLCLK. Please contact FAE for details.
15. D64 could be an optional pin reserved for DDI2_CTRLDATA. Please contact FAE for details.

Appendix B

Watchdog Timer

This appendix details information about the watchdog timer programming on the SOM-5883 CPU System on Module.

Sections include:

- Watchdog Timer Programming

B.1 Programming the Watchdog Timer

Trigger Event	Note
IRQ	(BIOS setting default disable)**
NMI	N/A
SCI	Support
Power Off	Support
H/W Restart	Support
WDT Pin Activate	Support

** WDT new driver support automatically selects available IRQ number from BIOS, and then sets it to EC. Only Win10 supports this.

In other OS, it will still use IRQ number from BIOS setting as usual.

For details, please refer to iManager & Software API User Manual.

Appendix C

Programming GPIO

This Appendix illustrates the General Purpose Input and Output pin settings.

Sections include:

- **GPIO Register**

C.1 GPIO Register

GPIO Byte Mapping	H/W Pin Name
BIT0	GPIO0
BIT1	GPIO1
BIT2	GPIO2
BIT3	GPIO3
BIT4	GPI0
BIT5	GPI1
BIT6	GPI2
BIT7	GPI4

For details, please refer to iManager & Software API User Manual.

Appendix D

System Assignments

This appendix gives you the information about the system resource allocation on the SOM-5883 CPU System on Module.

Sections include:

- System I/O Ports
- DMA Channel Assignments
- Interrupt Assignments
- 1st MB Memory Map

D.1 System I/O Ports

Table D.1: System I/O Ports

Addr.Range(Hex)	Device
0x00000299-0x0000029A	Motherboard resources
0x000002C0-0x000002DF	Motherboard resources
0x000002A0-0x000002BF	Motherboard resources
0x000002A0-0x000002BF	Motherboard resources
0x00000290-0x0000029F	Motherboard resources
0x0000029E-0x000002AD	Motherboard resources
0x00000060-0x0000006F	Motherboard resources
0x00000200-0x0000027F	Motherboard resources
0x00000300-0x0000037F	Motherboard resources
0x00000280-0x0000028F	Motherboard resources
0x00000280-0x0000028F	Motherboard resources
0x000002F0-0x000002F7	Motherboard resources
0x0000002E-0x0000002F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000B2-0x000000B3	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x0000164E-0x0000164F	Motherboard resources
0x00000062-0x00000062	Microsoft ACPI-Compliant Embedded Controller
0x00000066-0x00000066	Microsoft ACPI-Compliant Embedded Controller
0x0000EFA0-0x0000EFBF	Intel(R) SMBus - 43A3
0x000003F8-0x000003FF	Communications Port (COM1)
0x000002F8-0x000002FF	Communications Port (COM2)
0x00001800-0x000018FE	Motherboard resources
0x00003090-0x00003097	Standard SATA AHCI Controller
0x00003080-0x00003083	Standard SATA AHCI Controller
0x00003060-0x0000307F	Standard SATA AHCI Controller
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000D00-0x0000FFFF	PCI Express Root Complex
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller

Table D.1: System I/O Ports	
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x00002000-0x000020FE	Motherboard resources
0x000000F0-0x000000F0	Numeric data processor
0x0000FFF8-0x0000FFFF	Intel(R) Active Management Technology - SOL (COM3)
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer
0x00003000-0x0000303F	Intel(R) UHD Graphics
0x00001854-0x00001857	Motherboard resources

D.2 Interrupt Assignments

Table D.2: Interrupt Assignments	
Interrupt#	**Interrupt Source**
IRQ0	System timer
IRQ 27	Intel(R) Serial IO I2C Host Controller - 43E8
IRQ 4294967294	Intel(R) PCI Express Root Port #5 - 43BC
IRQ 4	Communications Port (COM1)
IRQ 3	Communications Port (COM2)
IRQ 14	Intel(R) Serial IO GPIO Host Controller - INT34C6
IRQ 6	Motherboard resources
IRQ 4294967293	Standard SATA AHCI Controller
IRQ54-68	Microsoft ACPI-Compliant System
IRQ69	Trusted Platform Module 2.0
IRQ70-511	Microsoft ACPI-Compliant System
IRQ13	Numeric data processor
IRQ 19	Intel(R) Active Management Technology - SOL (COM3)
IRQ 16	High Definition Audio Controller
IRQ 4294967281	Intel(R) Ethernet Controller (3) I225-LM
IRQ 4294967291	Intel(R) UHD Graphics
IRQ 4294967280	Intel(R) Management Engine Interface #1

D.3 1st MB Memory Map

Table D.3: 1st MB Memory Map

Addr. Range (Hex)	Device
0xFFEFA000-0xFFEFAFFF	Intel(R) Serial IO I2C Host Controller - 43E8
0xFEDC0000-0xFEDC7FFF	Motherboard resources
0xFEDA0000-0xFEDA0FFF	Motherboard resources
0xFEDA1000-0xFEDA1FFF	Motherboard resources
0xC0000000-0xCFFFFFFF	Motherboard resources
0xFED20000-0xFED7FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFEE00000-0xFEEFFFFF	Motherboard resources
0x50400000-0x506FFFFF	Intel(R) PCI Express Root Port #5 - 43BC
0x50400000-0x506FFFFF	PCI Express Root Complex
0x1128000-0x11280FF	Intel(R) SMBus - 43A3
0xFED00000-0xFED003FF	High precision event timer
0xFE000000-0xFE01FFFF	Motherboard resources
0xFE04C000-0xFE04FFFF	Motherboard resources
0xFE050000-0xFE0AFFFF	Motherboard resources
0xFE0D0000-0xFE0FFFFF	Motherboard resources
0xFE200000-0xFE7FFFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Motherboard resources
0xFD000000-0xFD68FFFF	Motherboard resources
0xFD6C0000-0xFD6CFFFF	Motherboard resources
0xFD6F0000-0xFDFFFFFF	Motherboard resources
0xFD6E0000-0xFD6EFFFF	Intel(R) Serial IO GPIO Host Controller - INT34C6
0xFD6D0000-0xFD6DFFFF	Intel(R) Serial IO GPIO Host Controller - INT34C6
0xFD6B0000-0xFD6BFFFF	Intel(R) Serial IO GPIO Host Controller - INT34C6
0xFD6A0000-0xFD6AFFFF	Intel(R) Serial IO GPIO Host Controller - INT34C6
0xFD690000-0xFD69FFFF	Intel(R) Serial IO GPIO Host Controller - INT34C6
0x50700000-0x50701FFF	Standard SATA AHCI Controller
0x50703000-0x507030FF	Standard SATA AHCI Controller
0x50702000-0x507027FF	Standard SATA AHCI Controller
0xA0000-0xBFFFF	PCI Express Root Complex
0xE0000-0xE3FFF	PCI Express Root Complex
0xE4000-0xE7FFF	PCI Express Root Complex
0xE8000-0xEBFFF	PCI Express Root Complex
0xEC000-0xEFFFF	PCI Express Root Complex
0xF0000-0xFFFFF	PCI Express Root Complex
0x1100000-0x110FFFF	Intel(R) USB 3.20 eXtensible Host Controller - 1.20 (Microsoft)
0xFED40000-0xFED44FFF	Trusted Platform Module 2.0
0xFE010000-0xFE010FFF	Intel(R) SPI (flash) Controller - 43A4
0xBFFFF000-0xBFFFFFFF	Intel(R) Active Management Technology - SOL (COM3)
0xFFEFC000-0xFFEFFFFF	High Definition Audio Controller
0xFFF00000-0xFFFFFFFF	High Definition Audio Controller

Table D.3: 1st MB Memory Map	
0x1110000-0x111FFFF	Intel(R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
0x50500000-0x505FFFFF	Intel(R) Ethernet Controller (3) I225-LM
0x50600000-0x50603FFF	Intel(R) Ethernet Controller (3) I225-LM
0x0000-0xFFFFFF	Intel(R) UHD Graphics
0x0000-0xFFFFFFF	Intel(R) UHD Graphics
0xFFEFB000-0xFFEFBFFF	Intel(R) Management Engine Interface #1
0xFFEFA000-0xFFEFAFFF	Intel(R) Serial IO I2C Host Controller - 43E8

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